



Product Specification

F150PET-Z05

- ☐ Draft Specifications
☐ Preliminary Specifications
☒ Final Specifications

Title	15.0-inches Touch Display Product Specifications
Model Name	F150PET-Z05
Version	2

Customer

Approved by

Date

Notice: This Specification is subject to change without notice.

Approved By	Prepared By
ZOE NIEN 2026-05-20	ALLEN FANG 2026-05-20



REVISION HISTORY

Version	ECN No.	Description of Changes	Date	Prepared
0		Preliminary specification first edition	2023-05-16	Allen Fang
1		Modified Label	2025-12-12	Allen Fang
2		Final specification	2026-05-20	Allen Fang



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1. GENERAL DESCRIPTION

1.1.INTRODUCTION

F150PET-Z05 is a 15.0" color active matrix TFT LCD module G150XNE-L01 with projected capacitive multi touch B156PBU-E03 air bonding and touch driving board B000PDU-E03.

The display format is intended to support the XGA mode (1024 (H) x 768 (V)) screen and 16.7M/262K colors (20 pins LVDS interface) with LED backlight driving circuit.

1.2.FEATURES

- XGA (1024 x 768) resolution
- DE (Data Enable) only mode
- LVDS interface with 1 pixel / clock
- PSWG (Panel Standardization Working Group)
- Industrial application
- RoHS product

1.3.APPLICATION

- TFT LCD monitor
- Industrial application
- Amusement
- Vehicle

2. GENERAL SPECIFICATIONS

Parameter	Specification	Unit	Remarks
Active Area Size	15.0	inch	
Display Area	304.1 (H) x 228.1 (V)	mm	
Outline Dimension	326.50 (H) x 253.500 (V) x 12.17 (D)	mm	D:PCAP+LCM
Weight	1450	g	Typ.
Driver Element	a-Si TFT active matrix	-	
Display Colors	16,7M/262K	Colors	
Electrical Interface	LVDS	-	
Number of Pixels	1024 (H) x 768 (V)	pixels	
Pixel Pitch	0.297 (H) x 0.297 (W)	mm	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normal Black	-	
Touch Technology	Projected Capacitive Multi-Touch Panel	-	
Touch Method	Finger or exclusive pen	-	
Numbers of Touch	10	Points	
Touch Driver	B000PDU-E03:USB	-	
Touch Sensor Glass Type	Strengthening Glass	-	
Touch Controller IC	EETI, EXC80W46	-	
Touch Driver Interface	USB 1.1 Full Speed / I ² C	-	
Support OS	Windows (Embedded/CE/XP/2000/Vista/7/8/10) / Linux / Android / MAC / QNX	-	

3. ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

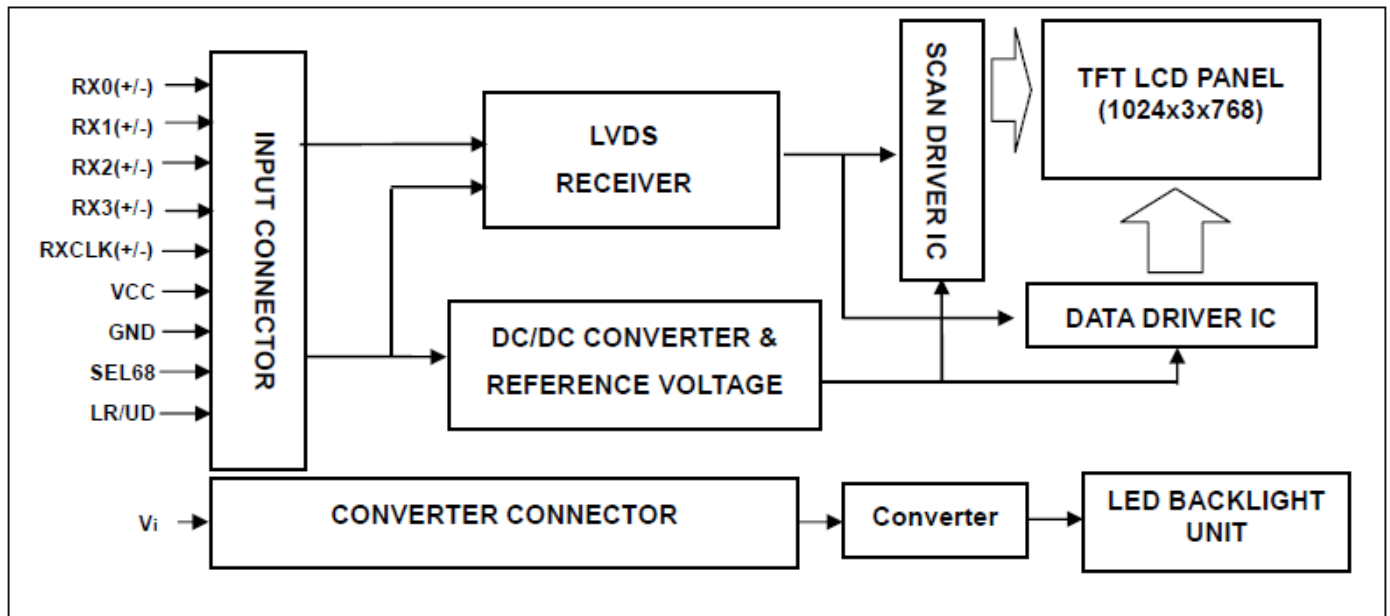
Parameter	Symbol	Min.	Max.	Unit
Logic/LCD Drive Voltage	V_{DD}	-0.3	+4.0	V
LCD Input Signal Voltage	V_{signal}	-0.3	+4.0	V
LED Input Voltage	LED_V_{DD}	-0.3	+18	V
LED Control Signal Voltage	LED_EN, PWM	-0.3	+5.5	V
Operating Temperature	T_{op}	-20	+70	°C
Operating Humidity	H_{op}	10	90	%RH
Storage Temperature	T_{st}	-30	+80	°C
Storage Humidity	H_{st}	10	90	%RH
Touch DC Supply Voltage	I ² C	-0.3	3.6	V
Touch DC Supply Voltage	USB	-0.3	5.5	V

Note: Maximum Wet-Bulb temperature should be 39°C and no condensation.

4. ELECTRICAL CHARACTERISTICS

4.1.LCD MODULE

4.1.1. LCD BLOCK DIAGRAM



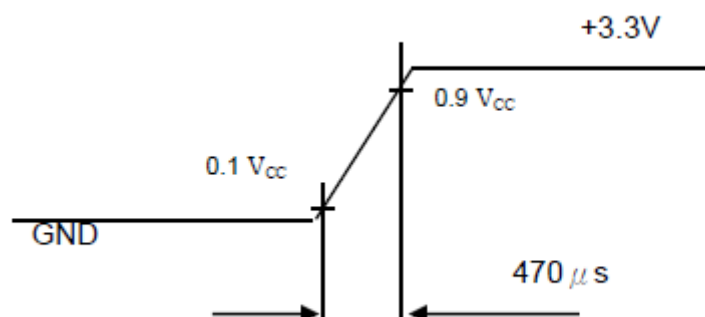
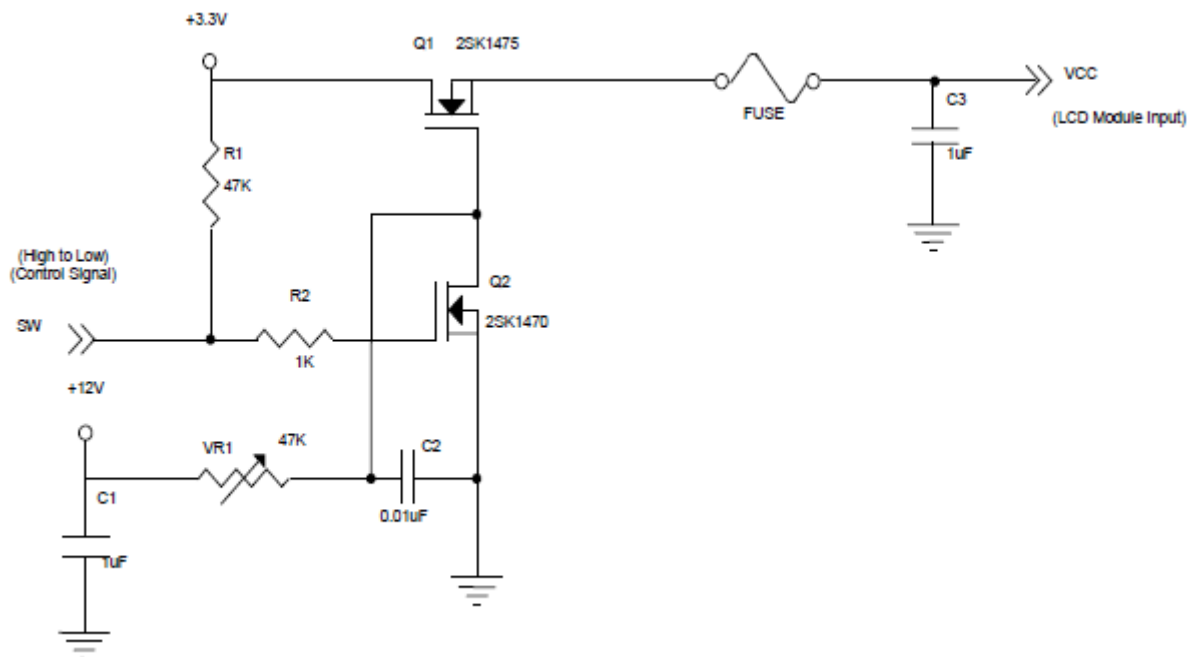
4.1.2. POWER SPECIFICATION

TFT LCD MODULE

Symbol	Parameter		Min.	Typ.	Max.	Units	Remark
V_{CC}	Power Supply Voltage		3.0	3.3	3.6	V	
V_{RP}	Ripple Voltage		-	-	100	mVp-p	
I_{RUSH}	Rush Current		-	-	2	A	(2)
I_{CC}	Power Supply Current	White	-	550	660	mA	(3)a
		Black	-	440	530	mA	(3)b
V_{id}	LVDS differential input voltage		200	-	600	mV	
V_{ic}	LVDS common input voltage		1.0	1.2	1.4	V	
V_{IH}	Differential Input Voltage for LVDS Receiver Threshold	"H" Level	-	-	100	mV	-
V_{IL}		"L" Level	-100	-	-	mV	-
R_T	Terminating Resistor		-	100	-	Ohm	-

Note (1): The module should be always operated within above ranges.

Note (2): Measurement Conditions:



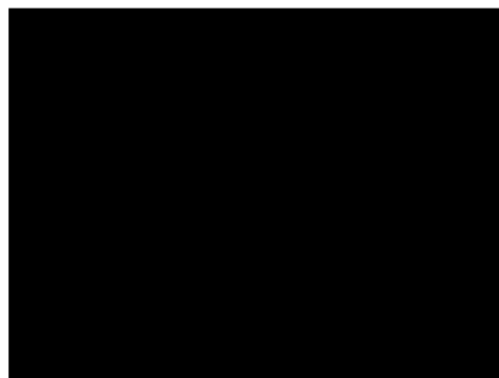
Note (3): The specified power supply current is under the conditions at $V_{DD} = 3.3V$, $T_a = 25 \pm 2^\circ C$, DC

a. White Pattern



Active Area

b. Black Pattern

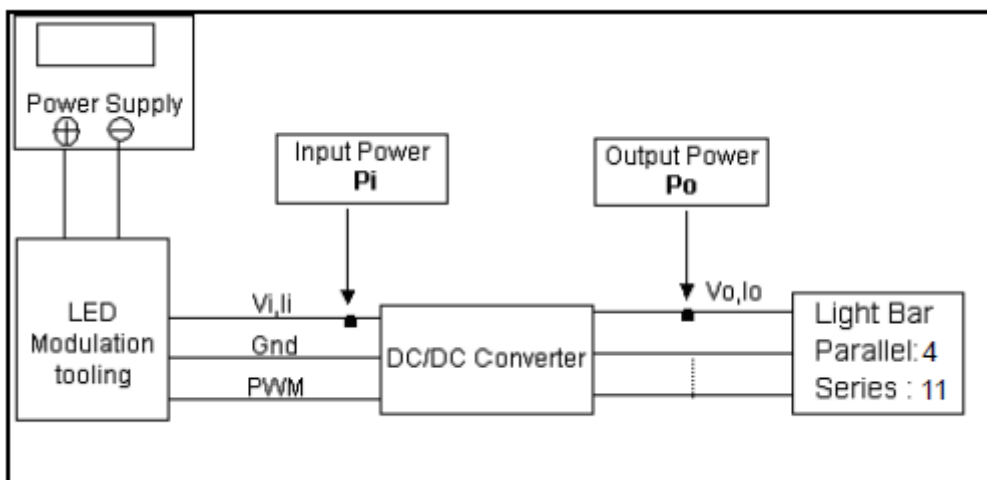


Active Area

4.1.3. LCD BACKLIGHT UNIT

Parameter		Symbol	Min.	Typ.	Max.	Units	Remark
Converter Power Supply Voltage		V_i	10.8	12	13.2	V	
Converter Power Supply Current		I_i	0.5	0.65	0.8	A	@ $V_i=12V$ (Duty 100%)
Backlight Power Consumption		P_{BL}	-	7.8	9.6	W	@ $V_i=12V$ (Duty 100%)
EN Control Level	Backlight ON	-	2.0	3.3	5.0	V	
	Backlight OFF		0	-	0.8	V	
PWM Dimming Control Level	PWM High Level	-	2.0	3.3	5.0	V	
	PWM Low Level		0	-	0.15	V	
PWM Dimming Control Duty Ratio		-	1	-	100	%	@200Hz
PWM Dimming Control frequency		f_{PWM}	190	200	20K	Hz	(2)
LED Life Time		L_L	50,000	70,000	-	Hour	(3)

Note (1): LED current is measured by utilizing a high frequency current meter as shown below:



Note (2): At 190 ~ 1 KHz PWM control frequency, duty ratio range is restricted from 5% to 100%.

1 K ~ 20 KHz PWM control frequency, duty ratio range is restricted from 20% to 100%.

If PWM control frequency is applied in the range from 1 KHz to 20 KHz, The “non-linear” phenomenon on the Backlight Unit may be found. So there is a **suggestion** that PWM control frequency should be **less than 1 KHz**.

Note (3): The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at $T_a=25\pm2^\circ\text{C}$ and Duty 100% until the brightness becomes $\leq 50\%$ of its original value. Operating LED under high temperature environment will reduce life time and lead to color shift.

4.1.4. LCD INPUT TERMINAL PIN ASSIGNMENT

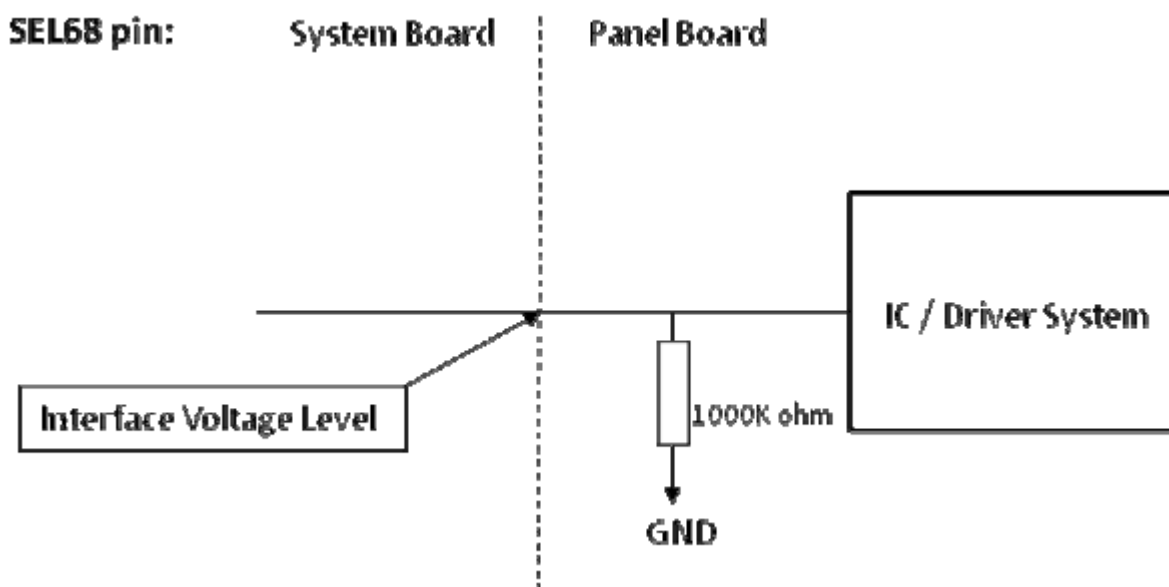
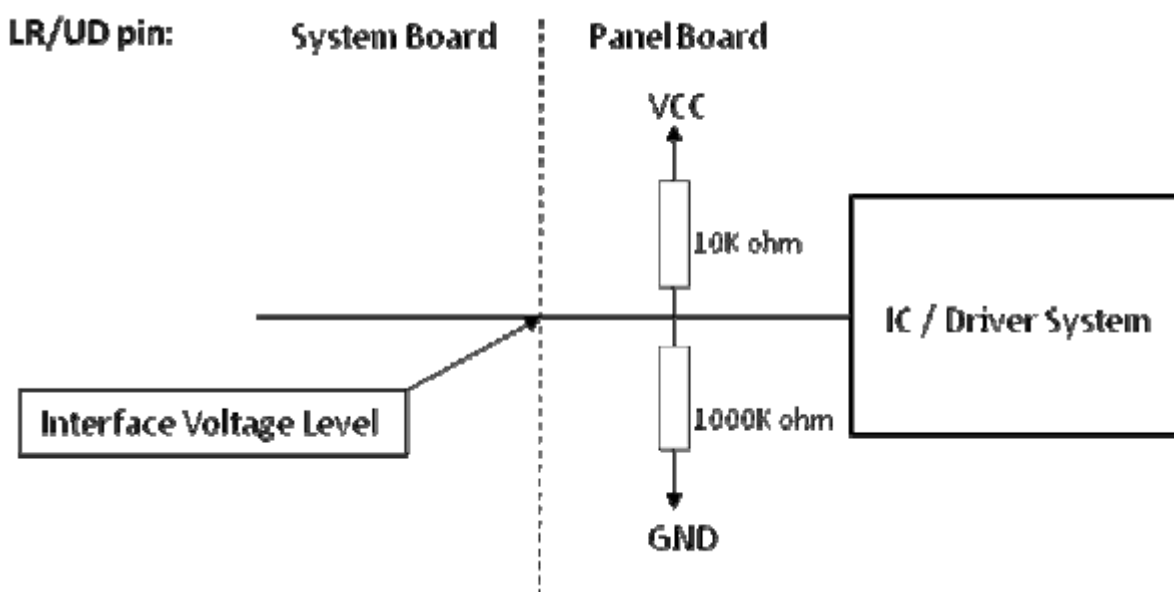
Pin	Symbol	Description	Note
1	VCC	Power Supply +3.3V(typical)	
2	VCC	Power Supply +3.3V(typical)	
3	NC	No Connection (Reserve for INX test)	
4	LR/UD	Reverse Scan Control H or NC = Normal mode. L = Horizontal / Vertical Reverse Scan.	
5	RX0-	Negative LVDS differential data input	
6	RX0+	Positive LVDS differential data input	
7	GND	Ground	
8	RX1-	Negative LVDS differential clock input	
9	RX1+	Positive LVDS differential clock input	
10	NC	No Connection (Reserve for INX test)	
11	RX2-	Negative LVDS differential data input	
12	RX2+	Positive LVDS differential data input	
13	GND	Ground	
14	RXCLK-	Negative LVDS differential clock input	
15	RXCLK+	Positive LVDS differential clock input	
16	GND	Ground	
17	RX3-	Negative LVDS differential data input	
18	RX3+	Positive LVDS differential data input	
19	NC	No Connection (Reserve for INX test)	
20	SEL68	LVDS 6/8 bit select function control, High→6bit Input Mode Low or NC→8bit Input Mode	(1)

Note (1) "Low" stands for 0V. "High" stands for 3.3V. "NC" stands for "No Connection"

Note (2) Pin3, Pin10, Pin19 input signals should be set to no connection or ground, this module would operate normally.

LVDS Connector

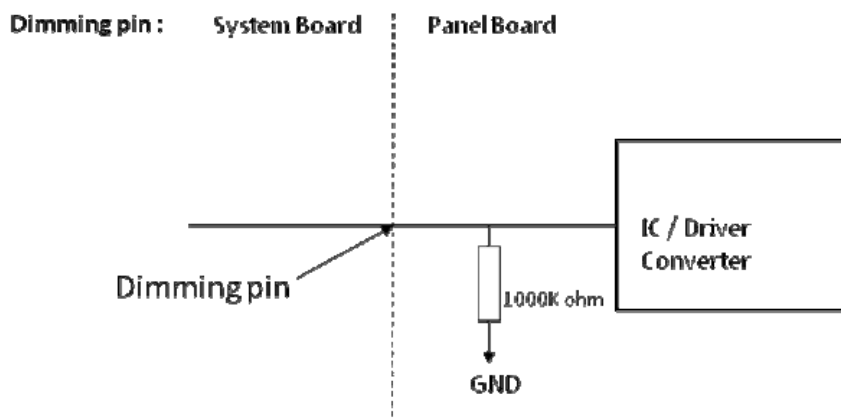
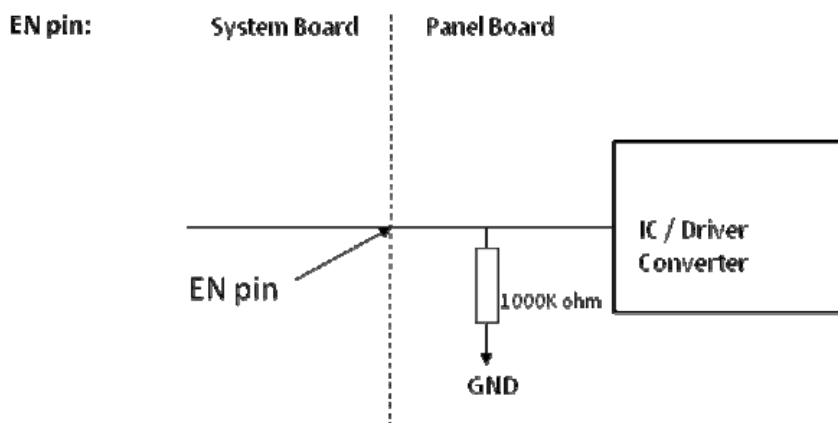
TFT-LCD Connector	Manufacturer	STM or equivalent
	Part Number	MSB240420HDA or equivalent
Mating Connector	Manufacturer	Hirose or equivalent
	Part Number	DF14-20S-1.25C or equivalent



4.1.5. BACKLIGHT UNIT (CONVERTER CONNECTOR PIN)

Pin No.	Symbol	Description
Pin1	V_i	Converter Input voltage 12V
Pin2	V_{GND}	Converter Ground
Pin3	V_{LED_EN}	3.3V-ON, 0V-OFF
Pin4	Dimming	PWM Dimming (Hi:3.3V _{DC} , Lo:0V _{DC})
Pin5	NC	No Connect

Connector Name / Description	Lamp Connector
Manufacturer	Cvilux or equivalent
Connector Model Number	CI4205M2HRP-NH or equivalent
Mating Model Number	MOLEX 51146-0500 or equivalent



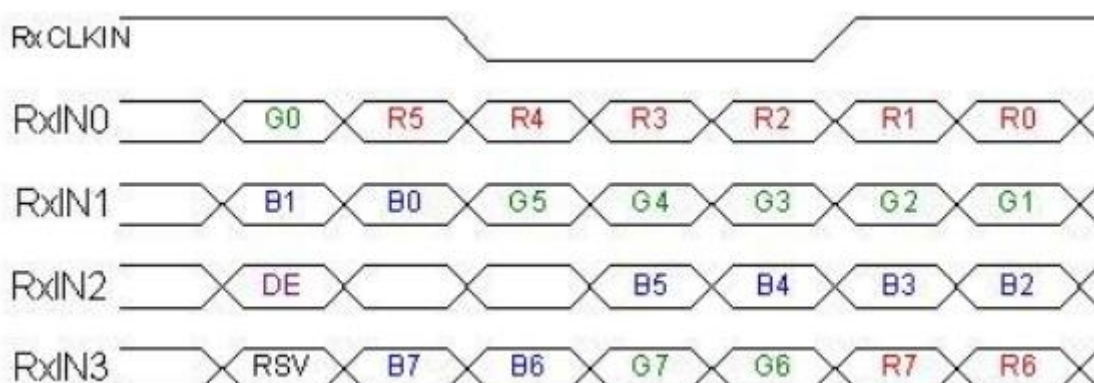
4.1.6. COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

0: Low Level voltage, 1: High Level Voltage

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Red (0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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	Red (253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Green (0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
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	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green (253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
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	Blue (253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

4.1.7. THE LCD INPUT DATA FORMAT



Note 1: Please follow PSWG.

Note 2: R/G/B data 7: MSB, R/G/B data 0: LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0	Red-pixel Data 8Bits LVDS input MSB: R7 ; LSB: R0
G7 G6 G5 G4 G3 G2 G1 G0	Green Data 7 Green Data 6 Green Data 5 Green Data 4 Green Data 3 Green Data 2 Green Data 1 Green Data 0	Green-pixel Data 8Bits LVDS input MSB: G7 ; LSB: G0
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0	Blue-pixel Data 8Bits LVDS input MSB: B7 ; LSB: B0
RxCLKIN	LVDS Data Clock	
DE	Data Enable Signal	When the signal is high, the pixel data shall be valid to be displayed.

Note: Output signals from any system shall be low state or High-impedance state when VDD is off.

4.1.8. LCD TIMING CHARACTERISTICS

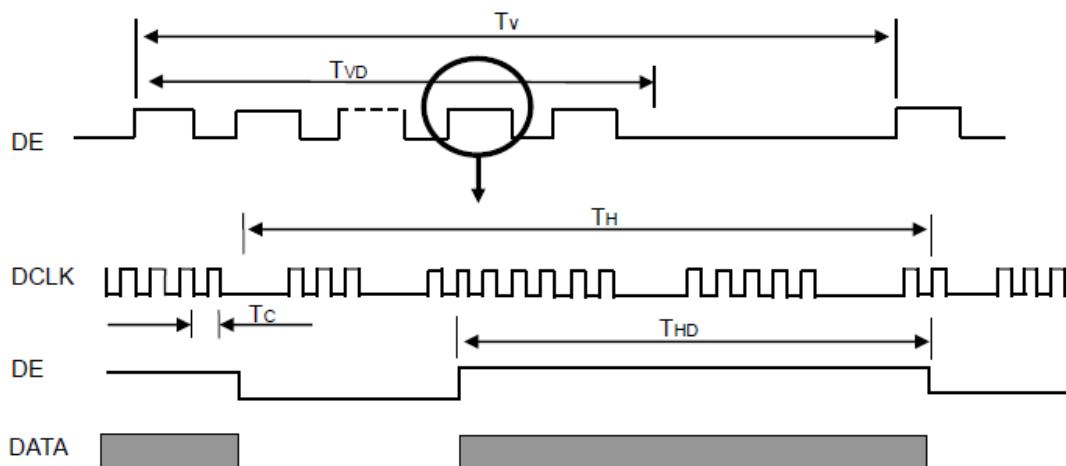
The input signal timing specifications are shown as the following table and timing diagram.

Parameter		Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	F_r	53.35	65	80	MHz	
	Period	T_C	12.5	15.38	18.75	ns	
	Input cycle to cycle jitter	T_{rcl}	---	---	200	ns	(a)
	Input Clock to data skew	TLVCCS	$-0.02 \cdot T_C$	-	$0.02 \cdot T_C$	ps	(b)
	Spread spectrum modulation range	F_{clkin_mod}	-	-	$1.02 \cdot F_C$	MHz	(c)
	Spread spectrum modulation frequency	F_{SSM}	-	-	200	KHz	
Vertical Display Term	Frame Rate	F_r	55	60	70	Hz	$T_v = T_{vd} + T_{vb}$
	Total	T_v	780	806	840	T_h	-
	Active	T_{vd}	768	768	768	T_h	-
	Blanking	T_{vb}	$T_v - T_{vd}$	38	$T_v - T_{vd}$	T_h	-
Horizontal Display Term	Total	T_h	1240	1344	1360	T_C	$T_h = T_{hd} + T_{hb}$
	Active	T_{hd}	1024	1024	1024	T_C	-
	Blanking	T_{HB}	$T_h - T_{hd}$	320	$T_h - T_{hd}$	T_C	-

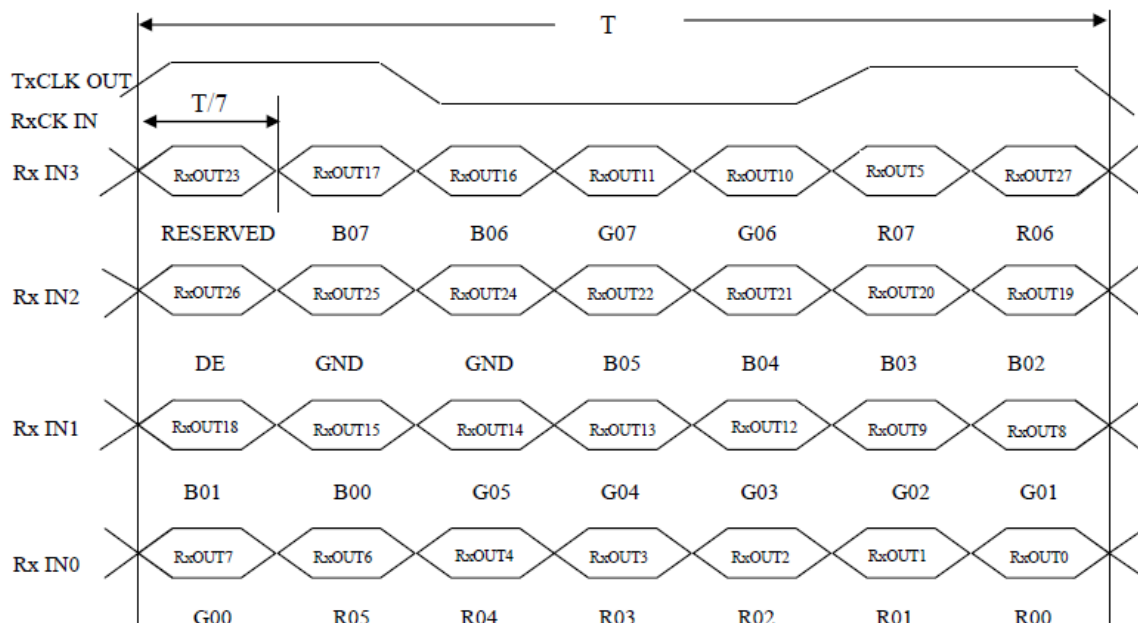
Note 1: Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise this module would operate abnormal.

Note 2: The T_v ($T_{vd} + T_{vb}$) must be integer, otherwise, the module would operate abnormally.

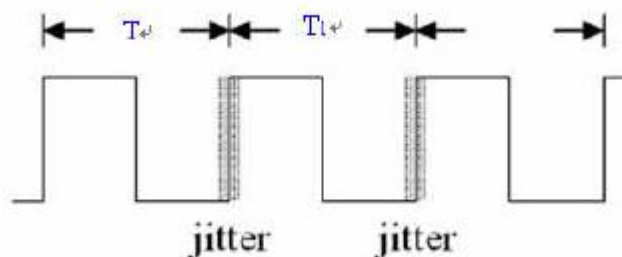
INPUT SIGNAL TIMING DIAGRAM



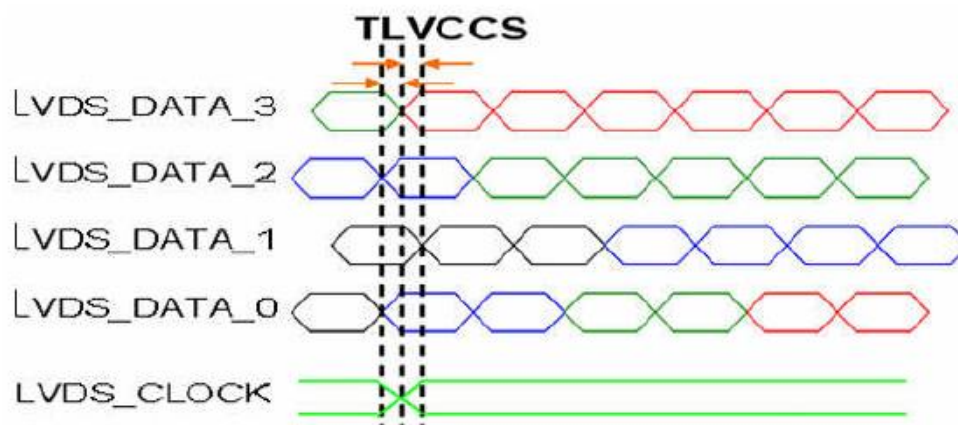
TIMING DIAGRAM of LVDS



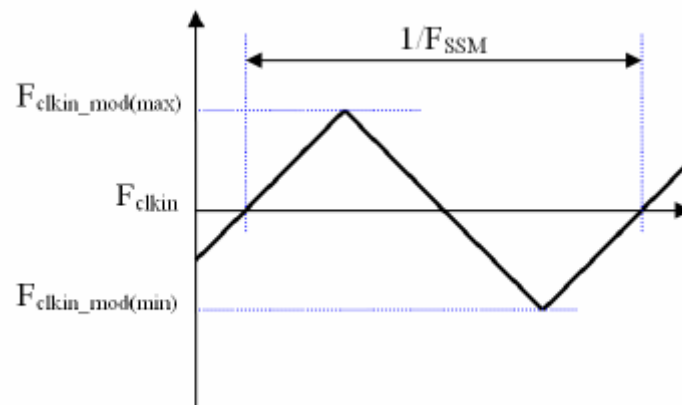
Note (a): The input clock cycle-to-cycle jitter is defined as below figures. $T_{rcl} = |T_1 - T|$



Note (b): Input Clock to data skew is defines as below figures.

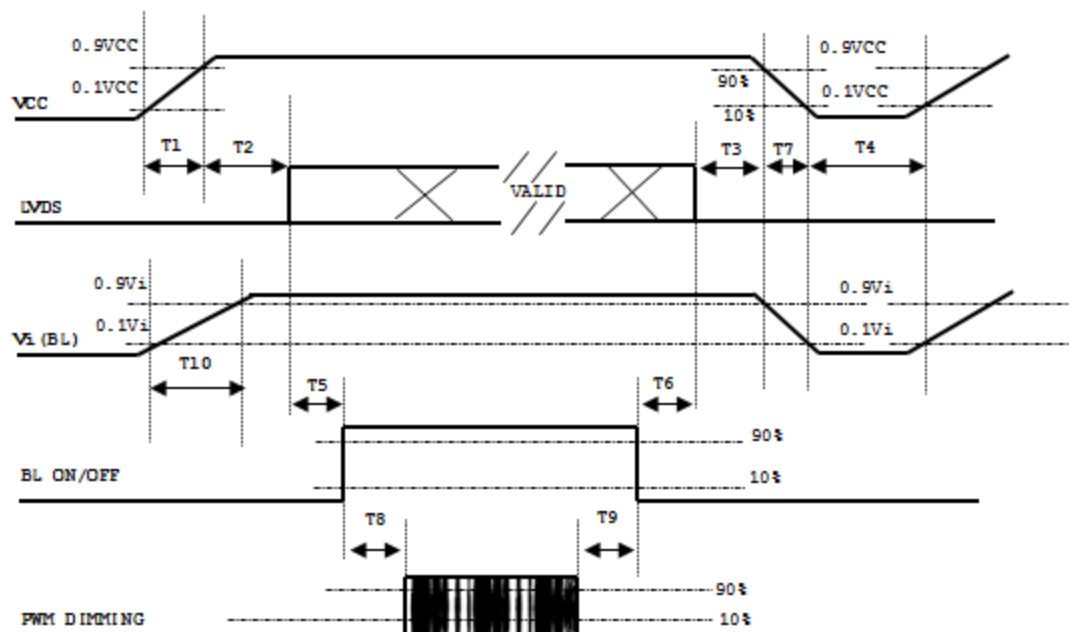


Note (c): The SSCG (Spread Spectrum clock generator) is defined as below figures.



4.1.9. POWER ON/OFF SEQUENCE FOR LCD

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram below.



Power ON/OFF sequence timing

Parameter	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	0	-	50	ms
T4	500	-	-	ms
T5	450	-	-	ms
T6	200	-	-	ms
T7	10	-	100	ms
T8	10	-	-	ms
T9	10	-	-	ms
T10	20	-	50	ms

Note:

- (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2) When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.
- (3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.
- (6) INX won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.
- (7) There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "T7 spec".

4.1.10. SCANNING DIRECTION

The following figures show the image see from the front view. The arrow indicates the direction of scan.

Fig.1 Normal Scan



Fig.2 Reverse Scan



Fig. 1 Normal scan (pin 4, LR/UD = High or NC)

Fig. 2 Reverse scan (pin 4, LR/UD = Low)

4.2.TOUCH CONTROL BOARD

B000PDU-E03

Parameter	Values			Unit	Notes
	Min.	Typ.	Max.		
Input Voltage	3.5	5	5.5	V	
Interface	USB:1.1 Full Speed / I ² C			-	
Max Resolution	4096*4096			-	
Precision	≤2.5			mm	
Two points separate distance	≥15 (pitch of finger center)			mm	
Touch point	1~10 points			-	
Power consumption (mA)	Active Mode: About 50mA			mA	
Report rate (points/sec)	≥120			Hz	
Response time	15			ms	

CONNECTOR PIN ASSIGNMENT

JP1 : USB

Brand: JST

Part number: SM05B-SRSS-TB or equivalence

JP2 : I2C

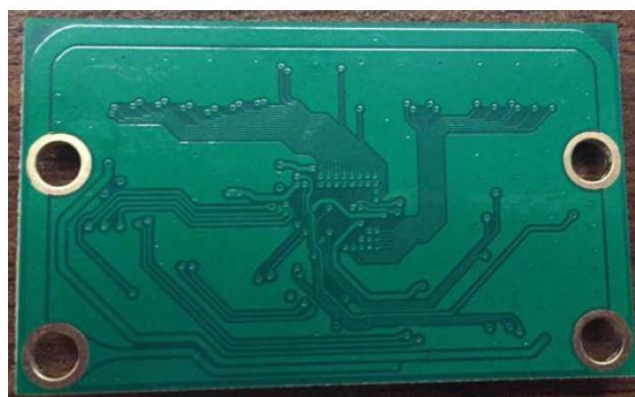
Brand: Centenary Materials

Part number: ZIF-FPC-1.0mm-SMT-6P-Bottom flap or equivalence

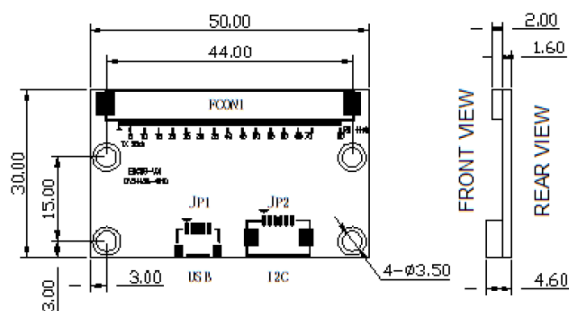
FCON1				JP1 USB		JP2 I2C	
Pin	Name	Pin	Name	Pin	Name	Pin	Name
1	Shielding(TX0)	33	GND	1	5V	1	RST
2~29	TX1-TX28	34	Shielding(RX0)	2	D-	2	INT
30	NC	35~78	RX1-RX44	3	D+	3	SCL
31	Shielding(TX29)	79	NC	4	GND	4	SDA
32	GND	80	Shielding(RX45)	5	GND	5	3.3V
						6	GND



FRONT VIEW



REAR VIEW



5. OPTICAL CHARACTERISTICS

The optical characteristics are measured under stable conditions of 25°C (Room Temperature)

Item		Symbol	Unit	Conditions	Min.	Typ.	Max.	Note
Color / Chromaticity Coordinates (CIE 1931)	Red	Rx	-	$\theta_x=0^\circ, \theta_y=0^\circ$ Grayscale Maximum	0.597	0.647	0.697	(1),(5)
		Ry			0.288	0.338	0.388	
	Green	Gx			0.271	0.321	0.371	
		Gy			0.556	0.606	0.656	
	Blue	Bx			0.107	0.157	0.207	
		By			0.000	0.039	0.089	
	White	Wx			0.263	0.313	0.363	
		Wy			0.279	0.329	0.379	
	Center Luminance of white				LC	cd/m ²	400	
Contrast Ratio		CR	-	1800	2500	-	(2),(5)	
Response Time		T _R	ms	$\theta_x=0^\circ, \theta_y=0^\circ$	-	16	21	(3)
		T _F			-	7	14	
White Variation		δW	%	$\theta_x=0^\circ, \theta_y=0^\circ$	75	80		(5),(6)
View Angle	Horizontal	θX+	degree	CR≥10	80	88	-	(1),(5)
		θX-			80	88	-	
	Vertical	θY+			80	88	-	
		θY-			80	88	-	

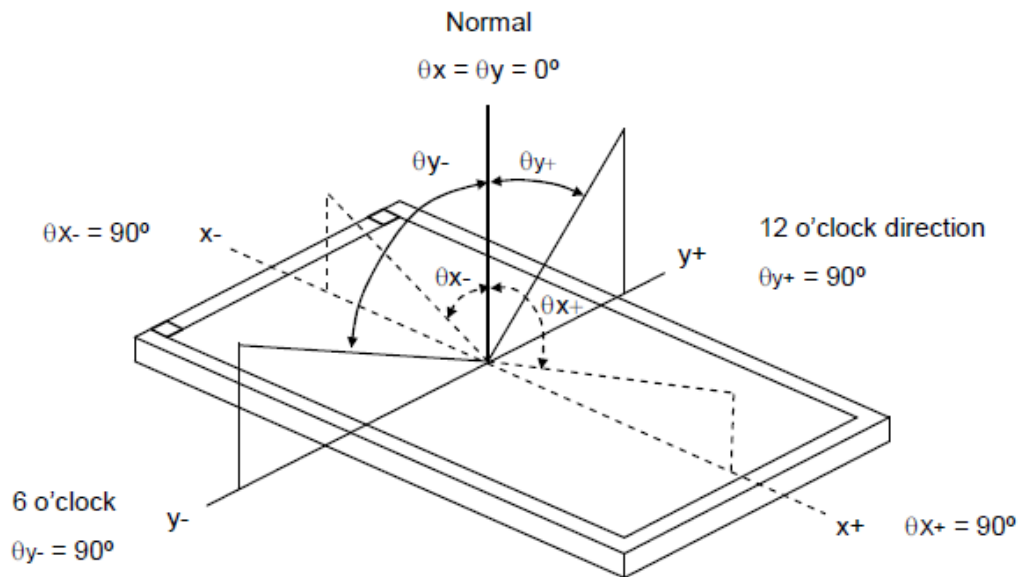
Definition :

Grayscale Maximum: Grayscale 255 (10 bits: grayscale 1023; 8 bits: grayscale 255; 6 bits: grayscale 63)

White: Luminance of Grayscale Maximum (All R, G, B)

Black: Luminance of grayscale 0 (All R, G, B)

Note (1): Definition of Viewing Angle (θ_x , θ_y):

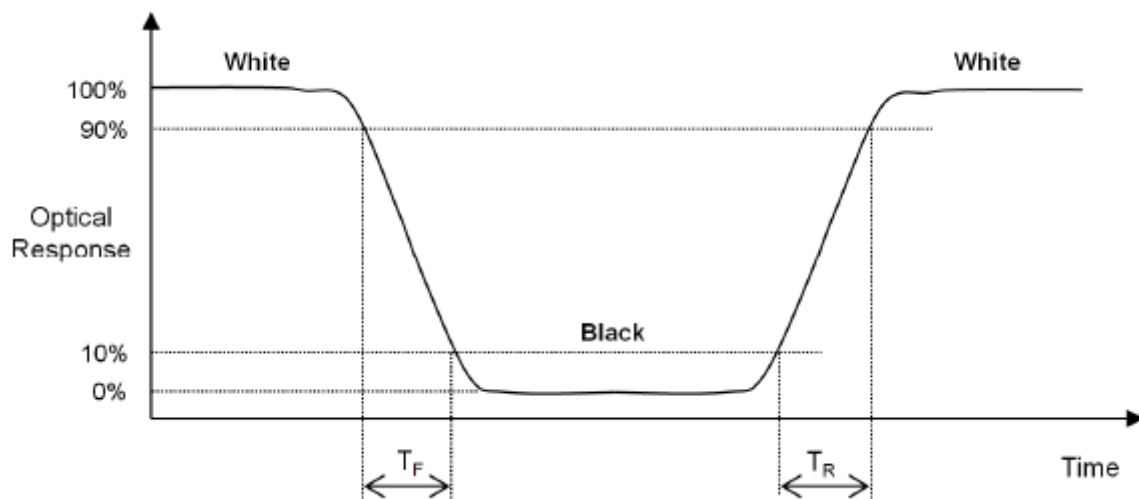


Note (2): Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression at center point.

Contrast Ratio CR = White / Black

Note (3): Definition of Response Time (T_R , T_F):

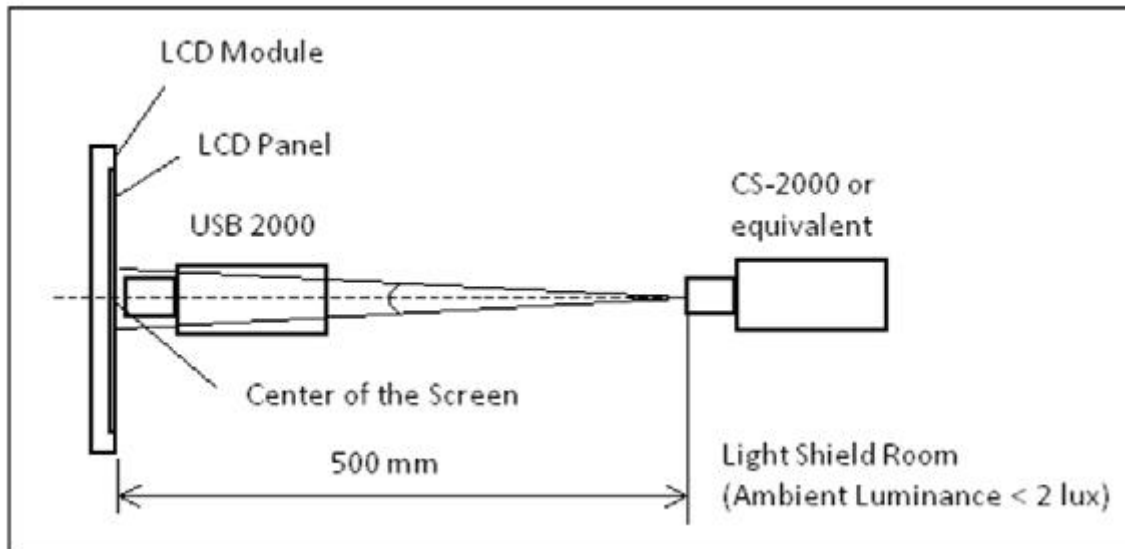


Note (4): Definition of Luminance of White (LC):

Measure the luminance of White at center point

Note (5): Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.

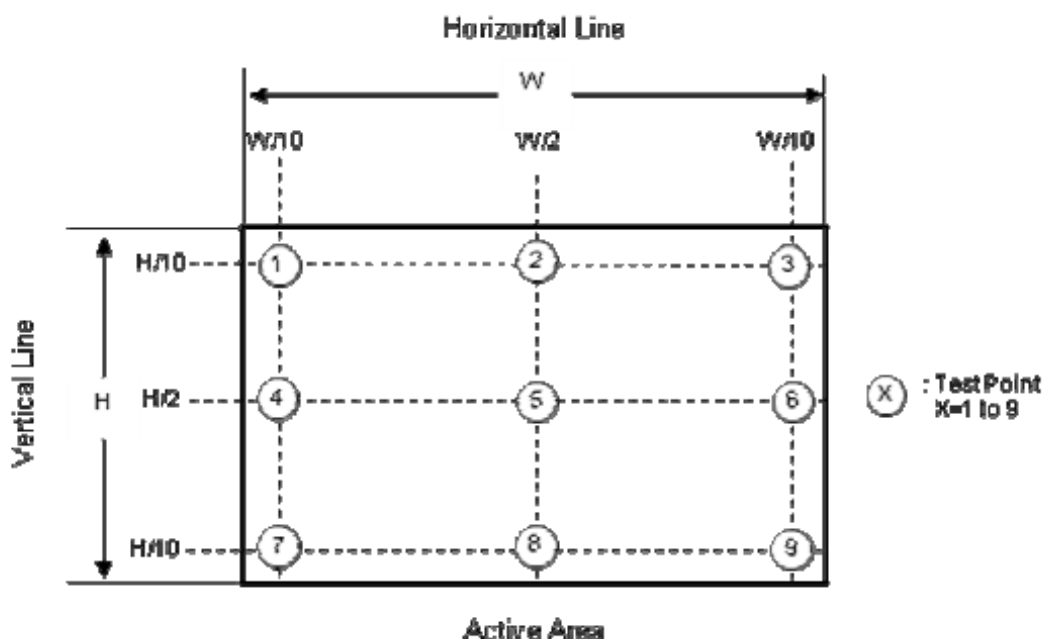


Note (6): Definition of White Variation (δW):

Measure the luminance of White at 9 points

Luminance of White : $L(X)$, where X is from 1 to 9.

$$\delta W = \frac{\text{Minimum} [L(1) \text{ to } L(9)]}{\text{Maximum} [L(1) \text{ to } L(9)]} \times 100\%$$



6. RELIABILITY TEST ITEMS

Test Item		Condition
1	High temperature/Humidity Bias	60°C, 90% 240hrs
2	High temperature storage test	80°C, 240hrs
3	Low temperature storage test	-30°C, 240hrs
4	High temperature operation test	70°C, 240hrs
5	Low temperature operation test	-20°C, 240hrs
6	Thermal Cycle storage test	-20°C(30min)~70°C(30min), 10cycles

7. LABEL

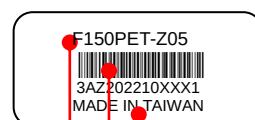
Model NO. F150PET-Z05

條碼

L=32mm

SN 3A Z 20 2 21 0 XXX1

code 1,2 Production location
 code 3 Production version
 code 4,5 Production year
 code 6 Production month
 code 7,8 Production day
 code 9 Production code
 code 10,11,12,13 Serial number



W=14mm

字型:Arial (6)
 code:128
 字型:Arial (8)

8. MECHANICAL CHARACTERISTICS

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	326.50	mm	PCAP : B150PBU-E03
	Vertical	253.50	mm	
	Depth	2.40	mm	
	Horizontal	326.50	mm	LCD : G150XNE-L01
	Vertical	253.50	mm	
	Depth	9.10	mm	
	Horizontal	50.00	mm	Touch Driver : B000PDU-E03
	Vertical	30.00	mm	
	Depth	1.60	mm	
Weight		1450	g	F150PET-Z05
Item		Specification		Remarks
PCAP Viewing Area		305.13 mm x 229.10 mm		
PCAP Sensor Area		325.50 mm x 252.50 mm		
PCAP FPC Length		110 mm		
PCAP Input method		Finger or exclusive pen		
PCAP Light Transmission		≥85%		
PCAP Operation force		≤10g		Finger Input
PCAP Hardness of surface		≥6H		Pressure 500gf test
PCAP Surface Treatment		Clear Type		

Front View



Back View



9. ENGINEERING DRAWING

