



Product Specification

F150PET-Z06

- ☐ Draft Specifications
☐ Preliminary Specifications
☒ Final Specifications

Title	15.0-inches Touch Display Product Specifications
Model Name	F150PET-Z06
Version	2

Customer	
Approved by	Date
Notice: This Specification is subject to change without notice.	

Approved By	Prepared By
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2026-05-20	2026-05-20



REVISION HISTORY

Version	ECN No.	Description of Changes	Date	Prepared
0		Draft specification first edition	2023-02-13	Allen Fang
1		Preliminary Specification	2025-12-22	Allen Fang
2		Final Specification	2026-05-20	Allen Fang



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1. GENERAL DESCRIPTION

1.1. INTRODUCTION

F150PET-Z06 is a 15.0" color active matrix TFT LCD module G150XTN03.5 with projected capacitive multi-touch B150PBU-E03 air bonding, with touch driver B000PDU-E03.

The display format is intended to support the XGA mode (1024(H) x 768(V)) screen and 262K/16.2M colors (LVDS 6/8-bits). And LED driving circuit for backlight built in PCBA.

1.2. FEATURES

- Long life LED backlight (50k hrs)
- Industrial application
- RoHS product

1.3. APPLICATION

- TFT LCD monitor
- Industrial application

1.4. GENERAL SPECIFICATIONS

Parameter	Specification	Unit	Remarks
Active Area Size	15.0	inch	
Display Area	304.128 x 228.096 (V)	mm	
Outline Dimension	326.50 (H) x 253.50 (V) x 12.58 (D)	mm	D:PCAP+LCM
Weight	1400	g	Typ.
Driver Element	a-Si TFT active matrix	-	
Display Colors	16,194,277 / 262,144	Colors	
Electrical Interface	LVDS	-	
Number of Pixels	1024 (H) x 768 (V)	pixels	
Pixel Pitch	0.297 (H) x 0.297(W)	mm	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	TN / Normal White	-	
Color Gamut	60	%	
Touch Technology	Projected Capacitive Multi-Touch Panel	-	
Touch Method	Finger or exclusive pen	-	
Numbers of Touch	10	Points	
Touch Driver	B000PDU-E03: USB	-	
Touch Sensor Glass Type	Strengthening Glass	-	
Touch Controller IC	EETI, EXC80W46	-	
Touch Driver Interface	USB 1.1 Full Speed / I ² C	-	
Support OS	Windows (Embedded/CE/XP/2000/Vista/7/8/10) / Linux / Android / MAC / QNX	-	

2. ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

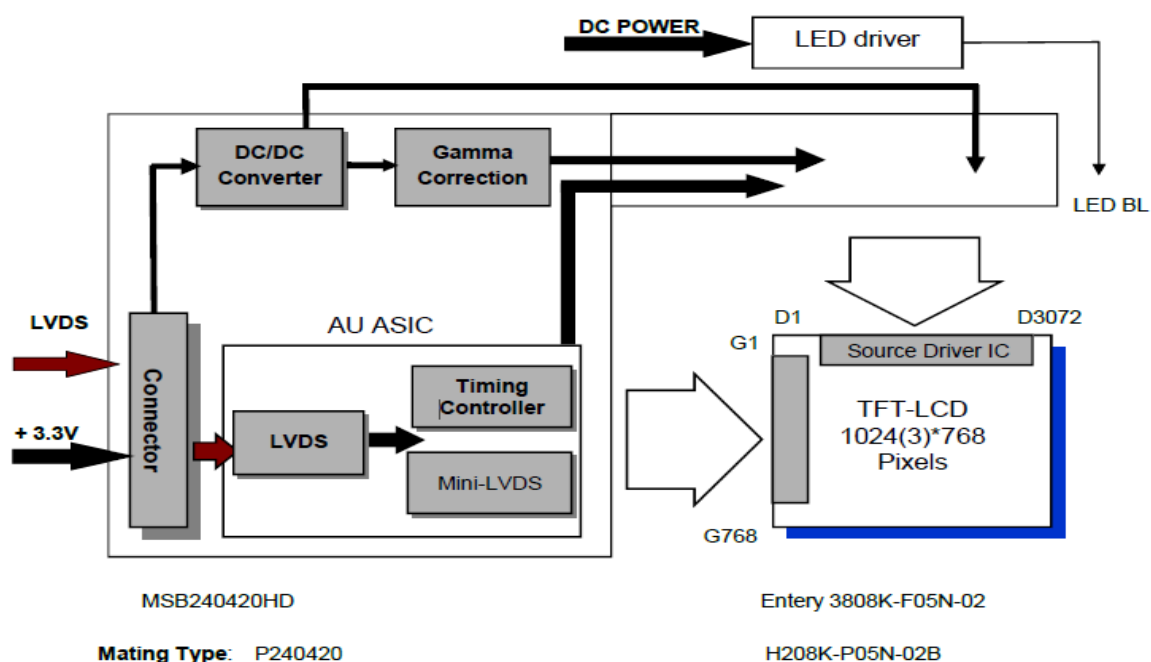
Parameter	Symbol	Min.	Max.	Unit
Logic/LCD Drive Voltage	V_{DD}	-0.3	+3.6	V
Converter Input Voltage	V_{LED}	-0.3	+13.2	V
Converter Control Signal Voltage	V_{LED_PWM}	-0.3	+5.5	V
Converter Control Signal Voltage	V_{LED_EN}	-0.3	+5.5	V
Operating Temperature	T_{op}	0	+60	°C
Operating Humidity	H_{op}	8	90	%RH
Storage Temperature	T_{st}	-20	+65	°C
Storage Humidity	H_{st}	8	90	%RH
Touch DC Supply Voltage	USB_V_{DD}	-0.3	+5.5	V

Note: Maximum Wet-Bulb temperature should be 39°C and no condensation.

3. ELECTRICAL CHARACTERISTICS

3.1. LCD BLOCK DIAGRAM

The following diagram shows the functional block of the 15 inch color TFT/LCD module:

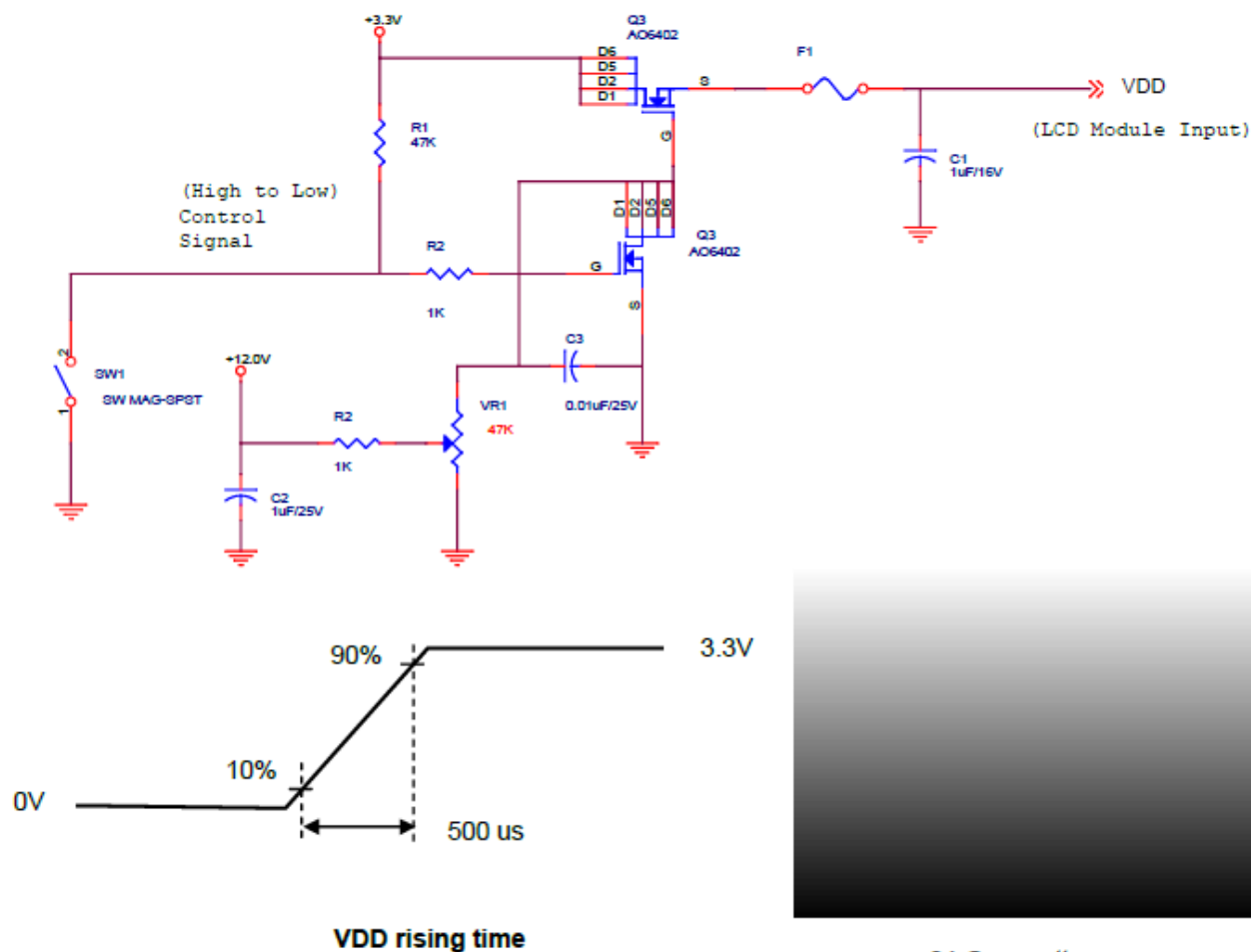


3.2. LCD MODULE

3.2.1. POWER SPECIFICATION

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Logic/LCD Drive Voltage	VDD	3	3.3	3.6	V	
V _{DD} Current	I _{VDD}	-	450	750	mA	64 Gray Bar Pattern (VDD=3.3V at 60Hz)
LCD Inrush Current	I _{rush} LCD	-	-	2	A	Note 1
VDD Power	P _{VDD}	-	1.5	2.5	W	64 Gray Bar Pattern (VDD=3.3V at 60Hz)

Notes 1: Measurement condition



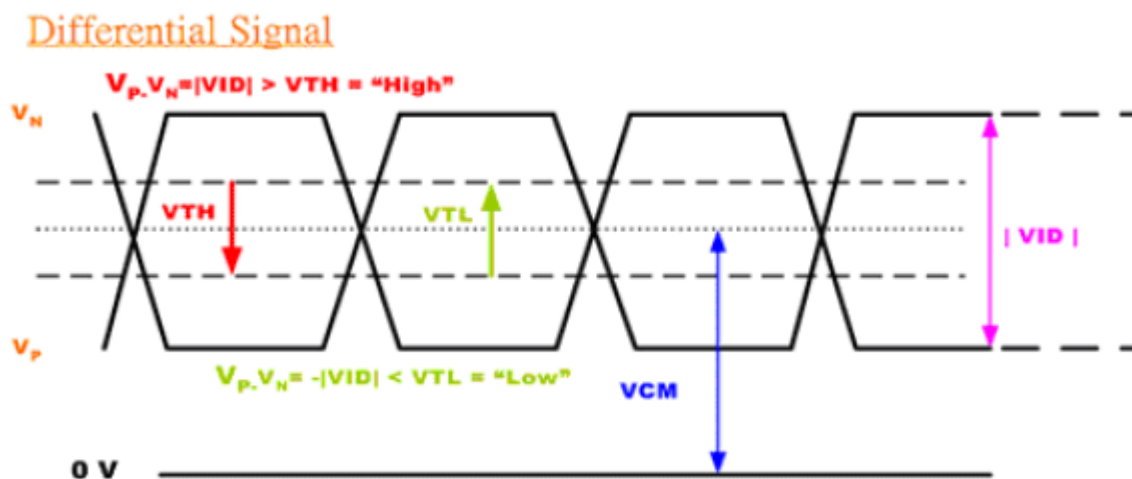
64 Gray pattern

3.2.2. SIGNAL ELECTRICAL CHARACTERISTICS

Input signals should be low or Hi-Z state when VDD is off.

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Differential Input High Threshold	VTH	-	-	100	mV	VICM=1.2V
Differential Input Low Threshold	VTL	100	-	-	mV	VICM=1.2V
Input Differential Voltage	VID	100	400	600	mV	
Differential Input Common Mode Voltage	VICM	1.1	--	1.45	V	VTH/VTL=±100mV

Note: LVDS Signal Waveform.



3.3. LCD BACKLIGHT UNIT

3.3.1. LED DRIVER CONNECTOR

Connector Name / Description	LED Connector
Manufacturer	ENTERY or compatible
Connector Model Number	3808K-F05N-02 or compatible
Mating Model Number	H2085K-P05N-02B or compatible

LED DRIVER Connector Pin Assignment

Pin No.	Symbol	Description
1	VCC	12V
2	GND	Ground
3	Enable	VLED On/Off
4	Dimming	PWM Dimming
5	NC	No Connect

3.3.2. PARAMETER GUIDELINE FOR LED BACKLIGHT

Following characteristics are measured under a stable condition using a inverter at 25°C. (Room Temperature):

Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max.		
Input Voltage		V_{CC}	10.8	12	13.2	V	
Input Current		I_{VCC}	-	0.402	0.42	A	100% PWM Duty
Power Consumption		P_{VLED}	-	4.83	5.1	W	100% PWM Duty
Inrush Current		I_{rush}			2	A	
EN Control Level	Backlight on	V_{LED_EN}	2.5	3.3	5.5	V	
	Backlight off				0.5	V	
PWM Control level	PWM High	V_{PWM}	2.5	3.3	5.5	V	
	PWM Low				0.5	V	
PWM Control Duty Ratio		D_{PWM}	5	-	100	%	
PWM Control Frequency		F_{PWM}	200	-	10K	Hz	
LED Forward Current		I_F	-	60	-	mA	Ta=25°C
LED Life Time		LT_{LED}	50,000	70,000	-	Hrs	$I_F=60mA$, Ta=25°C

Note:

1. Ta, means ambient temperature of TFT-LCD module.
2. If G150XTN03.5 module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.
3. Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.

3.4. INPUT TERMINAL PIN ASSIGNMENT

LVDS is a differential signal technology for LCD interface and a high-speed data transfer device.

Pin	Symbol	Description
1	VDD	Power supply: +3V
2	VDD	Power supply: +3V
3	NC	No Connect
4	NC	No Connect
5	RX0-	LVDS receiver signal channel 0
6	RX0+	LVDS Differential Data Input (R0, R1, R2, R3, R4, R5, G0)
7	GND	Ground
8	RX1-	LVDS receiver signal channel 1
9	RX1+	LVDS Differential Data Input (G1, G2, G3, G4, G5, B0, B1)
10	GND	Ground
11	RX2-	LVDS receiver signal channel 2
12	RX2+	LVDS Differential Data Input (B2, B3, B4, B5, HS, VS, DE)
13	GND	Ground
14	RXCLK-	LVDS receiver signal clock
15	RXCLK+	
16	GND	Ground
17	RX3-	LVDS receiver signal channel 3
18	RX3+	LVDS Differential Data Input (R6, R7, G6, G7, B6, B7)
19	GND	Ground
20	SEL68	6/8 bit LVDS data selection [H: 8 bits / L or NC: 6bit]

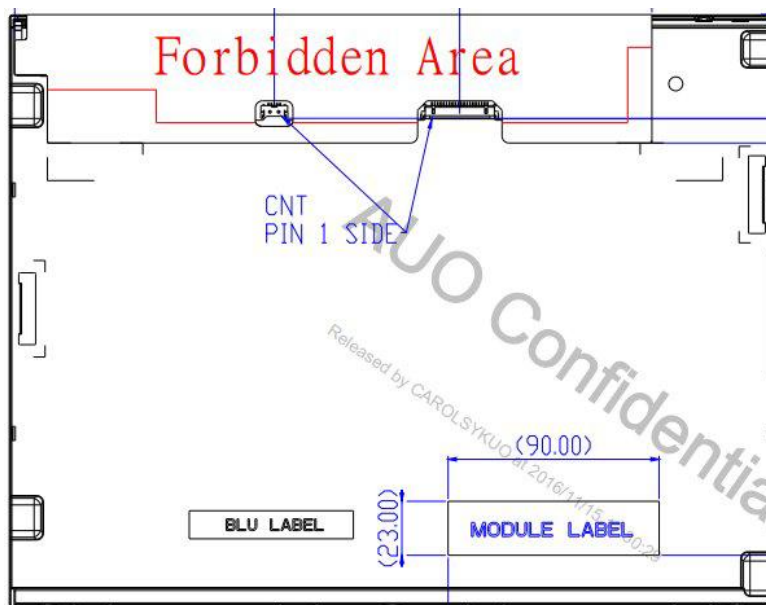
Note1. LCD connector: STM MSB240420HD or compatible

Note2. User's connector Part No.: STM P240420 or compatible

Note3. Input Signals should be in low status when V_{DD} is off.

Note4. High Stands for “3.3V”, Low stands for “0V”, NC stands for “No Connect”.

Note6. For 6 bits mode, please keep the Pin 17& Pin 18 NC or make sure that the voltage of Pin 18 is always higher than the voltage of Pin 17.



3.5.TOUCH CONTROL BOARD

B000PDU-E03

Parameter	Values			Unit	Notes
	Min.	Typ.	Max.		
Input Voltage	3.5	5	5.5	V	
Interface	USB:1.1 Full Speed / I ² C			-	
Max Resolution	4096*4096			-	
Precision	≤2.5			mm	
Two points separate distance	≥15 (pitch of finger center)			mm	
Touch point	1~10 points			-	
Power consumption (mA)	Active Mode: About 50mA			mA	
Report rate (points/sec)	≥120			Hz	
Response time	15			ms	

3.6. TOUCH CONTROL BOARD PIN ASSIGNMENT

JP1 : USB

Brand: JST

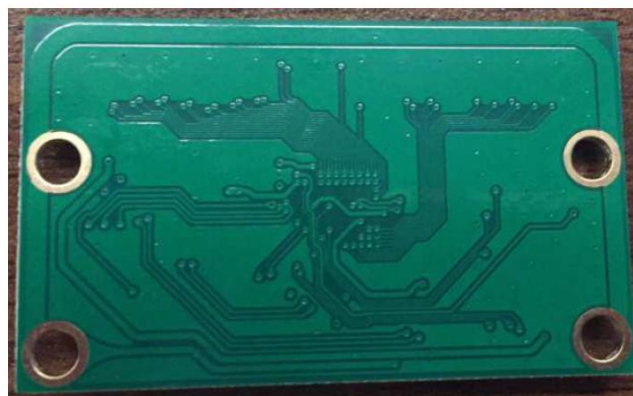
Part number: SM05B-SRSS-TB or equivalence

JP2 : I2C

Brand: Centenary Materials

Part number: ZIF-FPC-1.0mm-SMT-6P-Bottom flap or equivalence

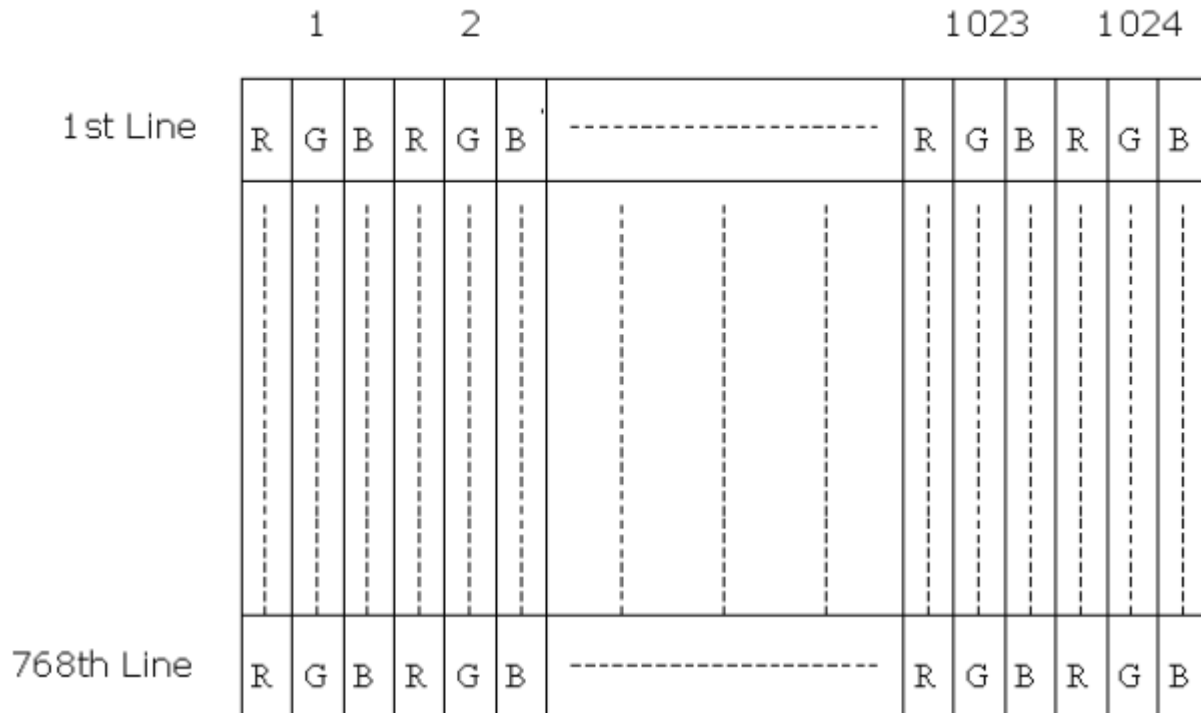
FCON1				JP1 USB		JP2 I2C	
Pin	Name	Pin	Name	Pin	Name	Pin	Name
1	Shielding(TX0)	33	GND	1	5V	1	RST
2~29	TX1-TX28	34	Shielding(RX0)	2	D-	2	INT
30	NC	35~78	RX1-RX44	3	D+	3	SCL
31	Shielding(TX29)	79	NC	4	GND	4	SDA
32	GND	80	Shielding(RX45)	5	GND	5	3.3V
						6	GND



4. LCD SIGNAL CHARACTERISTIC

4.1. PIXEL FORMAT IMAGE

Following figure shows the relationship between input signal and LCD pixel format.



4.1.1. SCANNING DIRECTION

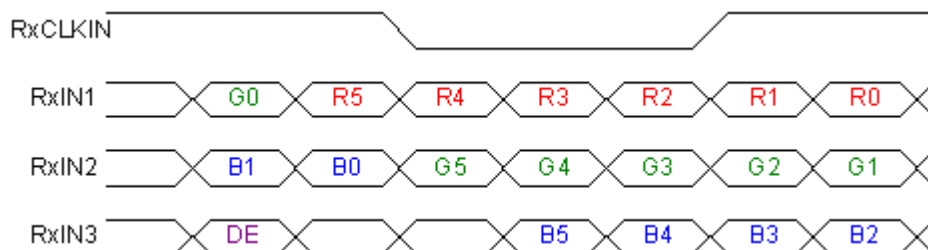
The following figures show the image seen from the front view. The arrow indicates the direction of scan.



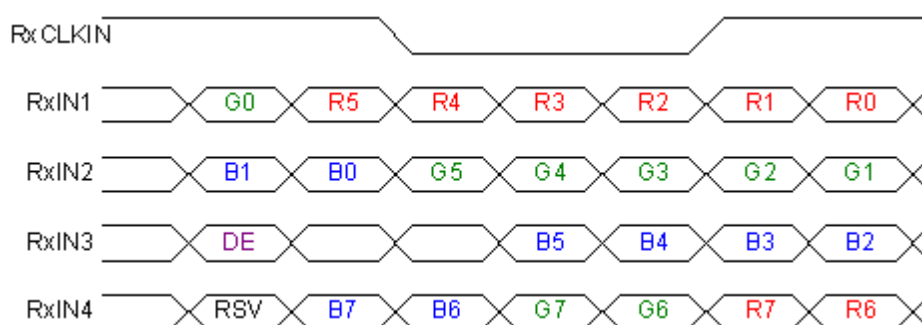
4.2. THE LCD INPUT DATA FORMAT

4.2.1. SEL68 AND ORDER_SEL

SEL68 = "High" for 6 bits LVDS Input



SEL68 = "Low" or "NC" for 8 bits LVDS Input



Note1: Please follow PSWG.

Note2: R/G/B data 7: MSB, R/G/B data 0: LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0	Red-pixel Data For 8Bits LVDS input MSB: R7 ; LSB: R0 For 6Bits LVDS input MSB: R5 ; LSB: R0
G7 G6 G5 G4 G3 G2 G1 G0	Green Data 7 Green Data 6 Green Data 5 Green Data 4 Green Data 3 Green Data 2 Green Data 1 Green Data 0	Green-pixel Data For 8Bits LVDS input MSB: G7 ; LSB: G0 For 6Bits LVDS input MSB: G5 ; LSB: G0
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0	Blue-pixel Data For 8Bits LVDS input MSB: B7 ; LSB: B0 For 6Bits LVDS input MSB: B5 ; LSB: B0
RxCLKIN	LVDS Data Clock	
DE	Data Enable Signal	When the signal is high, the pixel data shall be valid to be displayed.

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

4.3. LCD INTERFACE TIMING

4.3.1. LCD TIMING CHARACTERISTICS

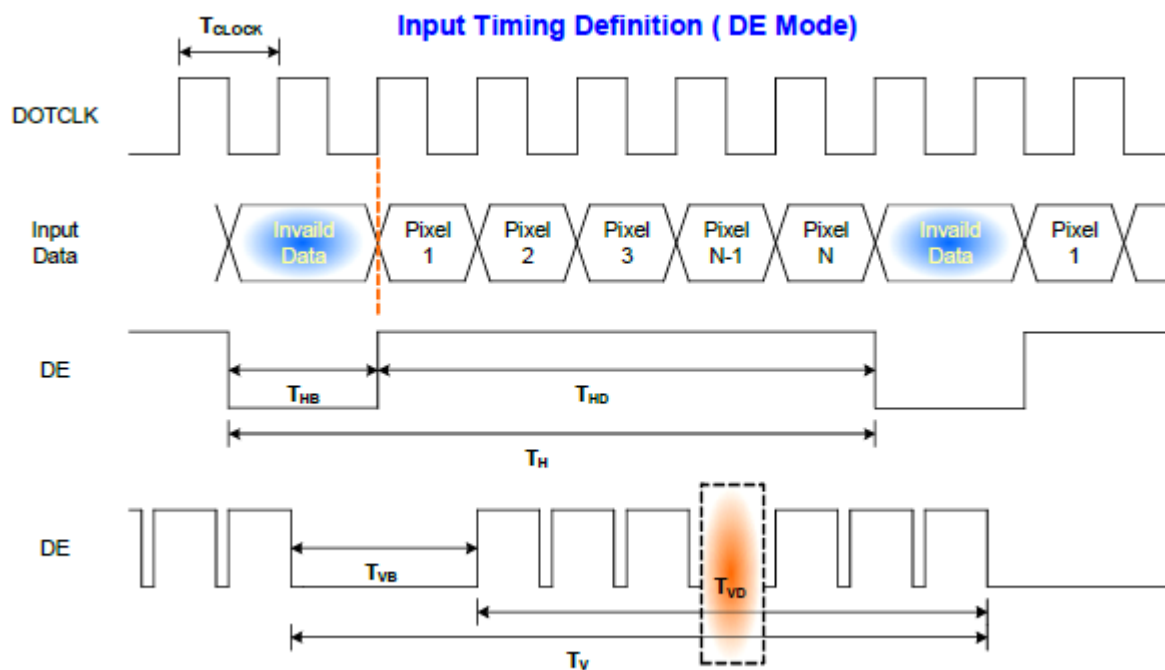
Signal		Symbol	Min.	Typ.	Max.	Unit
Clock Frequency		$1/T_{\text{dclk}}$	50	65	81	MHz
Vertical Section	Period	T_V	783	806	968	T_{Line}
	Active	T_{VD}	768	768	768	
	Blanking	T_{VB}	15	38	200	
Horizontal Section	Period	T_H	1224	1344	2024	T_{dclk}
	Active	T_{HD}	1024	1024	1024	
	Blanking	T_{HB}	200	320	1000	

Note: The Maximum clock frequency = Frame Rate < 81 MHz / [(V_Period)*(H_Period)]

Note: Support DE mode only

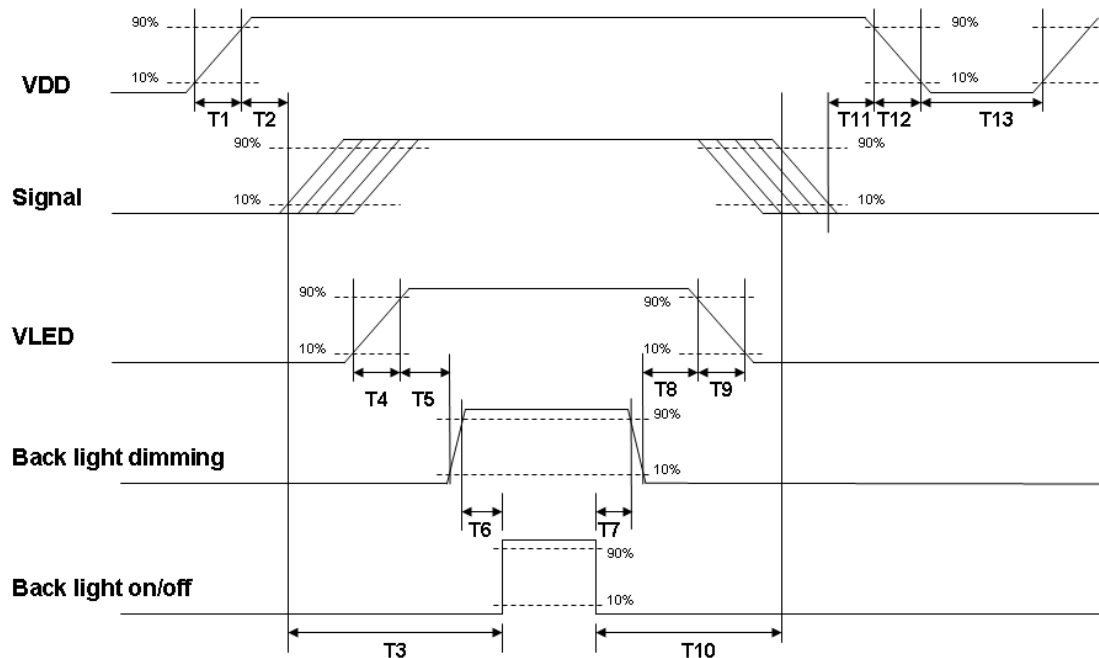
Note: Typical value refer to VESA STANDARD

4.3.2. LCD INPUT TIMING DIAGRAM



4.4. POWER SEQUENCE FOR LCD

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Parameter	Values			Unit
	Min.	Typ.	Max.	
T1	0.5	---	10	ms
T2	30	40	50	ms
T3	200	---	---	ms
T4	0.5	---	10	ms
T5	10	---	---	ms
T6	10	---	---	ms
T7	0	---	---	ms
T8	10	---	---	ms
T9	---	---	10	ms
T10	110	---	---	ms
T11	0	16	50	ms
T12	---	---	10	ms
T13	1000	---	---	ms

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

5. OPTICAL CHARACTERISTICS

The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
White Luminance		L _W		280	350	--	Cd/m ²	1
Uniformity		δ _{W5p}	9 points	75	80	--	%	1, 2, 3
Contrast Ratio		CR		400	800	--		4
Cross talk				--	--	1.5	%	5
Response Time		T _R	θ _X =0°, θ _Y =0°	--	5.7	--	ms	6
		T _F		--	2.3	--	ms	
		T _R +T _F		--	8	--	ms	
Viewing Angle	Horizontal	θ _r	CR=10	70	80	--	degree	7
		θ _l		70	80	--	degree	
	Vertical	θ _u		60	70	--	degree	
		θ _d		70	80	--	degree	
Color Coordinates (CIE 1931)	Red	R _x	θ _X =0°, θ _Y =0° Grayscale Maximum	0.547	0.597	0.647		
		R _y		0.289	0.339	0.389		
	Green	G _x		0.279	0.329	0.379		
		G _y		0.546	0.596	0.646		
	Blue	B _x		0.104	0.154	0.204		
		B _y		0.003	0.053	0.103		
	White	W _x		0.263	0.313	0.363		
		W _y		0.279	0.329	0.379		
Color Gamut				--	60	--	%	

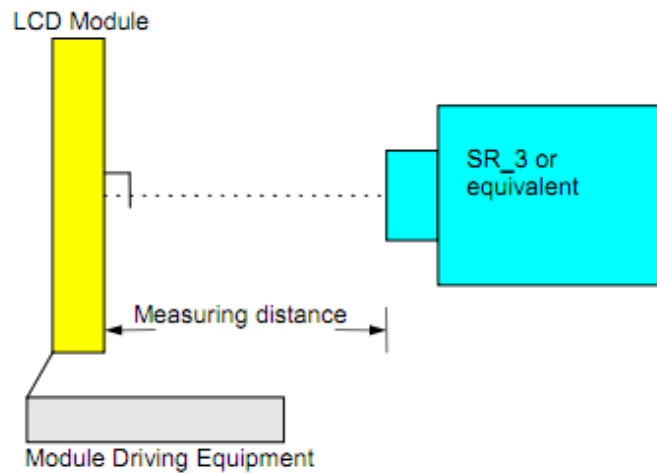
Note 1: Measurement method

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR_3 or equivalent)

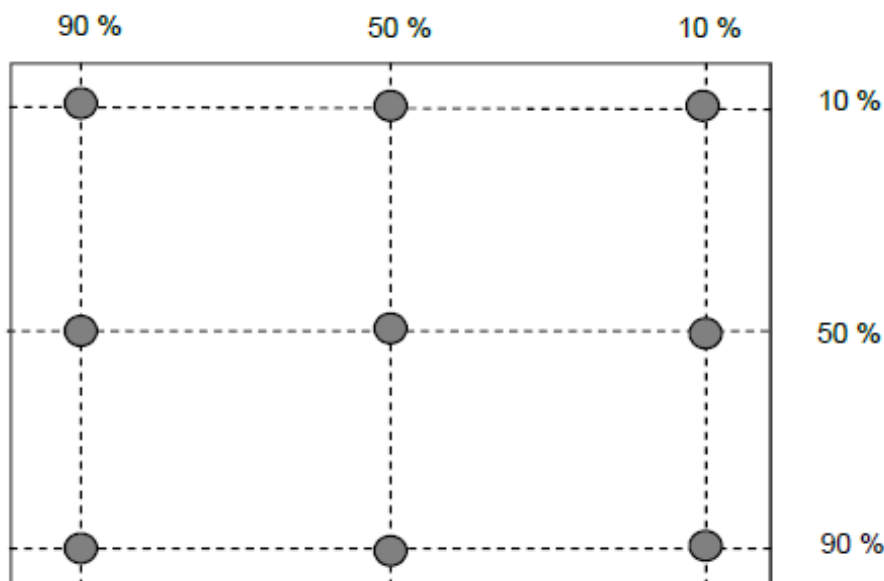
Aperture 1° with 50cm viewing distance

Test Point Center

Environment < 1 lux



Note 2: Definition of 9 points position (Display active area: 304.128mm (H) x228.096mm (V))



Note 3: The luminance uniformity of 9 points is defined by dividing the minimum luminance values by the maximum test point luminance

$$\delta_{w9} = \frac{\text{Minimum Brightness of nine points}}{\text{Maximum Brightness of nine points}}$$

Note 4: Definition of contrast ratio (CR)

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$

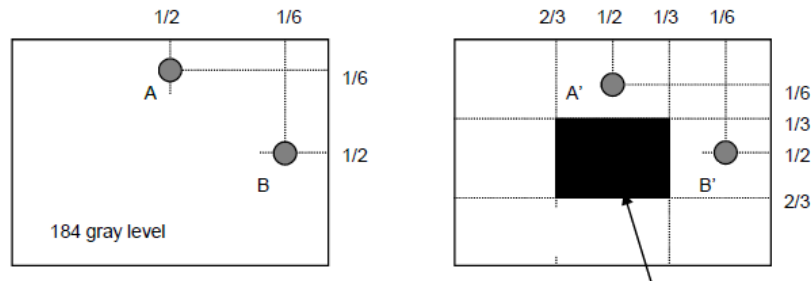
Note 5: Definition of cross talk (CT)

$$CT = |YB - YA| / YA \times 100 (\%)$$

Where

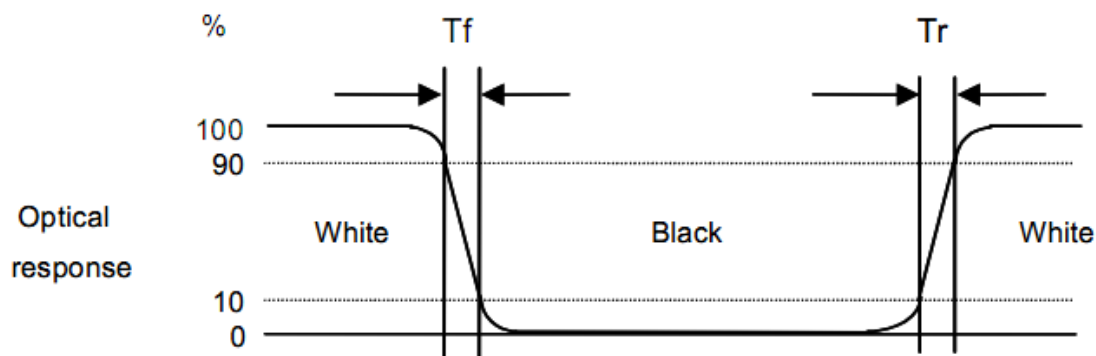
YA = Luminance of measured location without gray level 0 pattern (cd/m²)

YB = Luminance of measured location with gray level 0 pattern (cd/m²)



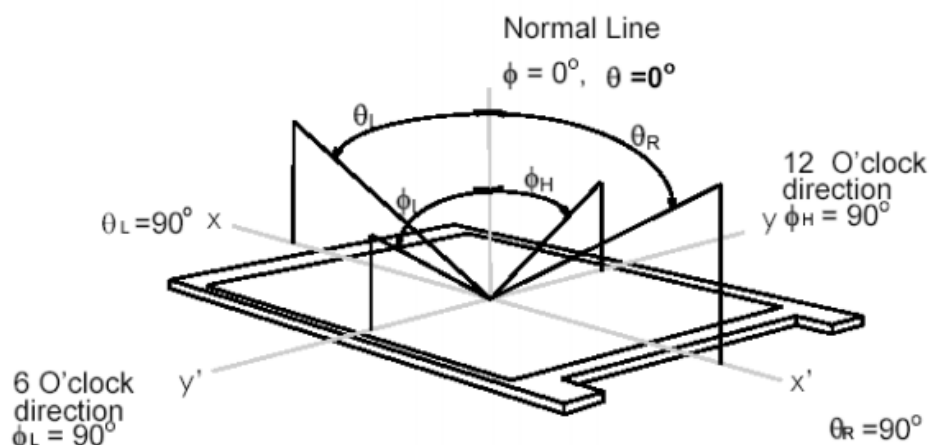
Note6: Definition of Response Time (TR, TF)

The output signals of photo detector are measured when the input signals are changed from “White” to “Black” (falling time) and from “Black” to “White” (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



Note7: Definition of viewing angle

Viewing angle is the measurement of contrast ratio ≥ 10 , at the screen center, over a 180° horizontal and 180° vertical range (off-normal view angles). The 180° viewing angle range is broken down as below: 90° (θ) horizontal left and right, and 90° (ϕ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



6. RELIABILITY TEST ITEMS

Test Item		Condition
1	High temperature storage test	65°C, 240hrs
2	Low temperature storage test	-20°C, 240hrs
3	High temperature/Humidity Bias test	50°C, 80%RH, 240hrs
4	High temperature operation test	60°C, 240hrs
5	Low temperature operation test	0°C, 240hrs
6	Thermal Cycle storage test	-20°C(30min)~60°C(30min), 10cycles

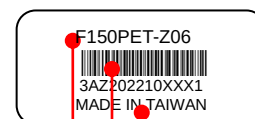
7. LABEL

Model NO. F150PET-Z06

條碼

SN 3A Z 20 2 21 0 XXX1

L=32mm



W=14mm

code 1,2 Production location

code 3 Production version

code 4,5 Production year

code 6 Production month

code 7,8 Production day

code 9 Production code

code 10,11,12,13 Serial number

字型:Arial (6)

code:128

字型:Arial (8)

8. MECHANICAL CHARACTERISTICS

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	326.50	mm	PCAP : B150PBU-E03
	Vertical	253.50	mm	
	Depth	2.40	mm	
	Horizontal	326.50	mm	LCD : G150XTN03.5
	Vertical	253.50	mm	
	Depth	9.50	mm	
	Horizontal	50.00	mm	Touch Driver : B000PDU-E03
	Vertical	30.00	mm	
	Depth	1.60	mm	
Weight		1400	g	F150PET-Z06
Item		Specification		Remarks
PCAP Viewing Area		305.13 mm x 229.10 mm		
PCAP Sensor Area		325.50 mm x 252.50 mm		
PCAP FPC Length		110.00 mm		
PCAP Input method		Finger or exclusive pen		
PCAP Light Transmission		≥85%		
PCAP Operation force		≤10g		Finger Input
PCAP Hardness of surface		≥6H		Pressure 500gf test
PCAP Surface Treatment		Clear Type		

Front View



Back View



9. ENGINEERING DRAWING

