



- ☒ Draft Specifications
☐ Preliminary Specifications
☐ Final Specifications

Title	15.6-inches Touch Display Product Specifications
Model Name	F156PET-T42
Version	0

Customer	
Approved by	Date
Notice: This Specification is subject to change without notice.	

Approved By	Prepared By
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2026-05-22	2026-05-22



REVISION HISTORY

Version	ECN No.	Description of Changes	Date	Prepared
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1. GENERAL DESCRIPTION

1.1. INTRODUCTION

F156PET-T42 is a 15.6" color active matrix TFT LCD IAV module G156HCE-E01 with projected capacitive multi-touch B156PFU-E15 air bonding.

The display supports 1920 x 1080 FHD AAS mode and can display 16.7M colors, 40 pins eDP interface (RGB 8-bits) with LED backlight driving circuit.

1.2. FEATURES

- FHD (1920x1080) resolution
- Long life LED backlight (50k hrs)
- NB module
- RoHS product

1.3. APPLICATION

- TFT LCD monitor



2. GENERAL SPECIFICATIONS

Parameter	Specification	Unit	Remarks
Active Area Size	15.6	inch	
Display Area	344.16(H) x 193.59 (V)	mm	
Outline Dimension	375.24 (H) x 240.68 (V) x 9.6 (D)	mm	D:PCAP+LCM
Weight	970	g	Typ.
Driver Element	a-Si TFT active matrix	-	
Display Colors	16.7M (RGB 8-bits)	Colors	
Electrical Interface	2 Lane eDP	-	
Number of Pixels	1920 (H) x 1080 (V)	pixels	
Pixel Pitch	0.17925 (H) x 0.17925(W)	mm	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	AAS / Normal Black	-	
Color Gamut	72	%	
Touch Technology	Projected Capacitive Multi-Touch Panel	-	
Touch Method	Finger or exclusive pen	-	
Numbers of Touch	10	Points	
Touch FPC Type	COF	-	
Touch Sensor Glass Type	Strengthening Glass	-	
Touch Controller IC	EETI, EXC81W60	-	
Touch Driver Interface	USB & I ² C	-	
Support OS	Android, Linux, Win7, Win8, Win10, XP	-	

3. ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

Parameter	Symbol	Min.	Max.	Unit
Logic/LCD Drive Voltage	V _{DD}	-0.3	+4.0	V
LCD Input Signal Voltage	V _{signal}	-0.3	+4.0	V
LED Input Voltage	LED_V _{in}	-0.3	18.0	V
LED Control Signal Voltage	LED_EN,PWM	-0.3	+5.5	V
Operating Temperature	T _{op}	0	+60	°C
Operating Humidity	H _{op}	10	90	%RH
Storage Temperature	T _{st}	-20	+70	°C
Storage Humidity	H _{st}	10	90	%RH
Touch DC Supply Voltage	I ² C	-0.3	3.6	V
Touch DC Supply Voltage	USB	-0.3	6.0	V

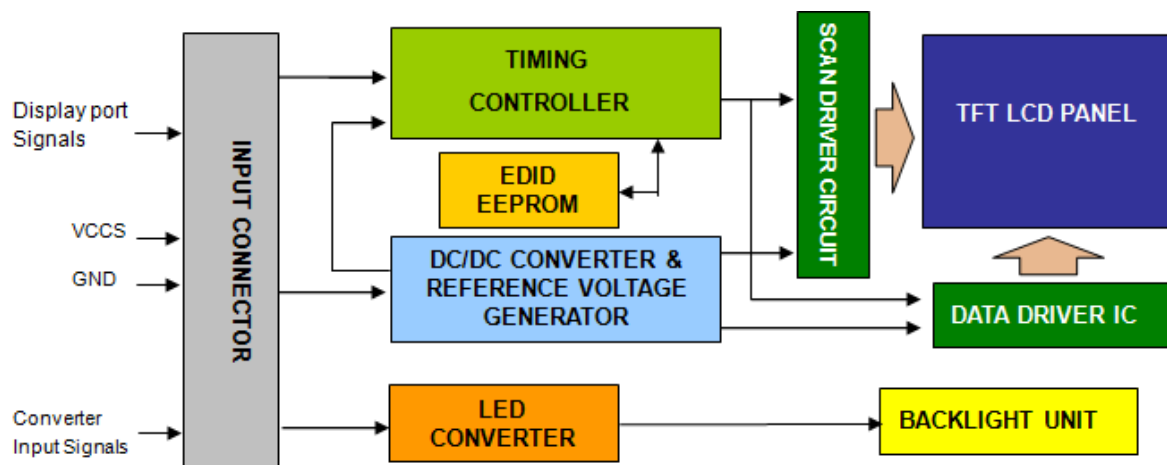
Note: Maximum Wet-Bulb temperature should be 39°C and no condensation.

4. ELECTRICAL CHARACTERISTICS

4.1. LCD MODULE

4.1.1. LCD BLOCK DIAGRAM

The following diagram shows the function block of the 15.6 inches wide Color TFT/LCD module.



4.1.2. POWER SPECIFICATION

The LCD power specifications are measured under 25°C and frame frequency under 60Hz.

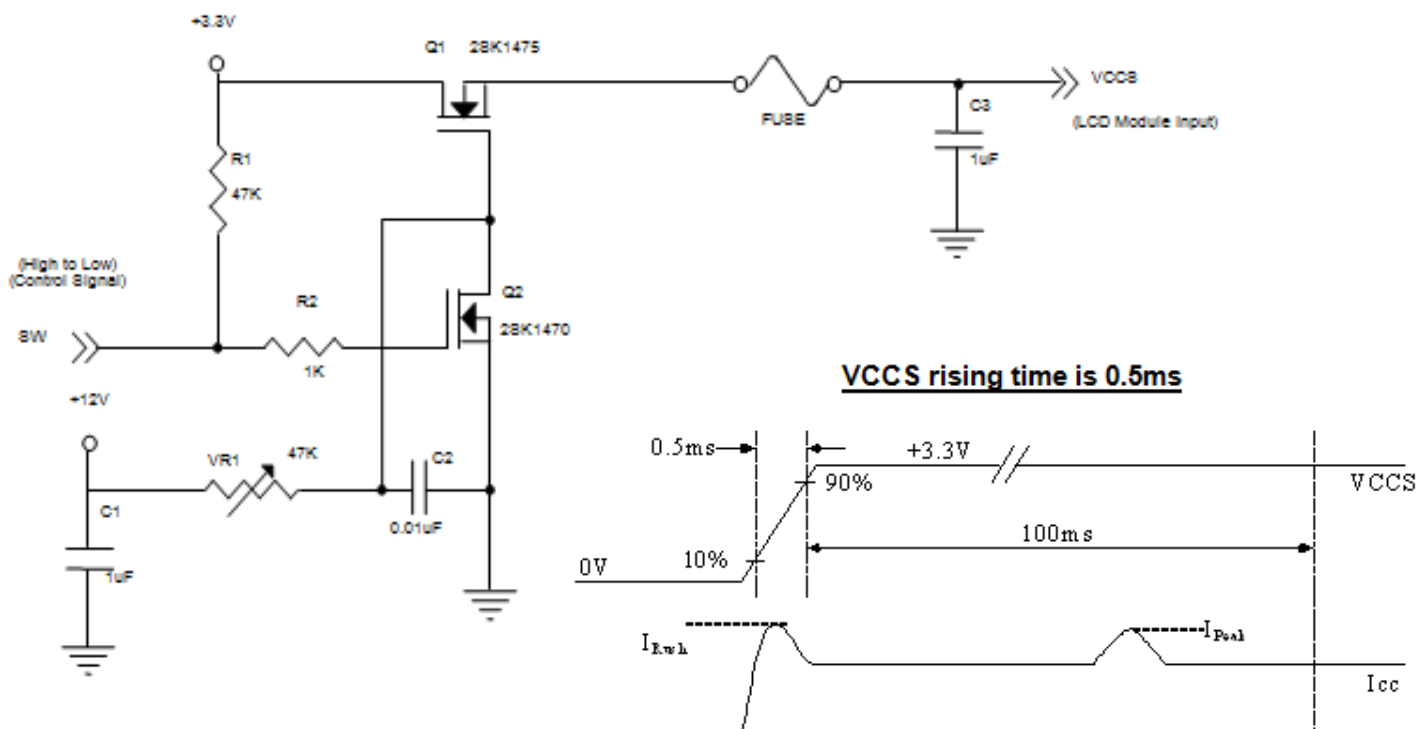
Symbol	Parameter	Min.	Typ.	Max.	Units	Note
VCCS	Power Supply Voltage	3.0	3.3	3.6	V	(1)
VRP	Ripple Voltage	-	50	-	mV	(1)
IRUSH	Inrush Current	-	-	1.5	A	(1),(2)
IPeak	Peak Current			1.5	A	(1),(2)
	Power Supply Current	Mosaic	198	237	mA	(3)a
		Black	176	211	mA	(3)
	HPD	High	2.25	-	V	(5)
		Low	0	-	V	(5)
RHPD	HPD Impedance	30K			ohm	(4)

Note (1), the ambient temperature is $T_a = 25 \pm 2^\circ\text{C}$.

Note (2), I_{RUSH} : the maximum current when VCCS is rising

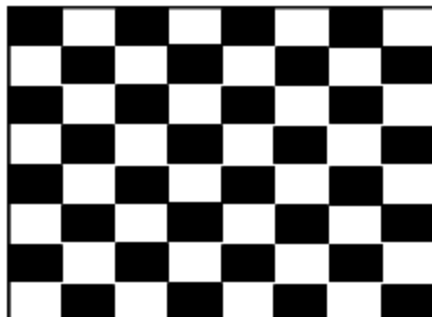
I_{IS} : the maximum current of the first 100ms after power-on

Measurement Conditions: Shown as the following figure. Test pattern: black.



Note (3), the specified power supply current is under the conditions at $V_{CCS} = 3.3\text{ V}$, $T_a = 25 \pm 2\text{ }^{\circ}\text{C}$, DC Current and $f_v = 60\text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. Mosaic Pattern



Active Area

Note (4), the specified signals have equivalent impedances pull down to ground in the LCD module respectively.

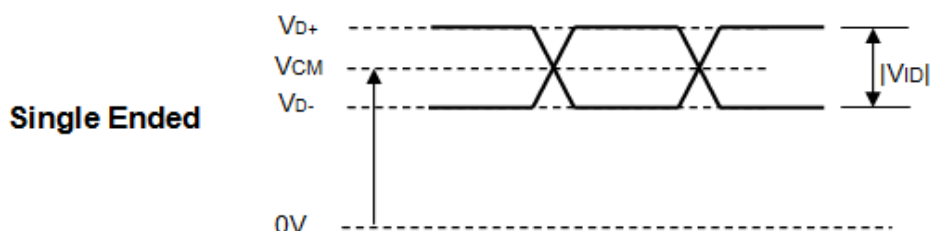
Customers should keep the input signal level requirement with the load of LCD modules. Please refer to 4.1.4 LED CONVERTER SPECIFICATION to obtain more information.

Note (5), when a source detects a low-going HPD pulse, it must be regarded as a HPD event. Thus, the source must read the link / sink status field or receiver capability field of the DPCD and take corrective action.

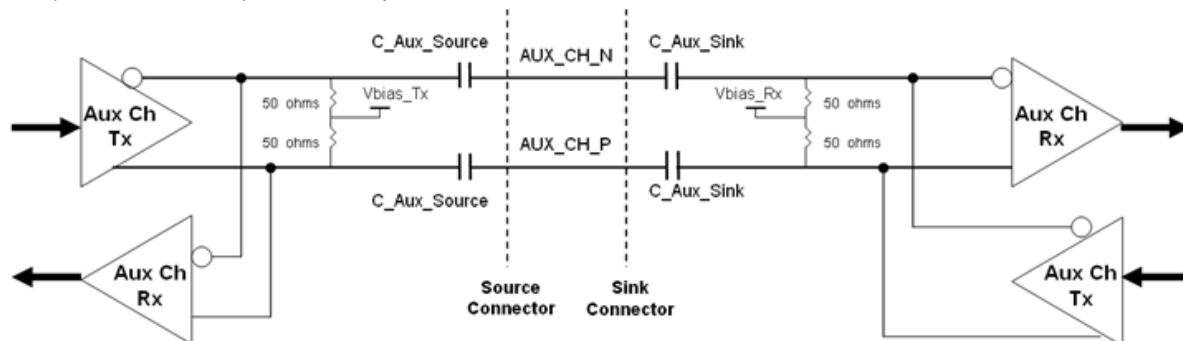
4.1.3. DISPLAY PORT INPUT SIGNAL ELECTRICAL SPECIFICATION

Symbol	Item	Min.	Typ.	Max.	Unit	Notes
VCM	Differential Signal Common Mode Voltage(Main Link and AUX)	0		2	V	(1)(4)
C_AUX_Source	AUX AC Coupling Capacitor	75		200	nF	(2)
C_ML_Source	Main Link AC Coupling Capacitor	75		200	nF	(3)
-	DPCD Version (address 00000h)	0x11h			-	(5)

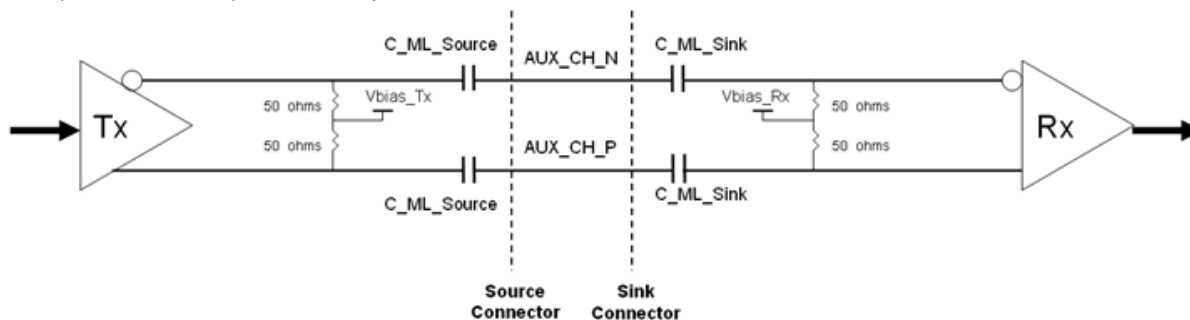
Note (1), Display port interface related AC coupled signals should follow VESA DisplayPort™ Standard Version1. Revision 1a and VESA Embedded DisplayPort™ Standard Version 1.2. There are many optional items described in eDP1.2. If some optional item is requested, please contact us.



Note (2) Recommended eDP AUX Channel topology is as below and the AUX AC Coupling Capacitor (C_AUX_Source) should be placed on the source device.



Note (3) Recommended Main Link Channel topology is as below and the Main Link AC Coupling Capacitor (C_ML_Source) should be placed on the source device.



Note (4) the source device should pass the test criteria described in Display Port Compliance Test Specification (CTS) 1.1

4.1.4. LED CONVERTER SPECIFICATION

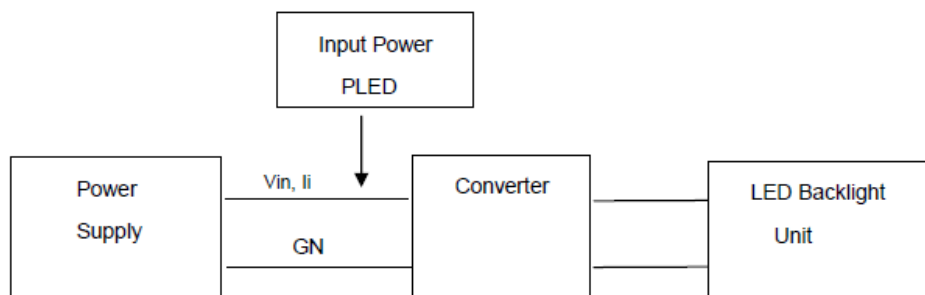
Parameter		Symbol	Min.	Typ.	Max.	Units	Remark
Converter Power Supply Voltage		LED_Vin	10.8	12	13.2	V	
Converter Power Supply Current		I _i	0.6	0.8	1.0	A	@LED_Vin=12V Duty=100%
Converter Input Rush Current		I _{irush}			2	A	@LED_Vin rising=1ms
Power Consumption		P _{CONVERTER}			9.6	W	@LED_Vin=12V Duty=100%
EN Control Level	Backlight ON	LED_EN	2.0	3.3	5.5	V	
	Backlight OFF		0	0	0.15		
PWM Control Lever	PWM High	LED_PWM	2.0	3.3	5.0	V	
	PWM Low		0	0	0.15		
PWM Input Frequency		FPWM	190	200	20K	Hz	
PWM Duty Ratio		Duty	10	-	100	%	
LED Light Bar Power Supply Voltage		VL	23.8	28.4	19.7	V	(1)(2) (Duty=100%)
LED Light Bar Power Supply Current		IL	-	47	-	mA	
LED Power Consumption		PL			8.63	W	(3)
LED Life-Time		L _L	50,000			Hrs	(4)

Note (1), LED light bar input voltage and current are measured by utilizing a true RMS multi-meter as shown below.

Note (2), for better LED light bar driving quality, it is recommended to utilize the adaptive boost converter with current balancing function to drive LED light-bar.

Note (3) $PL = IL \times VL$ (Without LED converter transfer efficiency)

Note (4) the life time of LED is estimated data and defined as the time when it continues to operate under the condition at $T_a = 25 \pm 2^\circ\text{C}$ and $IL = 20.5 \text{ mA}$ (Per EA) Duty 100% until the brightness becomes $\leq 50\%$ of its original value. Operating LED under high temperature environment will reduce life time and lead to color shift.

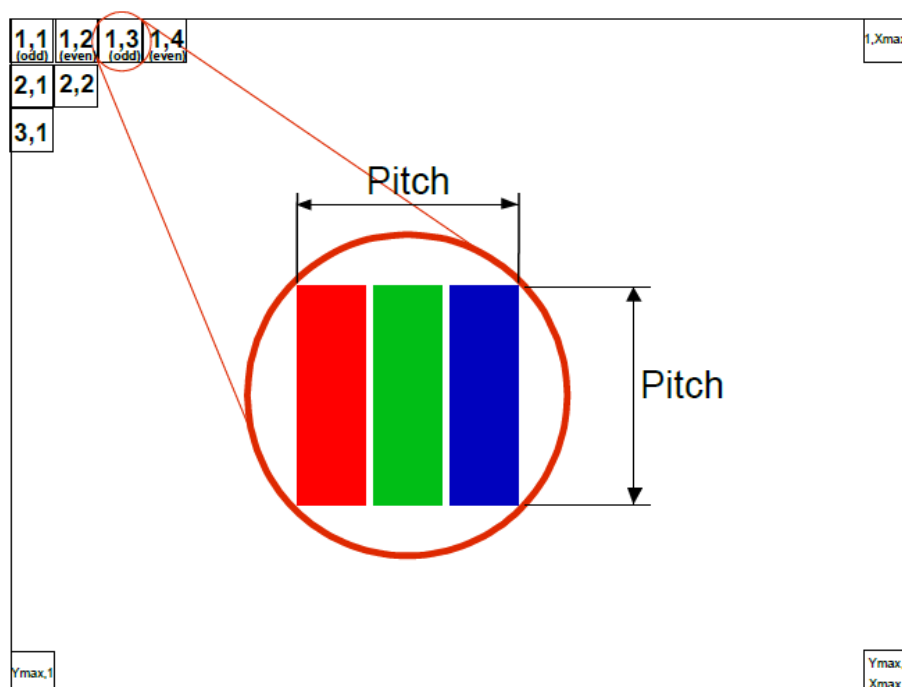


4.1.5. INTERFACE CONNECTIONS

Pin	Symbol	Description	Note
1	NC	No Connection (Reserved for LCD test)	
2	NC	No Connection (Reserved for LCD test)	
3	NC	No Connection (Reserved for LCD test)	
4	NC	No Connection (Reserved for LCD test)	
5	NC	No Connection (Reserved for LCD test)	
6	VDD	USB Power	
7	VDD	USB Power	
8	VSS	USB GND	
9	D+	USB D+	
10	D-	USB D-	
11	NC	No Connection (Reserved for LCD test)	
12	LED_VCCS	BL Power	
13	LED_VCCS	BL Power	
14	LED_VCCS	BL Power	
15	LED_VCCS	BL Power	
16	NC	No Connection (Reserved for LCD test)	
17	NC	No Connection (Reserved for LCD test)	
18	LED_PWM	PWM Dimming Control Signal of LED Converter	
19	LED_EN	BL Enable Signal of LED Converter	
20	BL_GND	BL Ground	
21	BL_GND	BL Ground	
22	BL_GND	BL Ground	
23	BL_GND	BL Ground	
24	HPD	Hot Plug Detet	
25	GND	Ground	
26	GND	Ground	
27	NC	No Connection (Reserved for LCD test)	
28	VCCS	Power Supply +3.3 V (Typical)	
29	VCCS	Power Supply +3.3 V (Typical)	
30	GND	Ground	
31	AUX-	Complement Signal-Auxiliary Channel	

32	AUX+	True Signal-Auxiliary Channel	
33	GND	Ground	
34	ML0+	True Signal-Main Lane 0	
35	ML0-	Complement Signal-Lane 0	
36	GND	Ground	
37	ML1+	True Signal-Main Lane 1	
38	ML1-	Complement Signal-Lane 1	
39	GND	Ground	
40	NC	No Connection (Reserved for LCD test)	

Note (1), the first pixel is odd as shown in the figure.



4.1.6. COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
	Green(253)	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
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	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

4.1.7. DISPLAY TIMING SPECIFICATION

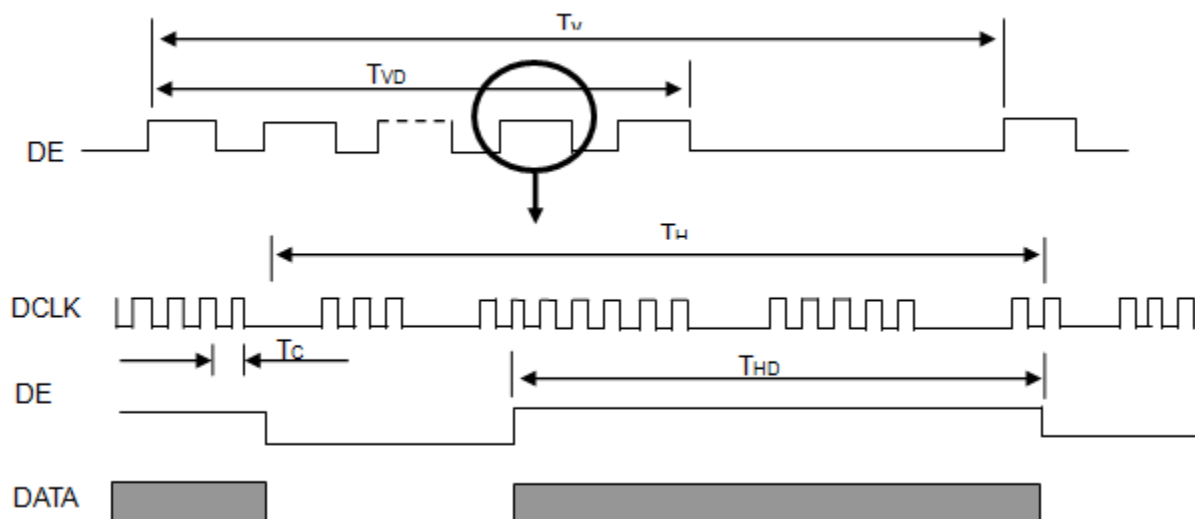
The input signal timing specifications are shown as the following table and timing diagram.

Refresh Rate 60Hz

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	Frequency	$1/T_C$	148.9	152.82	175	MHz	
DE	Frame rate	F_r	60	60	70	Hz	
	Vertical Total Time	T_V	1128	1132	1136	T_H	
	Vertical Active Display Period	T_{VD}	1080	1080	1080	T_H	
	Vertical Active Blanking Period	T_{VB}	48	52	56	T_H	
	Horizontal Total Time	T_H	2200	2250	2260	T_C	
	Horizontal Active Display Period	T_{HD}	1920	1920	1920	T_C	
	Horizontal Active Blanking Period	T_{HB}	280	330	340	T_C	

Note (1), the panel can operate at 60Hz normal mode and power saving mode, respectively. All reliability tests are based on specific timing of 60Hz refresh rate. We can only assure the panel's electrical function at power saving mode.

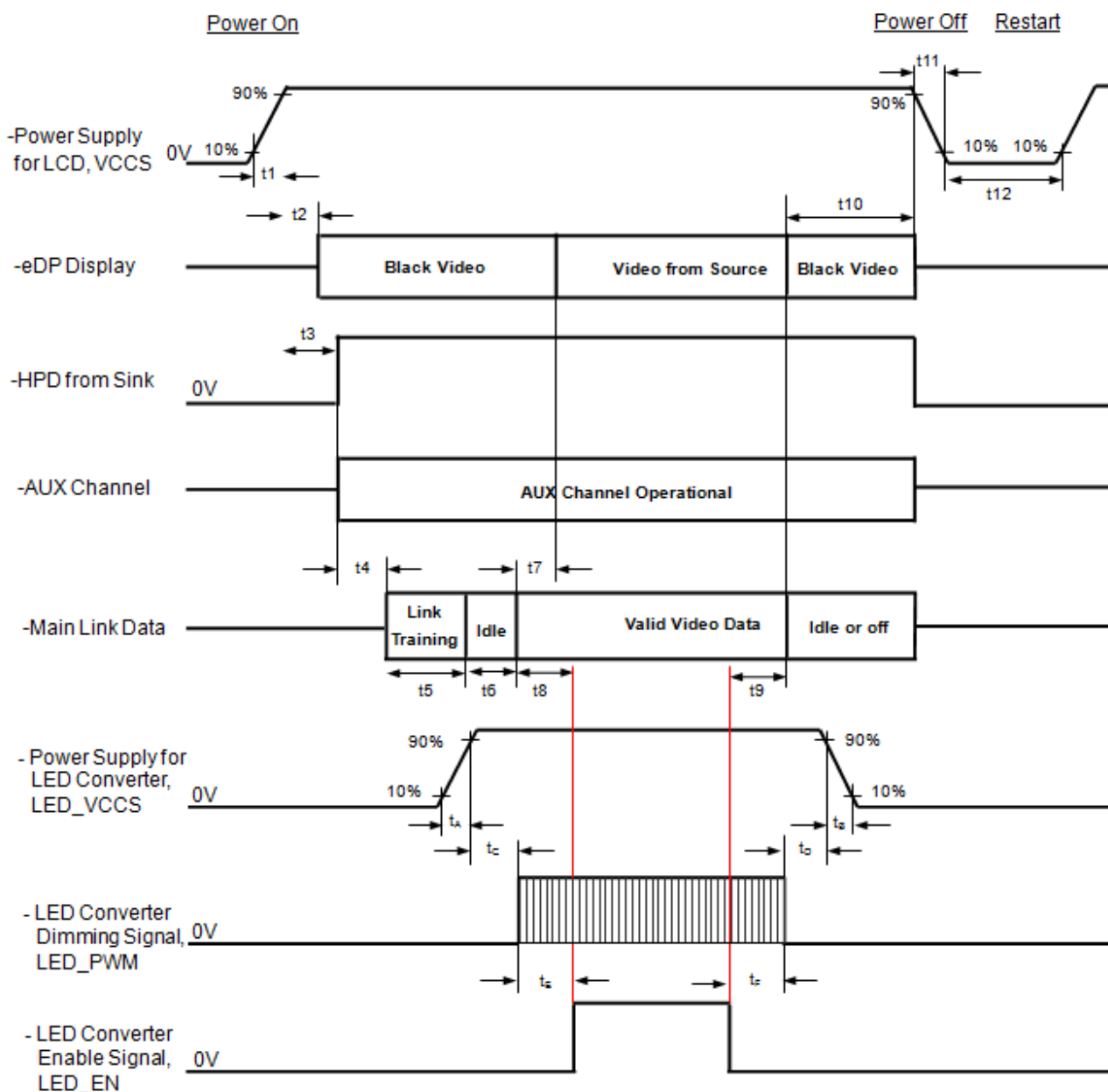
INPUT SIGNAL TIMING DIAGRAM



Note (2), the Input Signal must operate at eDP 2 lane.

Note (3), the $T_V (=T_{VD}+T_{VB})$ must be integer, otherwise, this module would operate abnormally

4.1.8. POWER SEQUENCE FOR LCD



Timing Specification

Parameter	Description	Reqd. By	Value		Unit	Notes
			Min	Max		
t1	Power rail rise time, 10% to 90%	Source	0.5	10	ms	
t2	Delay from LCD, VCCS to black video generation	Sink	0	200	ms	Automatic Black Video generation prevents display noise until valid video data is received from the Source (see Notes:2 and 3 below)



t3	Delay from LCD,VCCS to HPD high	Sink	0	200	ms	Sink AUX Channel must be operational upon HPD high (see Note:4 below)
t4	Delay from HPD high to link training initialization	Source	0	-	ms	Allows for Source to read Link capability and initialize
t5	Link training duration	Source	0	-	ms	Dependant on Source link training protocol
t6	Link idle	Source	0	-	ms	Min Accounts for required BS-Idle pattern. Max allows for Source frame synchronization
t7	Delay from valid video data from Source to video on display	Sink	0	50	ms	Max value allows for Sink to validate video data and timing. At the end of T7, Sink will indicate the detection of valid video data by setting the SINK_STATUS bit to logic 1 (DPCD 00205h, bit 0), and Sink will no longer generate automatic Black Video
t8	Delay from valid video data from Source to backlight on	Source	80	-	ms	Source must assure display video is stable *: Recommended by INX. To avoid garbage image.
t9	Delay from backlight off to end of valid video data	Source	50	-	ms	Source must assure backlight is no longer illuminated. At the end of T9, Sink will indicate the detection of no valid video data by setting the SINK_STATUS bit to logic 0 (DPCD 00205h, bit 0), and Sink will automatically display Black Video. (See Notes: 2 and 3 below) *: Recommended by INX. To avoid garbage image.
t10	Delay from end of valid video data from Source to power off	Source	0	500	ms	Black video will be displayed after receiving idle or off signals from Source
t11	VCCS power rail fall time, 90% to 10%	Source	0.5	10	ms	-
t12	VCCS Power off time	Source	500	-	ms	-
tA	LED power rail rise time, 10% to 90%	Source	0.5	10	ms	-
tB	LED power rail fall time, 90% to 10%	Source	0	10	ms	-
tC	Delay from LED power rising to LED dimming signal	Source	1	-	ms	-
tD	Delay from LED dimming signal to LED power falling	Source	1	-	ms	-



tE	Delay from LED dimming signal to LED enable signal	Source	0	-	ms	-
tF	Delay from LED enable signal to LED dimming signal	Source	0	-	ms	-

Note (1) Please don't plug or unplug the interface cable when system is turned on.

Note (2) The Sink must include the ability to automatically generate Black Video autonomously. The Sink must automatically enable Black Video under the following conditions:

- Upon LCDVCC power-on (within T2 max)
- When the "NoVideoStream_Flag" (VB-ID Bit 3) is received from the Source (at the end of T9)

Note (3) The Sink may implement the ability to disable the automatic Black Video function, as described in Note (2), above, for system development and debugging purposes.

Note (4) The Sink must support AUX Channel polling by the Source immediately following LCDVCC power-on without causing damage to the Sink device (the Source can re-try if the Sink is not ready). The Sink must be able to response to an AUX Channel transaction with the time specified within T3 max.

4.2. TOUCH PANEL

No.	Item	Specification
1	Touch Controller IC	EXC81W60
2	Number of Channel	RX 49 x TX 28
3	Number of Support	10 points Finger Touch
4	FPC Type / Interface	COF / USB & I ² C
5	OS	Android, Linux, Win7, Win8, Win10, XP

Interface	Symbol	Condition	Min.	Typ.	Max.	Unit
I2C	V _{DD}	-	-	3.3	-	V
USB				5		

4.2.1. TOUCH DRIVER PIN ASSIGNMENT

J1. I2C

Brand: JST

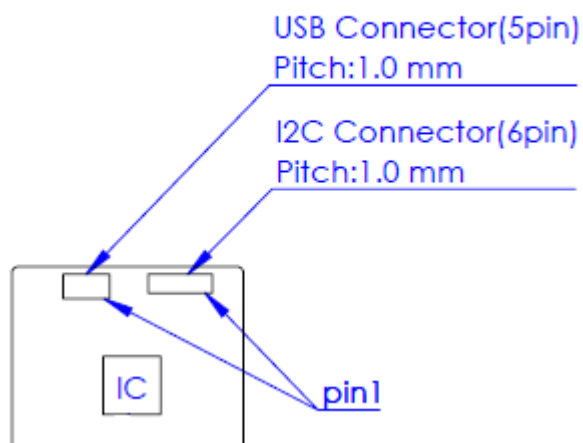
Part number: SM06B-SRSS-TB or equivalence

J2. USB

Brand: JST

Part number: SM05B-SRSS-TB or equivalence

J1 I2C	Pin Definition	J2 USB	Pin Definition
1	RST	1	GND
2	V _{DD} (3.3V)	2	V _{DD} (5V)
3	GND	3	GND
4	INT	4	D+
5	SDA	5	D-
6	SCL		

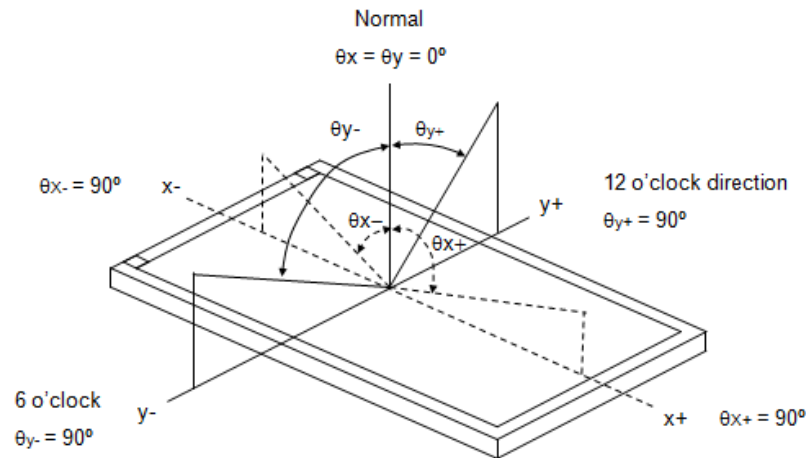


5. OPTICAL CHARACTERISTICS

The relative measurement methods of optical characteristics are shown as below and all items are measured at the center point of screen except white variation. The following items should be measured under the test conditions $T_a=25 \pm 1^\circ\text{C}$, $H_a=50 \pm 10\%RH$ and stable environment shown in Note (5).

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Note					
Color / Chromaticity (CIE 1931)	Red	R _x	θ _x =0°, θ _Y =0° CS-2000 R=G=B=255 Gray scale	Typ-0.03	0.648	Typ+0.03	-	(1),(5)					
		R _y			0.338								
	Green	G _x			0.313								
		G _y			0.600								
	Blue	B _x			0.153								
		B _y			0.050								
	White	W _x			0.305								
		W _y			0.340								
	Color Gamut				C.G				68	72	-	%	(7)
	Center Luminance of White				L _c				360	450	-	cd/m²	(4),(5)
Contrast Ratio		CR	700	1000	-		(2),(5)						
Response Time		T _R	θ _x =0°, θ _Y =0°	-	14	19	ms	(3)					
		T _F		-	11	16							
White Variation		W	θ _x =0°, θ _Y =0°	70	-	-	%	(5)(6)					
View Angle	Horizontal	degree		80	89	-	Deg.	(1),(5)					
				80	89	-							
	Vertical			80	89	-							
				80	89	-							

Note (1) Definition of View Angle (θ_x , θ_y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

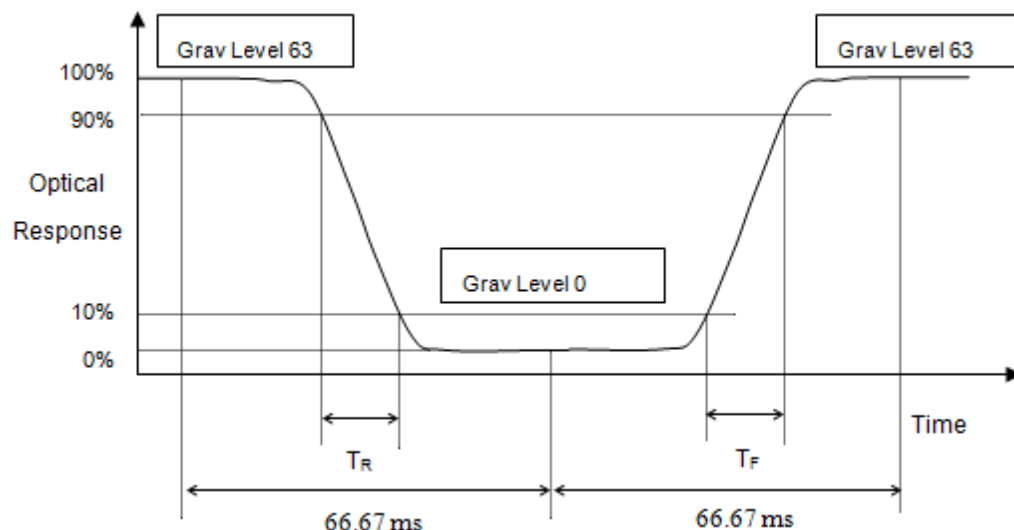
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

$$CR = CR(1)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R , T_F):



Note (4) Definition of Average Luminance of White (LAVE):

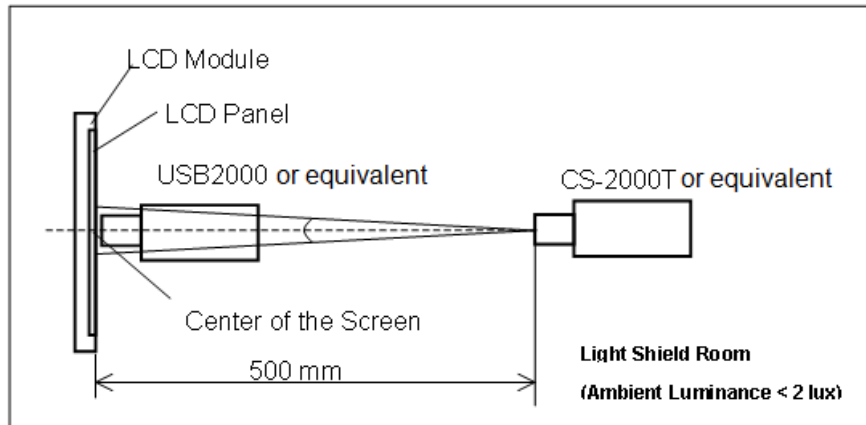
Measure the luminance of gray level 63 at 5 points

$$LAVE = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

L (x) is corresponding to the luminance of the point X at Figure in Note (6)

Note (5) Measurement Setup:

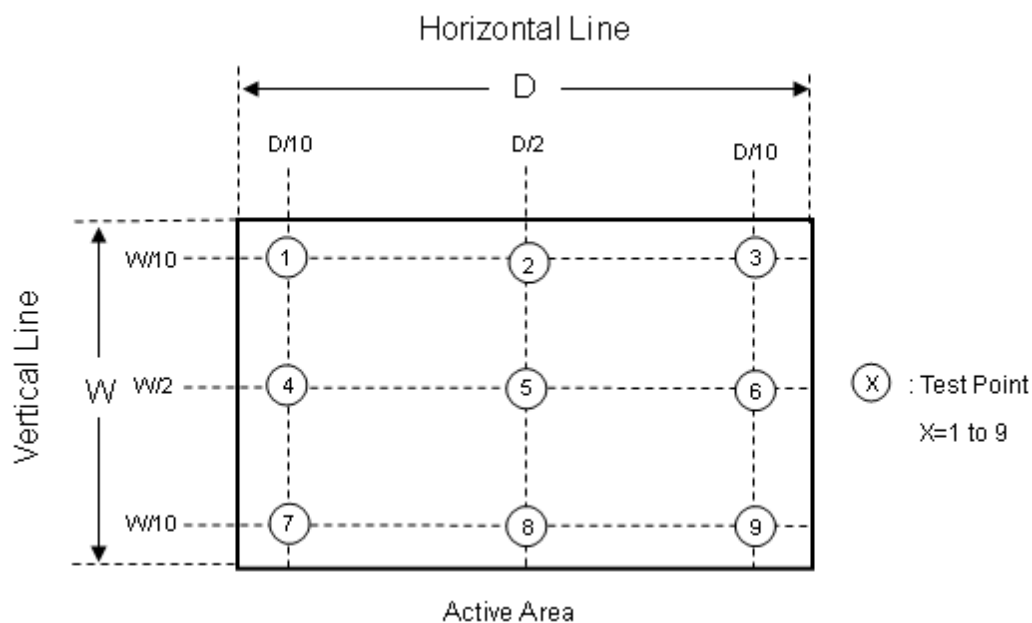
The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 63 at 9 points

$$\delta W = (\text{Minimum } [L(1) \sim L(9)] / \text{Maximum } [L(1) \sim L(9)]) * 100\%$$



Note (7) Definition of color gamut (C.G %):

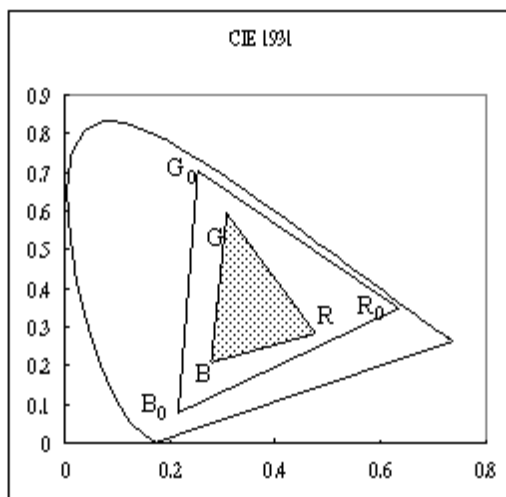
$$C.G \% = R G B / R_0 G_0 B_0 * 100\%$$

R_0, G_0, B_0 : The color coordinates of red, green, and blue defined by NTSC, respectively.

R, G, B : The color coordinates of module on 63 gray levels of red, green, and blue, respectively.

$R_0 G_0 B_0$: The area of triangle defined by R_0, G_0, B_0

R G B: area of triangle



defined by R, G, B

6. RELIABILITY TEST ITEMS

Test Item		Condition
1	High temperature/Humidity Bias	50°C,80% 240hrs
2	High temperature storage test	70°C, 240hrs
3	Low temperature storage test	-20°C, 240hrs
4	High temperature operation test	60°C, 240hrs
5	Low temperature operation test	0°C, 240hrs
6	Thermal Cycle storage test	-20°C(30min)~70°C(30min), 10cycles



Product Specification

F156PET-T42

7. LABEL

Model NO. F156PET-T42

條碼

L=32mm

SN 3A Z 20 2 21 0 XXX1

code 1,2 Production location

code 3 Production version

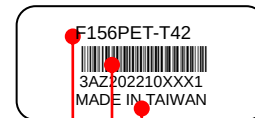
code 4,5 Production year

code 6 Production month

code 7,8 Production day

code 9 Production code

code 10,11,12,13 Serial number



W=14mm

字型:Arial (6)

code:128

字型:Arial (8)



8. MECHANICAL CHARACTERISTICS

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	375.24	mm	PCAP : B156PBU-E15
	Vertical	240.68	mm	
	Depth (Typ.)	2.00	mm	
	Horizontal	351.84	mm	LCD : G156HCE-E01
	Vertical	208.88	mm	
	Depth (Max.)	6.5	mm	
Weight		970	g	F156PET-T42
Item		Specification		Remarks
PCAP Viewing Area		345.24 mm x 194.68 mm		
PCAP Sensor Area		358.24 mm x 211.94 mm		
PCAP FPC Length		55.31 mm		
PCAP Input method		Finger or exclusive pen		
PCAP Light Transmission		≥85%		
PCAP Operation force		≤10g		Finger Input
PCAP Hardness of surface		≥7H		Pressure 500gf test
PCAP Surface Treatment		Clear Type		



Front View

TBD

Back View

TBD

Product Specification

F156PET-T42

9. ENGINEERING DRAWING

