



- ☐ Draft Specifications
- ☒ Preliminary Specifications
- ☐ Final Specifications

Title	17.0-inches Touch Display Product Specifications
Model Name	F170PET-T21
Version	1

Customer

Approved by

Date

Notice: This Specification is subject to change without notice.

Approved By	Prepared By
ZOE NIEN	ALLEN FANG
2025-12-29	2025-12-29



REVISION HISTORY

Version	ECN No.	Description of Changes	Date	Prepared
0		Preliminary specification first edition	2023-06-11	Allen Fang
1		Modified Label	2025-12-29	Allen Fang



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1. GENERAL DESCRIPTION

1.1.INTRODUCTION

F170PET-T21 is a 17.0" color active matrix TFT LCD module M170ETN01.1 (ES8.0 from 10D) with projected capacitive multi-touch B170PBU-E06 air bonding, with touch Driver ETP-MB-EXC84H4254STAG.

The display format is intended to support the SXGA+ mode (1280(H) x 1024(V)) screen and 16.7M colors (8 bits RGB data input).The input interface is Dual channel LVDS and this module doesn't contain a driver board for backlight.

1.2.FEATURES

- SXGA+ (1280x1024) resolution
- LVDS interface
- 8 bits color depth, display 16.7M colors
- DE (Data Enable) only
- RoHS product

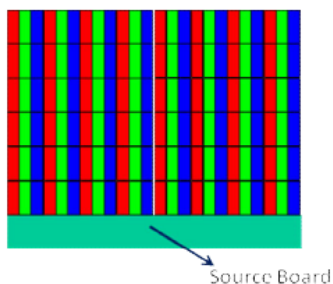
1.3.APPLICATION

- TFT LCD monitor

2. GENERAL SPECIFICATIONS

Parameter	Specification	Unit	Remarks
Active Area Size	17.0	inch	
Display Area	337.92 (H) x 270.34 (V)	mm	
Outline Dimension	358.50 (H) x 296.50 (V) x 13.64 (D)	mm	D:PCAP+LCM
Weight	1960	g	Typ.
Driver Element	a-Si TFT active matrix	-	
Display Colors	16,7M (RGB 8-bits)	Colors	
Electrical Interface	2-ch LVDS	-	
Number of Pixels	1280 (H) x 1024 (V)	pixels	
Pixel Pitch	0.264 (H) x 0.264(W)	mm	
Pixel Arrangement	RGB vertical stripe	-	Note 1
Display Mode	Normally White	-	
Color Gamut	72	%	
Touch Technology	Projected Capacitive Multi-Touch Panel	-	
Touch Method	Finger or exclusive pen	-	
Numbers of Touch	10	Points	
Touch Driver	ETP-MB-EXC84H4254STAG: USB	-	
Touch Sensor Glass Type	Strengthening Glass	-	
Touch Controller IC	EETI, EXC80H84	-	
Touch Driver Interface	USB 2.0	-	
Support OS	Windows (Embedded/CE/XP/2000/Vista/7/8/10) / Linux / Android / MAC	-	

Note 1: The following shows the figure of pixel arrangement



3. ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. (Ta=25°C)

Parameter	Symbol	Min.	Max.	Unit
Power Supply Input Voltage	V _{DD}	-0.3	+6.0	V
Operating Temperature	T _{op}	0	+50	°C
Operating Humidity	H _{op}	10	90	%RH
Storage Temperature	T _{st}	-20	+60	°C
Storage Humidity	H _{st}	10	90	%RH
Touch DC Supply Voltage	USB	-0.3	6.0	V

Note:

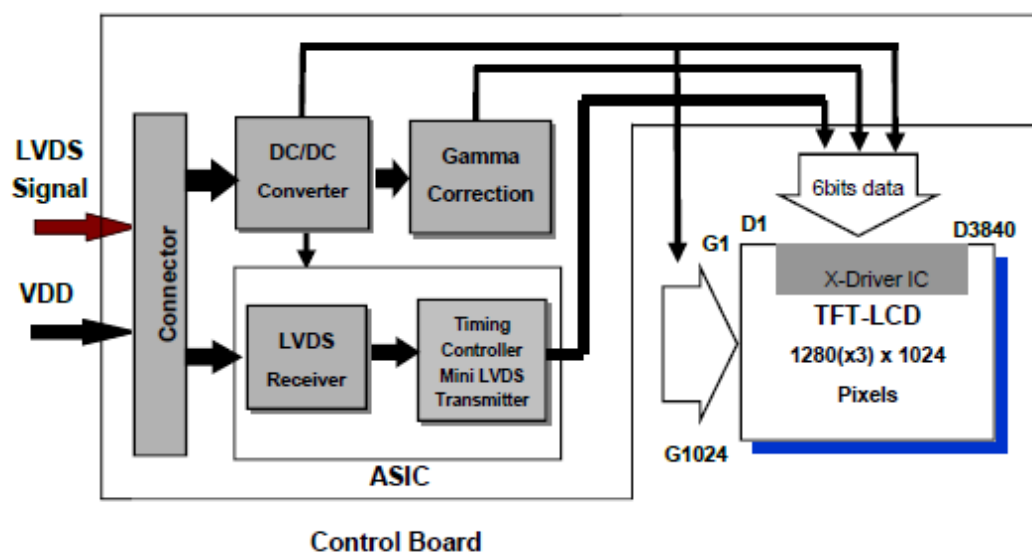
1. Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.
2. Maximum Wet-Bulb temperature should be 39°C and no condensation. The relative humidity must not exceed 90% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C.

4. ELECTRICAL CHARACTERISTICS

4.1.LCD MODULE

4.1.1. LCD BLOCK DIAGRAM

The following diagram shows the function block of the 17.0 inches wide Color TFT/LCD module.



4.1.2. POWER SPECIFICATION

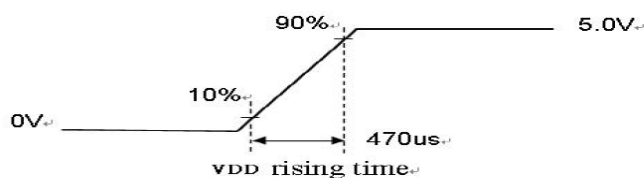
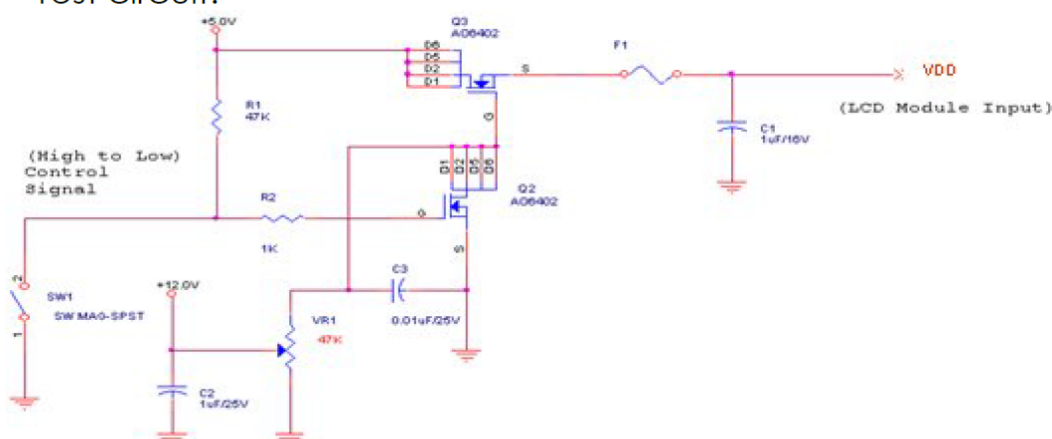
LCD Input power specifications are as below;

The power specifications are measured under 25°C and frame frequency under 60Hz.

Symbol	Parameter	Min.	Typ.	Max.	Units	Remark
VDD	Power Supply Input Voltage	4.5	5.0	5.5	V	
IDD	Power Supply Input Current (RMS)	-	0.6	0.72	A	VDD=5.0V,Black Pattern, Fv=60Hz
			0.72	0.87	A	VDD=5.0V,Black Pattern, Fv=75Hz
PDD	VDD Power Consumption	-	3	3.6	W	VDD=5.0V,Black Pattern, Fv=60Hz
			3.6	4.32	W	VDD=5.0V,Black Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	A	Note 1
VDDrp	Allowable VDD Ripple Voltage	-	-	500	mV	VDD=5.0V,Black Pattern, Fv=75Hz

Note 1:

Test circuit:



The duration of VDD rising time: 470us.

4.1.3. LVDS SPECIFICATION

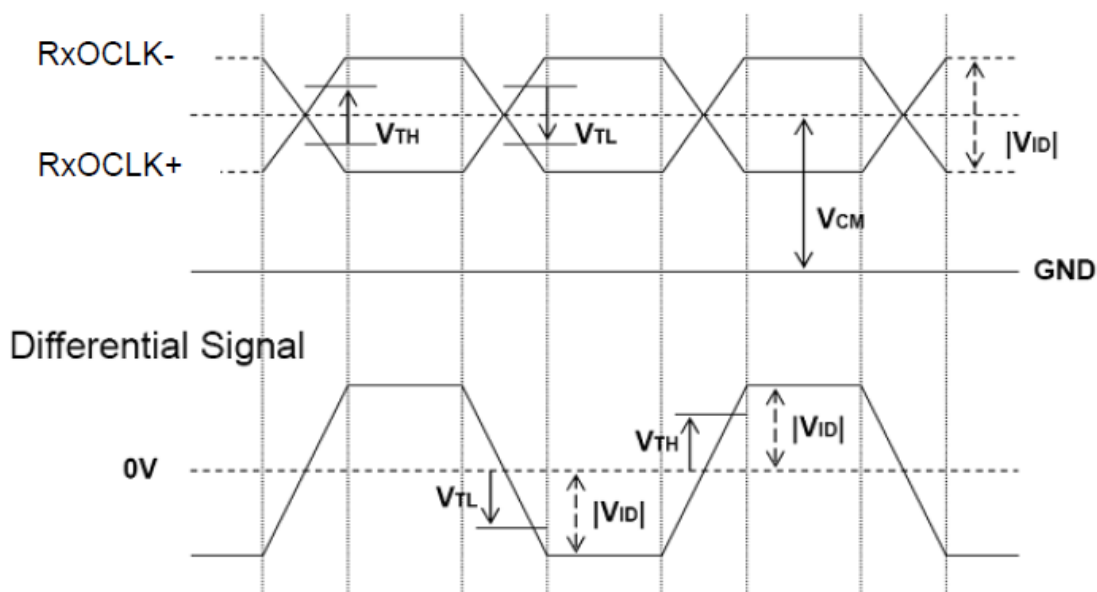
a. DC CHARACTERISTICS

Symbol	Item	Min.	Typ.	Max.	Unit	Remark
V_{TH}	Differential Input High Threshold			+100	mV	$V_{CM}=1.2V$
V_{TL}	Differential Input Low Threshold	-100			mV	$V_{CM}=1.2V$
$ V_{ID} $	Input Differential Voltage	100		600	mV	
V_{CM}	Differential Input Common Mode Voltage	1.0	1.2	1.5	V	$V_{TH}/V_{TL}=200mV$

LVDS Signal Waveform:

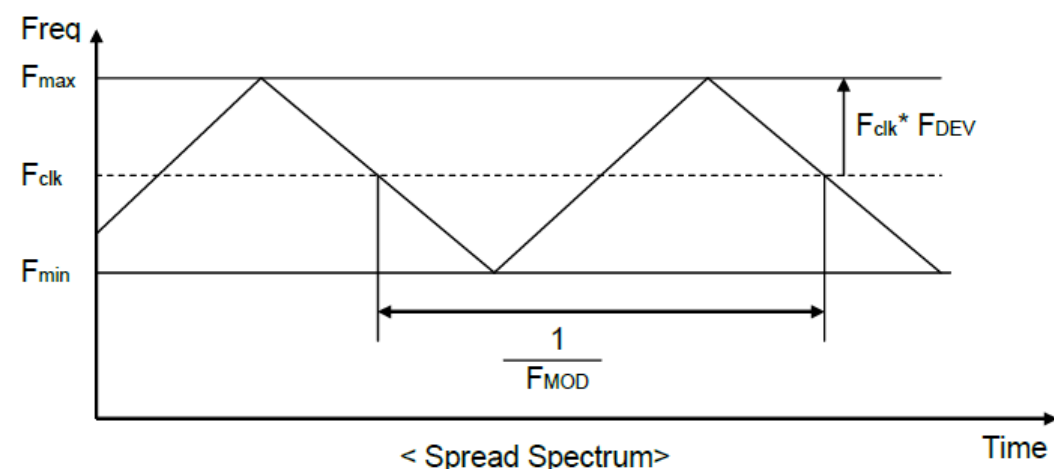
Use RxOCLK- & RxOCLK+ as example.

Single-End



b. AC CHARACTERISTICS

Symbol	Description	Min.	Max.	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	+3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



F_{clk} : LVDS Clock Frequency

4.1.4. LCD BACKLIGHT UNIT

Absolute Maximum Rating

Permanent damaged may occur if exceeding the following maximum rating. (Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
I_S	LED String Current	0	120	mA	100% duty ratio

Recommended Operating Condition

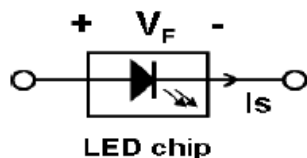
(Ta=25°C)

Symbol	Parameter	Min.	Typ.	Max.	Units	Remark
I _S	LED String Current	-	45	49.5	mA	100% duty ratio of LED chip (Note 6)
V _S	LED String Voltage	23.0	24.8	26.6	V	I _S =45mA@100% duty ratio (Note1, 5, 7)
ΔV _S	Maximum V _S Voltage Deviation of light bar	-	-	1.8	V	I _S =45mA@100% duty ratio (Note 2)
P _{BLU}	LED Light Bar Power Consumption	-	4.5	4.8	W	(Note 3)
LT _{LED}	LED Life Time	30,000			Hour	(Note 4)
OVP	Over Voltage Protection in system board	110% V _S (Max.)			V	(Note 5)
I _F P	LED Pulse Forward Current		180		mA	Duty ≤ 10% Pulse time within 10ms
			142.5		mA	Duty ≤ 24% Pulse time within 10ms
			135		mA	Duty ≤ 30% Pulse time within 10ms
			86.25		mA	Duty ≤ 75% Pulse time within 10ms

Note 1: V_S (Typ.) = V_F (Typ.) x LED No. (One string);

a. V_F: LED chip forward voltage, V_F (Min.) = 2.56 V, V_F (Typ.) = 2.76 V, V_F (Max.) = 2.96 V

b. The same equation to calculate V_S (Min.) & V_S (Max.) for respective V_F (Min.) & V_F (Max.)



Note 2: ΔV_S (Max.) = ΔV_F X LED No. (One string);

a. ΔV_F: LED chip forward voltage deviation; (0.2V, each Bin of LED V_F)

Note 3: P_{BLU} (Typ.) = V_S (Typ.) X I_S (Typ.) x 4; (4 is total String No. of LED Light bar)

P_{BLU} (Max.) = V_S (Max.) X I_S (Typ.) x 4;

Note 4: Definition of life time:

a. Brightness of LED becomes to 50% of its original value

b. Test Condition: I_S = 45mA and Ta=25°C (Room Temperature)

Note 5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than maximum value of LED string voltage (V_S) at least.

Note 6: AUO strongly recommended “Analog Dimming” method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

Note 7: Ensure that the LED light bar is not subjected either forward or reverse voltage while monitor set is on standby mode or not in use.

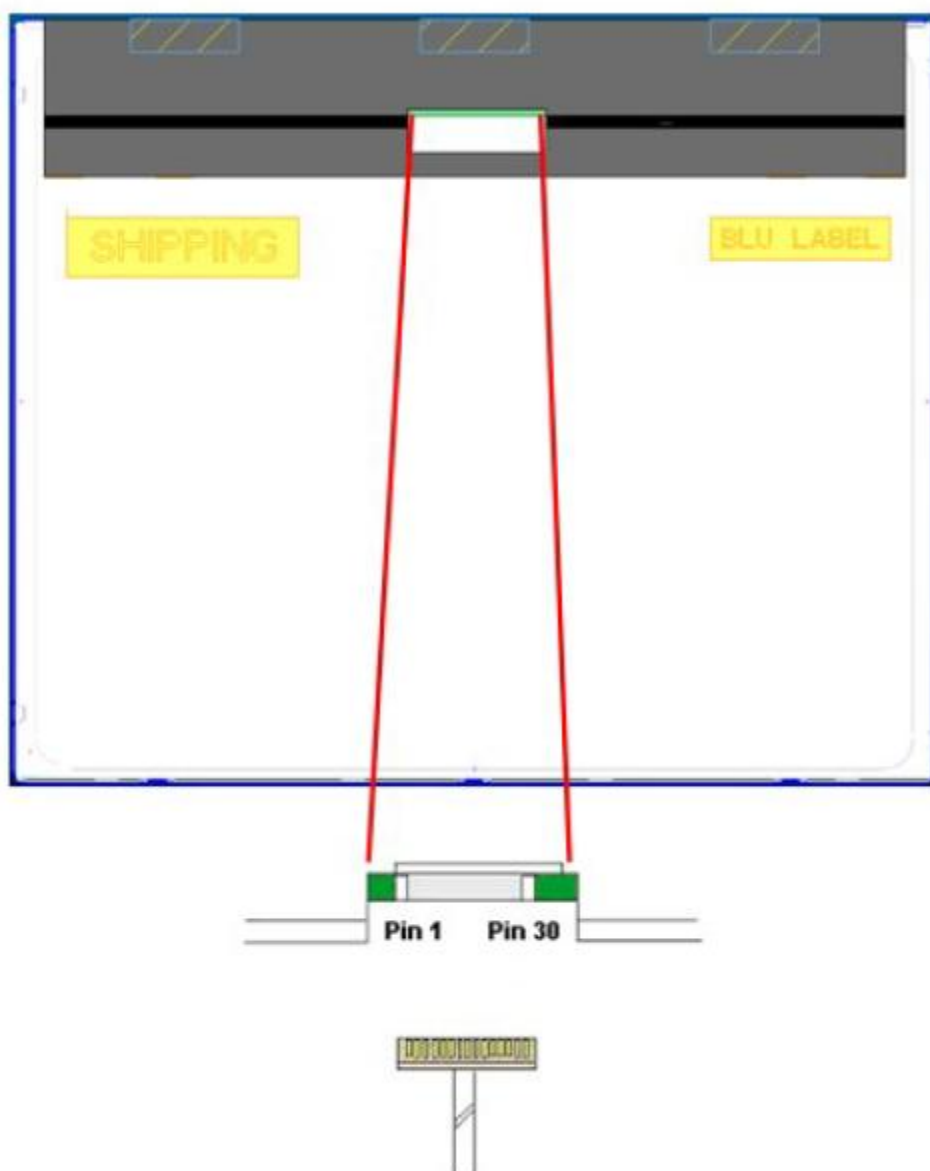
4.1.5. LCD INPUT TERMINAL PIN ASSIGNMENT

Connector Pin Assignment

Pin	Symbol	Description	Note
1	RXO0-	Negative LVDS differential data input (Odd data)	
2	RXO0+	Positive LVDS differential data input (Odd data)	
3	RXO1-	Negative LVDS differential data input (Odd data)	
4	RXO1+	Positive LVDS differential data input (Odd data)	
5	RXO2-	Negative LVDS differential data input (Odd data)	
6	RXO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RXOCLK-	Negative LVDS differential clock input (Odd data)	
9	RXOCLK+	Positive LVDS differential clock input (Odd data)	
10	RXO3-	Negative LVDS differential data input (Odd data)	
11	RXO3+	Positive LVDS differential data input (Odd data)	
12	RXE0-	Negative LVDS differential data input (Even data)	
13	RXE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RXE1-	Negative LVDS differential data input (Even data)	
16	RXE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RXE2-	Negative LVDS differential data input (Even data)	
19	RXE2+	Positive LVDS differential data input (Even data)	
20	RXECLK-	Negative LVDS differential clock input (Even data)	
21	RXECLK+	Positive LVDS differential clock input (Even data)	
22	RXE3-	Negative LVDS differential data input (Even data)	
23	RXE3+	Positive LVDS differential data input (Even data)	
24	GND	Must connect to ground	
25	NC	No connection (for AUO test only, Do not connect)	
26	NC	No connection (for AUO test only, Do not connect)	
27	NC	No connection (for AUO test only, Do not connect)	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	

Connector type

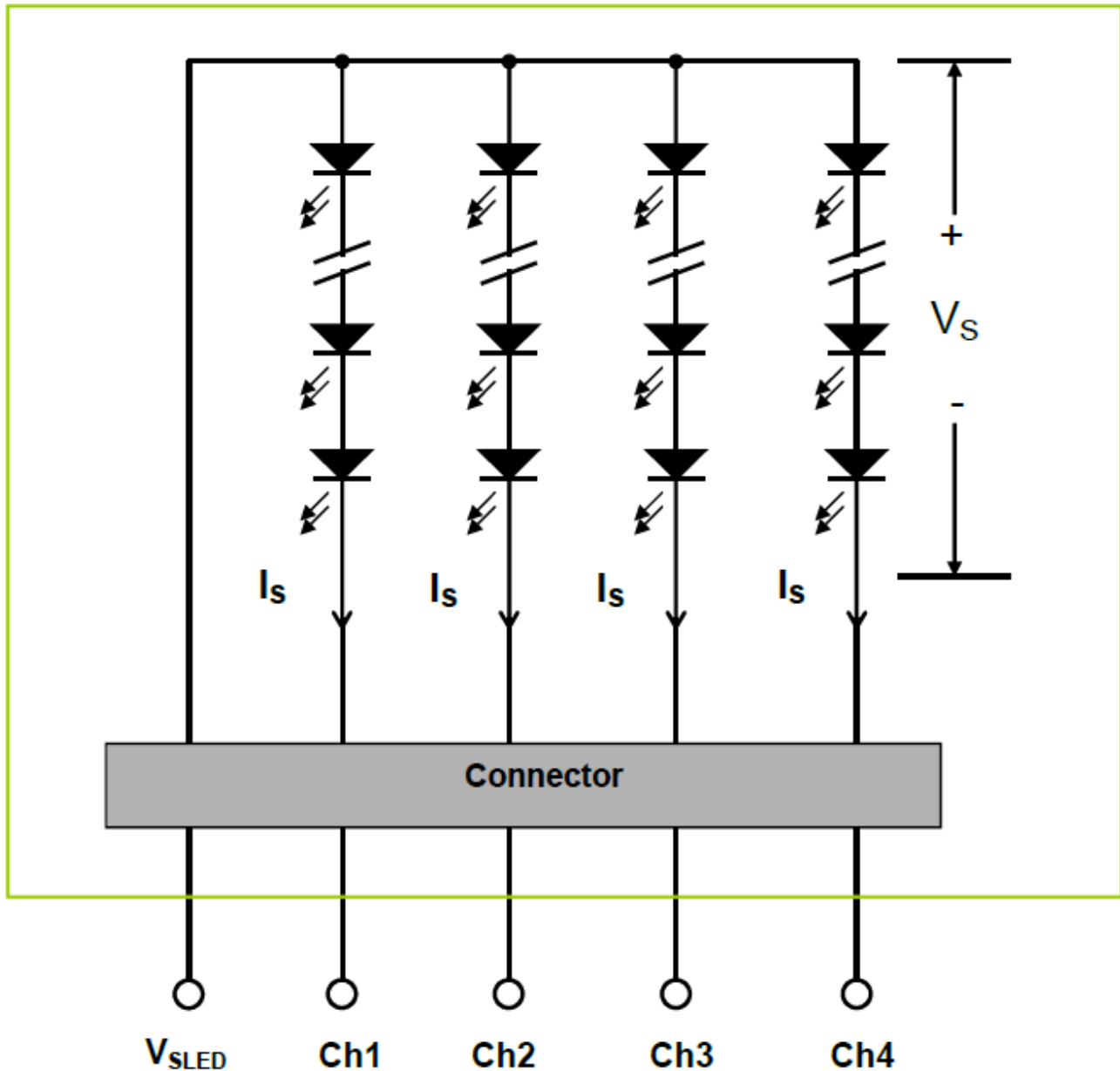
TFT-LCD Connector	Manufacturer	P-TWO	STM
	Part Number	AL230F-A0G1D-P	MSCKT2407P30HB
Mating Connector	Manufacturer	JAE	
	Part Number	FI-X30HL (Locked Type)	



4.1.6. LED LIGHT BAR

Block Diagram :

The following shows the block diagram of the 17inch Backlight Unit, and it includes 36 pcs LEDs in the LED light bar. (4 strings and 9 pcs LEDs of one string)

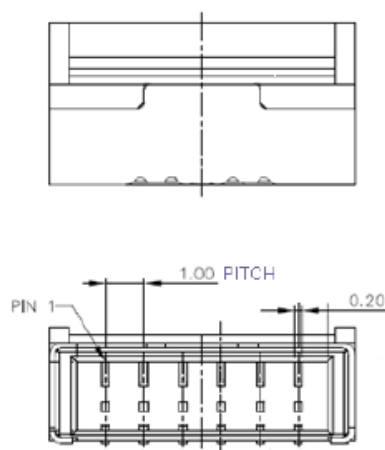


Interface Connection :

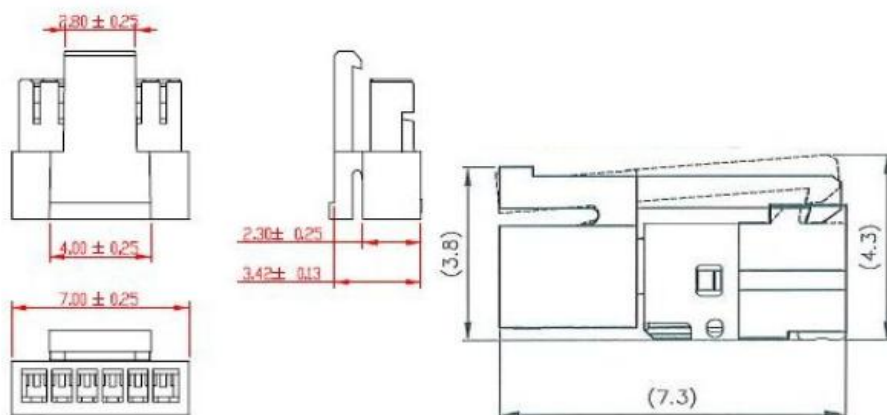
Backlight Connector	Manufacturer	ENTERY
	Part Number	3707K-S06N-21R
Mating Connector	Manufacturer	ENTREY
	Part Number	H112K-P06N-13B (Locking type)

Backlight Connector dimension:

$$H \times V \times D = 13.9 \times 3.00 \times 4.25, \text{Pitch} = 1.0 (\text{unit} = \text{mm})$$

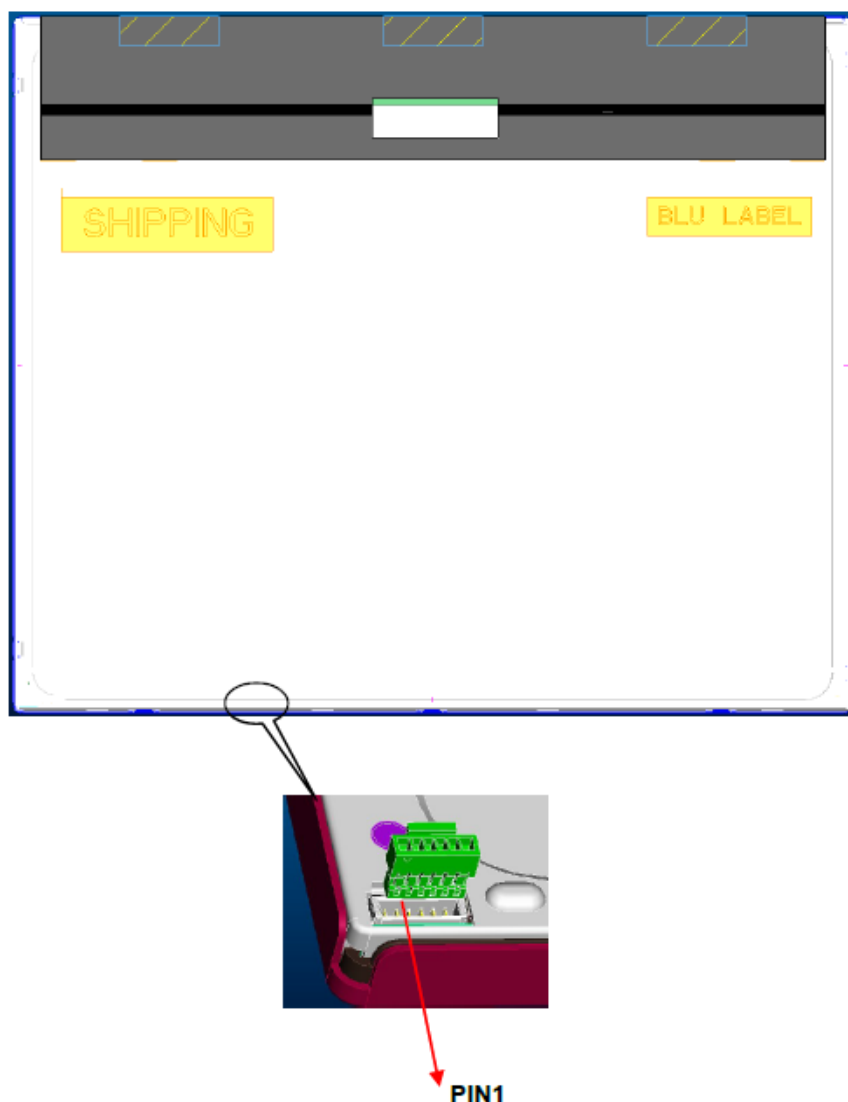


Mating Connector dimension:



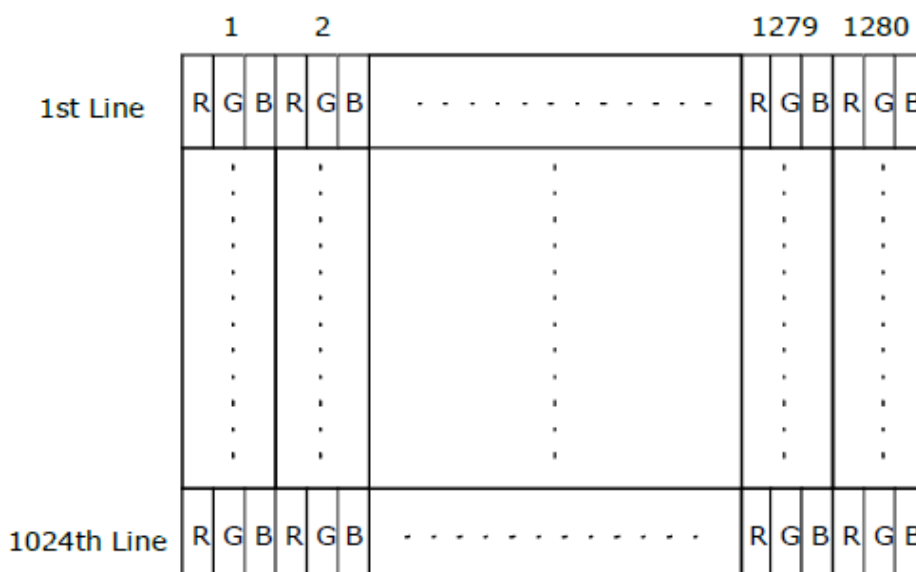
LED LIGHT BAR Connector Pin Assignment

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	V _{SLED}	LED Power Supply Voltage Input Terminal	
4	V _{SLED}	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	

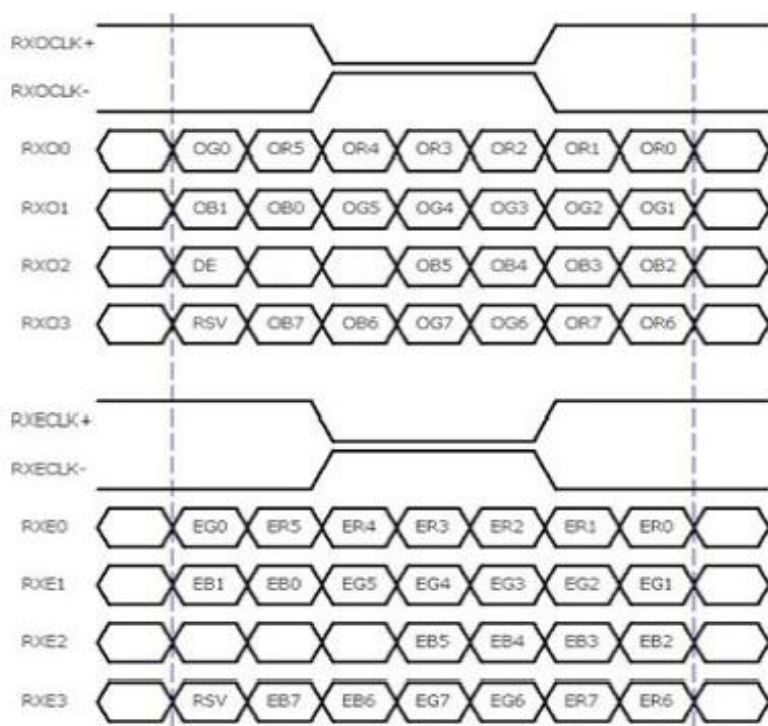


4.1.7. PIXEL FORMAT IMAGE

Following figure shows the relationship of the input signals and LCD pixel format.



4.1.8. THE LCD INPUT DATA FORMAT



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note :

1: O ="Odd Pixel Data" E ="Even Pixel Data"

2: Refer to LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1280 (Even Pixel Data).

4.1.9. COLOR VERSUS INPUT DATA

The following table is for color versus input data (8bit).

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB :R7, LSB :R0)								GREEN data (MSB :G7, LSB :G0)								BLUE data (MSB :B7, LSB :B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0		
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1		

4.1.10. LCD TIMING CHARACTERISTICS

It only support DE mode, and the input timing are shown as the following table.

Parameter		Symbol	Min.	Typ.	Max.	Unit
Vertical Section	Period	T _V	1036	1066	1873	T _{LINE}
	Active	T _{VD}	1024			
	Blanking	T _{VB}	12	42	849	
	Frequency	F _V	50	60	76	Hz
Horizontal Section	Period	T _H	730	844	1320	T _{CLOCK}
	Active	T _{HD}	640			
	Blanking	T _{HB}	90	204	680	
	Frequency	F _H	51.8	64	93.7	KHz
LVDS Clock		T _{CLK}	14.6	18.5	26	ns
		F _{CLK}	37.8	54	68.4	MHz

Note 1: The equation is listed as following. Please don't exceed the above recommended value.

$$F_H (\text{Min.}) = F_{CLK} (\text{Min.}) / T_H (\text{Min.});$$

$$F_H (\text{Typ.}) = F_{CLK} (\text{Typ.}) / T_H (\text{Typ.});$$

$$F_H (\text{Max.}) = F_{CLK} (\text{Max.}) / T_H (\text{Min.});$$

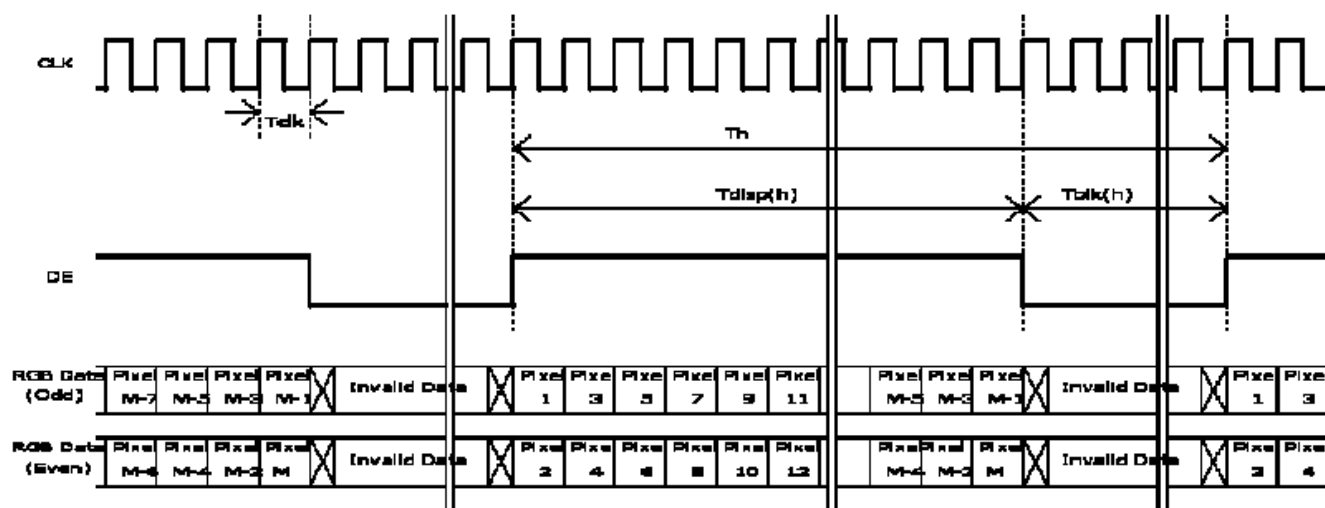
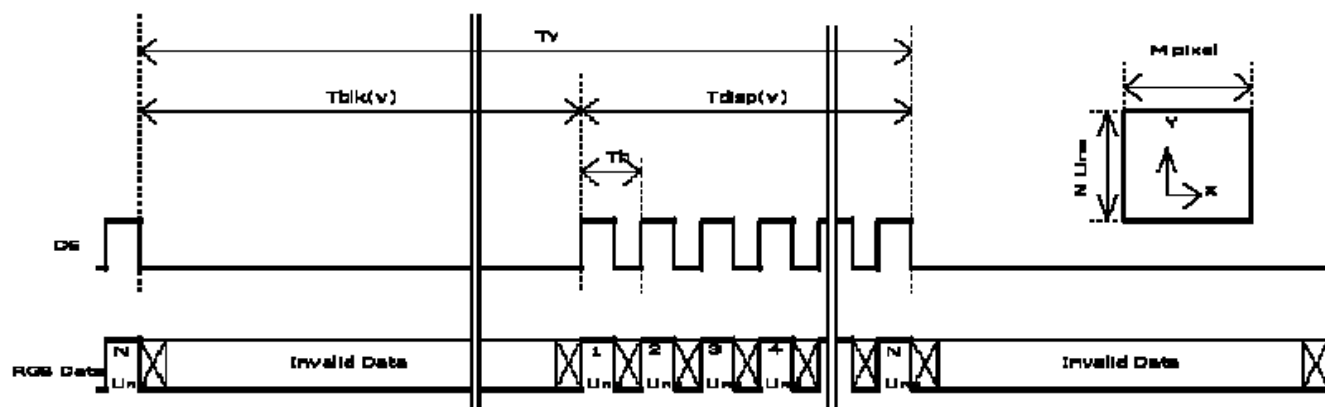
Note 2: The equation is listed as following. Please don't exceed the above recommended value.

$$F_{CLK} (\text{Min.}) = F_V (\text{Min.}) \times T_H (\text{Min.}) \times T_V (\text{Min.});$$

$$F_{CLK} (\text{Typ.}) = F_V (\text{Typ.}) \times T_H (\text{Typ.}) \times T_V (\text{Typ.});$$

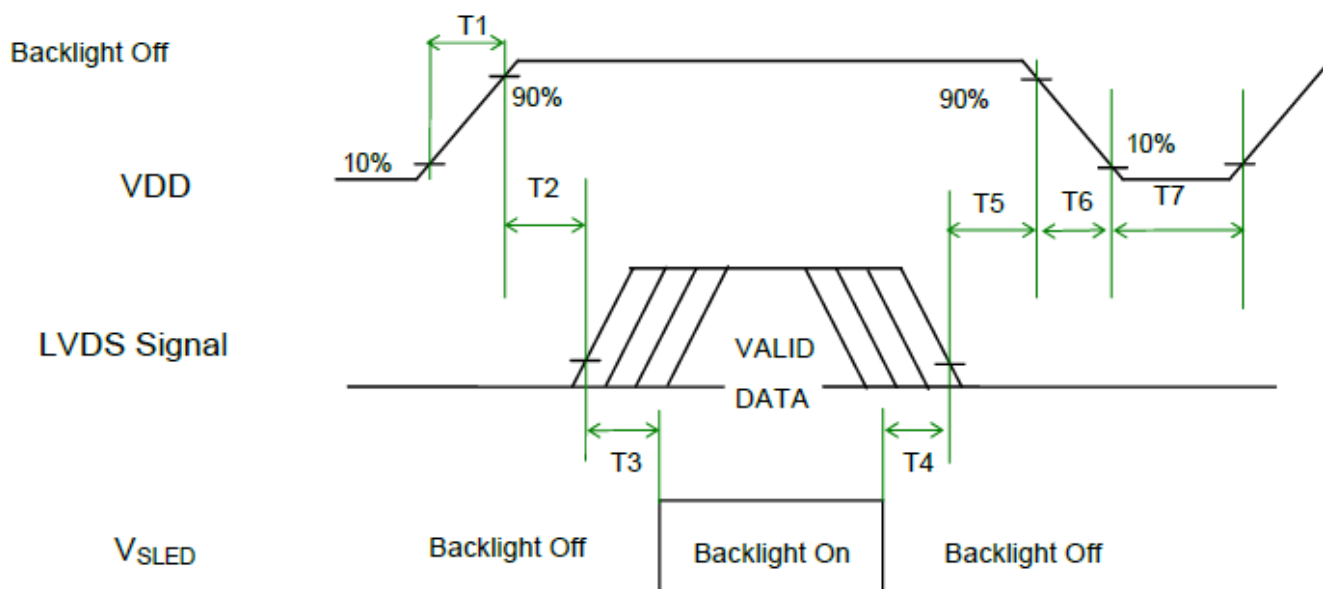
$$F_{CLK} (\text{Min.}) = F_V (\text{Max.}) \times T_H (\text{Typ.}) \times T_V (\text{Typ.});$$

4.1.11. LCD INPUT TIMING DIAGRAM



4.1.12. POWER SEQUENCE FOR LCD

VDD power, LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Parameter	Value			Unit	Note
	Min.	Typ.	Max.		
T1	0.5	-	10	ms	
T2	0	-	50	ms	
T3	500	-	-	ms	
T4	100	-	-	ms	1
T5	0	-	50	ms	2, 3
T6	0	-	150	ms	
T7	1000	-	-	ms	

Note:

1. Recommend setting T5=0ms to avoid electronic noise when VDD is off.
2. During T5 period, please keep the level of input LVDS signals with Hi-Z state.
3. If T4<100ms, there will be no reliability concern, but the display may momentarily show abnormal screen.

4.2. TOUCH PANEL

4.2.1. TOUCH PIN ASSIGNMENT

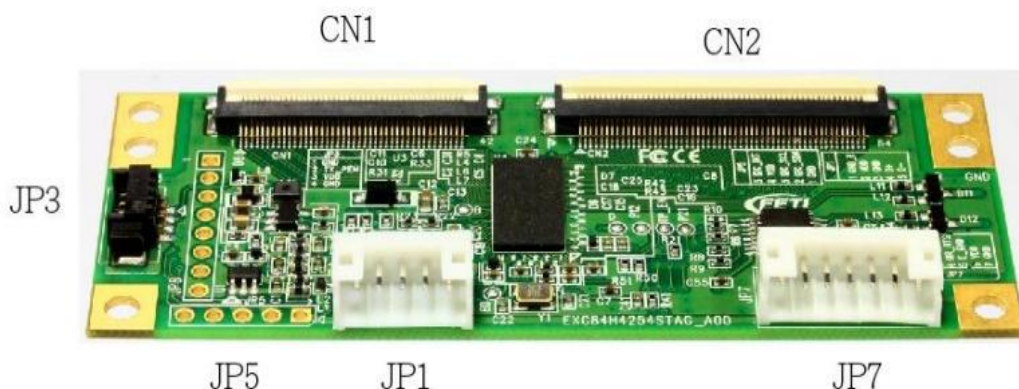
CN1						CN2					
PIN	SYMBOLS	PIN	SYMBOLS	PIN	SYMBOLS	PIN	SYMBOLS	PIN	SYMBOLS	PIN	SYMBOLS
1	TX_S2	21	TX20	41	TX0	1	RX_S1	21	RX32	41	RX12
2	TX39	22	TX19	42	TX_S0	2	RX51	22	RX31	42	RX11
3	TX38	23	TX18			3	RX50	23	RX30	43	RX10
4	TX37	24	TX17			4	RX49	24	RX29	44	RX9
5	TX36	25	TX16			5	RX48	25	RX28	45	RX8
6	TX35	26	TX15			6	RX47	26	RX27	46	RX7
7	TX34	27	TX14			7	RX46	27	RX26	47	RX6
8	TX33	28	TX13			8	RX45	28	RX25	48	RX5
9	TX32	29	TX12			9	RX44	29	RX24	49	RX4
10	TX31	30	TX11			10	RX43	30	RX23	50	RX3
11	TX30	31	TX10			11	RX42	31	RX22	51	RX2
12	TX29	32	TX9			12	RX41	32	RX21	52	RX1
13	TX28	33	TX8			13	RX40	33	RX20	53	RX0
14	TX27	34	TX7			14	RX39	34	RX19	54	RX_50
15	TX26	35	TX6			15	RX38	35	RX18		
16	TX25	36	TX5			16	RX37	36	RX17		
17	TX24	37	TX4			17	RX36	37	RX16		
18	TX23	38	TX3			18	RX35	38	RX15		
19	TX22	39	TX2			19	RX34	39	RX14		
20	TX21	40	TX1			20	RX33	40	RX13		

4.2.2. TOUCH DRIVER

Control Board : ETP-MB-EXC84H4254STAG

NO.	Item	Specification
1	Touch Controller IC	EXC80H84
2	Number Of Support	10 Fingers Touch
3	FPC Type / Interface	USB / RS232

Interface	Symbol	Condition	Min.	Typ.	Max.	Unit
JP1/JP7	VDD	DC	3.5	5	5.5	V



Connector Pin Configuration

JP1 USB connector

JP1	
1	GND_E
2	VDD
3	GND
4	D+
5	D-

JP5 I2C connector

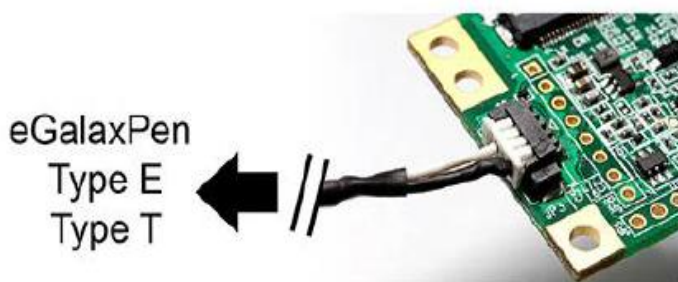
JP5	
1	GND
2	I2C_SDA
3	I2C_SCL
4	VDD
5	I2C_INT

JP7 RS232 connector

JP7	
1	UR_CTS
2	UR_TX
3	UR_RX
4	UR_RTS
5	GND_E
6	VDD
7	GND

JP3 Tethered active stylus connector

JP3	
1	GND_D
2	TX_PEN
3	VDD
4	GND_D



*JP3 connector is reserved for EETI tethered active stylus:

Type E: Entry-type active stylus

Type T: Tethered USB active stylus

5. OPTICAL CHARACTERISTICS

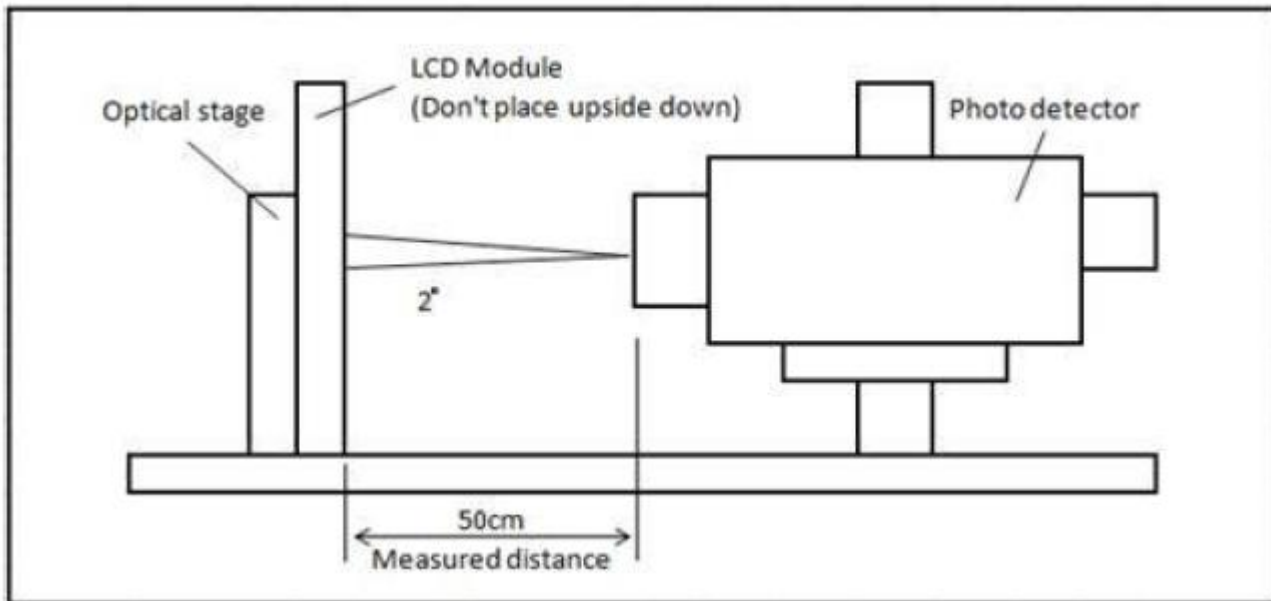
The optical characteristics are measured under stable condition.

Test Condition:

1. Equipment setup: Please refer to Note 1.
2. Panel Lighting time: 30 minutes
3. $V_{DD}=5.0V$, $F_V=60Hz$, $I_S=45mA$, $T_a=25^{\circ}C$

Item		Unit	Conditions		Min.	Typ.	Max.	Note
White Luminance		cd/m²	Center of screen		200	250	-	1
Luminance Uniformity		%	9 points		75	80	-	2
Contrast Ratio		-	Center of screen		600	1000	-	3
View Angle		degree	Horizontal CR=10	Right	75	85	-	4
				Left	75	85	-	
			Vertical CR=10	Upper	70	80	-	
				Lower	70	80	-	
			Horizontal CR=5	Right	75	85	-	
				Left	75	85	-	
			Vertical CR=5	Upper	70	80	-	
				Lower	70	80	-	
Response Time		ms	Rising		-	3.8	5.5	5
			Falling		-	1.2	2.5	
			Rising + Falling		-	5	8	
Color / Chromaticity Coordinates (CIE 1931)	Red	Rx	CIE 1931		0.589	0.639	0.689	By SR-3
		Ry			0.283	0.333	0.383	
	Green	Gx			0.252	0.302	0.352	
		Gy			0.574	0.624	0.674	
	Blue	Bx			0.107	0.157	0.207	
		By			0.008	0.058	0.108	
	White	Wx			0.263	0.313	0.363	
		Wy			0.279	0.329	0.379	
NTSC		%			-	72	-	

Note 1: Equipment setup:

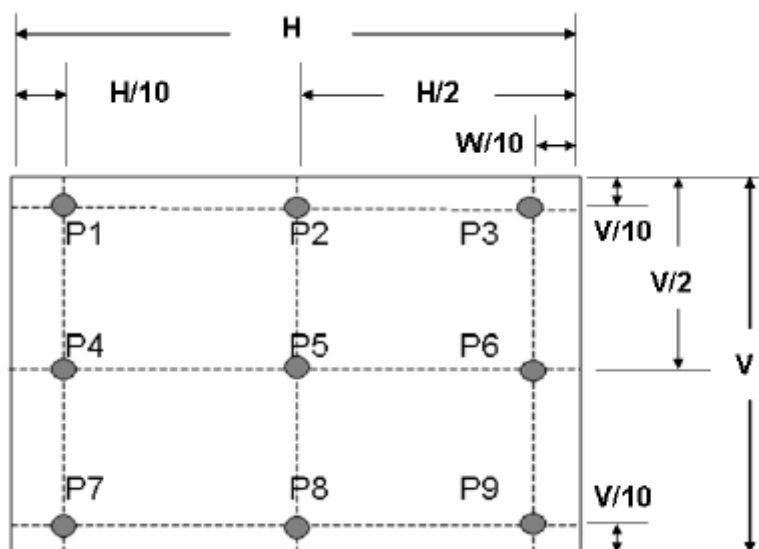


Note 2: Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 points (P1~P9)}}{\text{Maximum Luminance of 9 points (P1~P9)}}$$

Test Pattern: White Pattern



Note 3: Contrast Ratio Measurement

Definition:

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance of White Pattern}}{\text{Luminance of Black Pattern}}$$

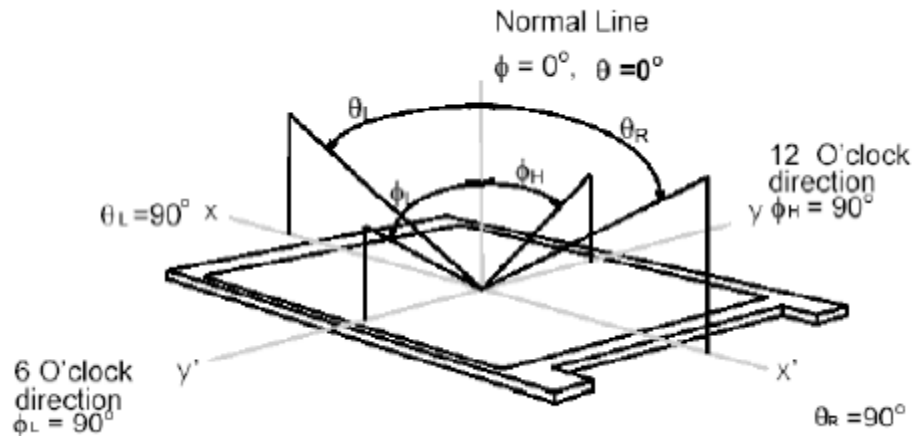
Measured position: Center of screen (P5) & perpendicular to the screen ($\theta=\phi=0^\circ$)

Note 4: Viewing angle measurement

Definition: The angle at which the contrast ratio is greater than 10 & 5.

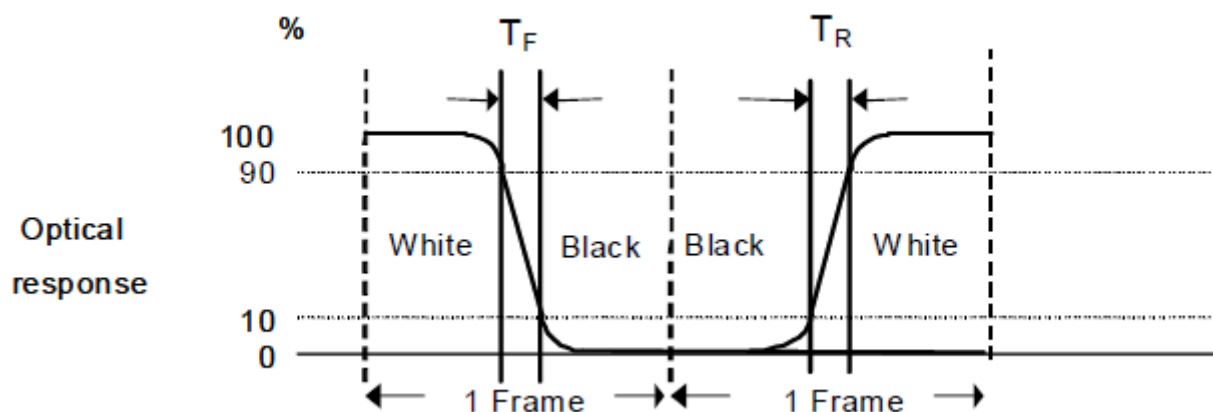
Horizontal view angle: Divide to left & right (θ_L & θ_R)

Vertical view angle: Divide to up & down (ϕ_H & ϕ_L)



Note 5: Response time Measurement

The output signals of photo detector are measured when the input signals are changed from “Black” to “White” (rising time, T_R), and from “White” to “Black” (falling time, T_F), respectively. The response time is interval between the 10% and 90% of optical response. (Black & White color definition: Please refer to section 4.1.9)



6. RELIABILITY TEST ITEMS

Test Item		Condition
1	High temperature/Humidity Bias	50°C, 80% 240hrs
2	High temperature storage test	60°C, 240hrs
3	Low temperature storage test	-20°C, 240hrs
4	High temperature operation test	50°C, 50%RH, 240hrs
5	Low temperature operation test	0°C, 240hrs
6	Thermal Cycle storage test	-20°C(30min)~60°C(30min), 10cycles

7. LABEL

Model No. F170PET-T21

條碼

SN 3A Z 20 2 21 0 XXX1

W=14mm



L=32mm

字型:Arial (6)

Code: 128

字型 : Arial(8)

Code1, 2 Production location

Code 3 Production version

Code 4, 5 Production year

Code 6 Production month

Code 7, 8 Production day

Code 9 Production code

Code 10, 11, 12, 13 Serial number

8. MECHANICAL CHARACTERISTICS

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	357.00	mm	PCAP : B170PBU-E06
	Vertical	287.50	mm	
	Depth	2.70	mm	
	Horizontal	358.5	mm	LCD : M170ETN01.1 (ES8.0 from 10D)
	Vertical	296.5	mm	
	Depth	10.30	mm	
	Horizontal	82.50	Mm	Touch Driver : ETP-MP-EXC84H4254STAG
	Vertical	33.00	Mm	
	Depth	7.50	mm	
Weight		1960	g	F170PET-T21
Item		Specification		Remarks
PCAP Viewing Area		341.92 mm x 274.34 mm		
PCAP Sensor Area		356.00 mm x 286.50 mm		
PCAP FPC Length		96.70 mm		
PCAP Input method		Finger or exclusive pen		
PCAP Light Transmission		$\geq 90\% \pm 3\%$		
PCAP Operation force		$\leq 10g$		Finger Input
PCAP Hardness of surface		$\geq 7H$		Pressure 500gf test
PCAP Surface Treatment		Haze $< 4\%$		BYK Gardner , ASTM D1003



Product Specification

F170PET-T21

Front View



Back View



9. ENGINEERING DRAWING

