



Product Specification

F320PET-Z02

- ☐ Draft Specifications
☒ Preliminary Specifications
☐ Final Specifications

Title	32.0-inches Touch Display Product Specifications
Model Name	F320PET-Z02
Version	1

Customer	
Approved by	Date
Notice: This Specification is subject to change without notice.	

Approved By	Prepared By
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2026-01-09	2026-01-09



REVISION HISTORY

Version	ECN No.	Description of Changes	Date	Prepared
0		Preliminary specification first edition	2022-12-07	Allen Fang
1		Modified Label	2026-01-09	Allen Fang



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1. General Description

1.1. Introduction

F320PET-Z02 is a color active matrix TFT LCD module P320HVN06.1 with projected capacitive multi-touch B320PBU-E01 air bonding, with touch driver B000PDU-E01.

The display format is intended to support the FHD mode (1920(H) x 1080(V)) screen and 2 channel 51 pins LVDS interface, RGB 8-bits colors, with white LEDs backlight unit.

1.2. Features

- High Brightness
- Wide view angle
- High color saturation
- High color depth

1.3. Application

- Desktop Type of PC & Workstation Use
- Slime-size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

2. General Specification

Parameter	Specification	Unit	Remarks
Active Area Size	31.5	inch	
Display Area	698.4 (H) x 392.85 (V)	mm	
Outline Dimension	744.50 (H) x 438.50 (V) x 37.75 (D)	mm	D:PCAP+LCM
Weight	7200	g	(Typ.)
Driver Element	a-Si TFT active matrix	-	
Display Colors	8 bits	Colors	
Number of Pixels	1920 (H) x 1080 (V)	pixels	
Pixel Pitch	0.3637 (H) x 0.3637(W)	Um	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normal Black	-	
Color Gamut	72% of NTSC (Typ.)	-	
Touch Technology	Projected Capacitive Multi-Touch Panel	-	
Touch Method	Finger or exclusive pen	-	
Numbers of Touch	10	Points	
Touch Driver	B000PDU-E01:USB	-	
Touch Sensor Glass Type	Tempering Glass	-	
Touch Controller IC	EETI, EXC80W83	-	
Touch Interface	USB 1.1 Full Speed	-	
Support OS	Windows (Embedded/CE/XP/2000/Vista/7/8/10) / Linux / Android / MAC / QNX	-	

3. Absolute Maximum Ratings

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

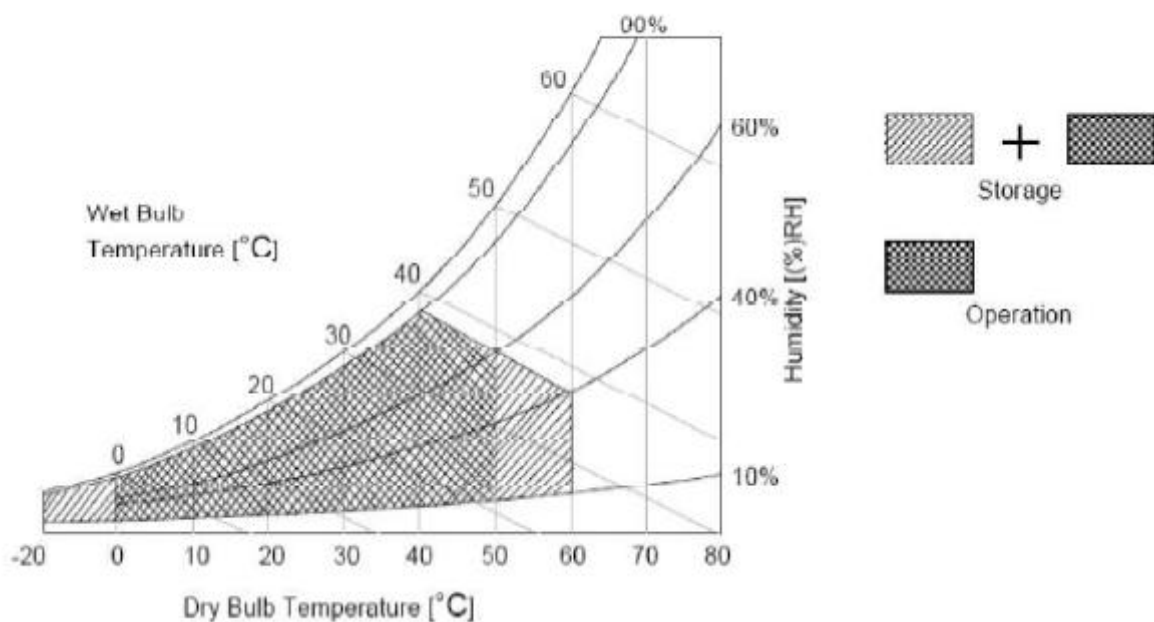
Parameter	Symbol	Min.	Max.	Unit	Remarks
Logic/LCD Drive Voltage	V_{DD}	-0.3	14.0	V	Note 1
Input Voltage of Signal	V_{In}	-0.3	4	V	
Operating Temperature	T_{op}	0	+50	°C	Note 2
Operating Humidity	H_{op}	10	90	%RH	
Storage Temperature	T_{st}	-20	+60	°C	
Storage Humidity	H_{st}	10	90	%RH	
Panel Surface Temperature	P_{st}		65	°C	Note 3
Touch DC Supply Voltage	USB_ V_{DD}	-0.3	6.0	V	

Note:

1. Duration: 50 msec.
2. Maximum Wet-Bulb temperature should be 39°C and no condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C.

3. Surface temperature is measured at 50°C Dry condition.



4. Electrical Specifications

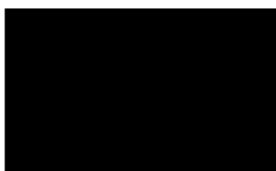
4.1. LCD Module

Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max.		
Power Supply Input Voltage		V_{DD}	10.8	12.7	14	V	1
Power Supply Input Current	Black Pattern	I_{DD}	-	0.29	0.3	A	2
	White Pattern		-	0.3	0.31	A	
	H-Strip pattern		-	0.45	0.49	A	
Power Consumption	Black Pattern	P_C	-	3.81	4.57	W	
	White Pattern		-	3.94	4.73	W	
	H-Strip pattern		-	6.22	7.464	W	
In-Rush Current		I_{Rush}	-	-	4	A	3

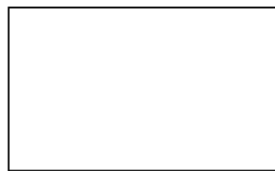
Notes:

1. The ripple voltage should be fewer than 5% of V_{DD} .
2. Test Condition :
 - (1) $V_{DD}=12.0V$, (2) $F_v=60Hz$, (3) $F_{clk}=74.25MHz$, (4) Temperature= $25^{\circ}C$
 - (5) Power dissipation check pattern. (Only for power design)

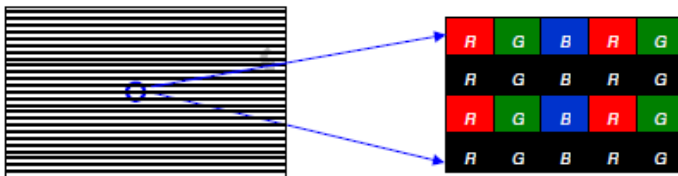
a. Black pattern



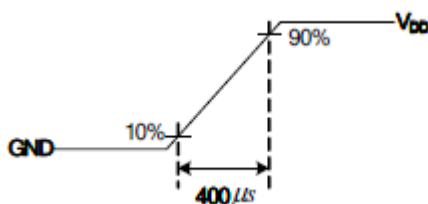
b. White pattern



c. H-Strip pattern



3. Measurement condition : Rising time= $400\mu s$



4.2. LCD Back-Light Unit

Parameter	Symbol	Condition	Values			Unit	Notes
			Min.	Typ.	Max.		
Power Supply Input Voltage	V_{DDB}	-	22	24	26	V	-
Power Supply Input Current	I_{DDB}	$V_{DDB}=24V$	-	1.625	2	A	1
Power Consumption	P_{DDB}	$V_{DDB}=24V$	-	39	52	Watt	
Control signal voltage	V_{signal}	$V_{DDB}=24V$	2	-	5	V	
			0	-	0.5		
Control signal current	I_{signal}	$V_{DDB}=24V$	-	-	1.5	mA	
Brightness Voltage	V_{adj}	$V_{DDB}=24V$	0(bright)		5(dark)	V	
DET status signal	DET	$V_{DDB}=24V$	Open Collector			-	
			0	-	0.8	V	
LED lifetime	LT_{LED}	-	30,000	-	-	Hr	2

Note 1. Dimming ratio=100%, ($T_a=25\pm5^\circ C$, turn on for 45 minutes)

Note 2. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value.
[Operating condition : Continuous operating at $T_a=25\pm2^\circ C$, for single LED only]

4.3. Touch Control Board

B000PDU-E01

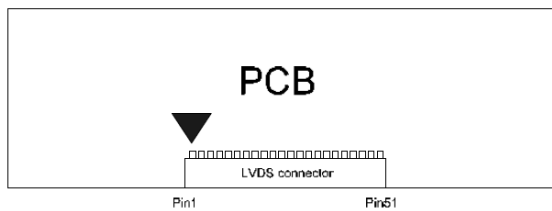
Parameter	Values			Unit	Notes
	Min.	Typ.	Max.		
Input Voltage	3.5	5	5.5	V	
Interface	USB:1.1 Full Speed			-	
Max Resolution	4096*4096			-	
Precision	≤ 2			mm	
Two points separate distance	≥ 14 (pitch of finger center)			mm	
Touch point	1~10 points			-	
Power consumption (mA)	Active Mode: About 60mA			mA	
Report rate (points/sec)	≥ 120			Hz	
Response time	15			ms	

4.4. Input Terminal Pin Assignment

LCD connector: FI-RTE51SZ-HF (JAE, LVDS connector) or compatible (CHIEF LAND 115E51-0000RA-M3-R / P-two 187059-5122)

Pin	Symbol	Description	Note	Pin	Symbol	Description	Note
1	N.C.	No connection	2	31	CH2_Y1+	LVDS Channel 2, Signal 1+	
2	N.C.	No connection	2	32	CH2_Y2-	LVDS Channel 2, Signal 2-	
3	N.C.	No connection	2	33	CH2_Y2+	LVDS Channel 2, Signal 2+	
4	N.C.	No connection	2	34	GND	Ground	
5	N.C.	No connection	2	35	CH2_CLK-	LVDS Channel 2, Clock-	
6	N.C.	No connection	2	36	CH2_CLK+	LVDS Channel 2, Clock+	
7	LVDS_SEL	Open/High(3.3V) for NS, Low (GND) for JEIDA	3,4	37	GND	Ground	
8	N.C.	No connection	2	38	CH2_Y3-	LVDS Channel 2, Signal 3-	
9	N.C.	No connection	2	39	CH2_Y3+	LVDS Channel 2, Signal 3+	
10	N.C.	No connection	2	40	N.C.	No connection	2
11	GND	Ground		41	N.C.	No connection	2
12	CH1_Y0-	LVDS Channel 1, Signal 0-		42	N.C.	No connection	2
13	CH1_Y0+	LVDS Channel 1, Signal 0+		43	N.C.	No connection	2
14	CH1_Y1-	LVDS Channel 1, Signal 1-		44	GND	Ground	
15	CH1_Y1+	LVDS Channel 1, Signal 1+		45	GND	Ground	
16	CH1_Y2-	LVDS Channel 1, Signal 2-		46	GND	Ground	
17	CH1_Y2+	LVDS Channel 1, Signal 2+		47	N.C.	No connection	2
18	GND	Ground		48	V _{DD}	Power Supply, +12V DC Regulated	
19	CH1_CLK-	LVDS Channel 1, Clock-		49	V _{DD}	Power Supply, +12V DC Regulated	
20	CH1_CLK+	LVDS Channel 1, Clock+		50	V _{DD}	Power Supply, +12V DC Regulated	
21	GND	Ground		51	V _{DD}	Power Supply, +12V DC Regulated	
22	CH1_Y3-	LVDS Channel 1, Signal 3-					
23	CH1_Y3+	LVDS Channel 1, Signal 3+					
24	N.C.	No connection	2				
25	N.C.	No connection	2				
26	GND	Ground					
27	GND	Ground					
28	CH2_Y0-	LVDS Channel 2, Signal 0-					
29	CH2_Y0+	LVDS Channel 2, Signal 0+					
30	CH2_Y1-	LVDS Channel 2, Signal 1-					

Note1. Pin number start from the left side as the following figure.



Note2. Please leave this pin unoccupied. It cannot be connected with any signal (Low/GND/High).

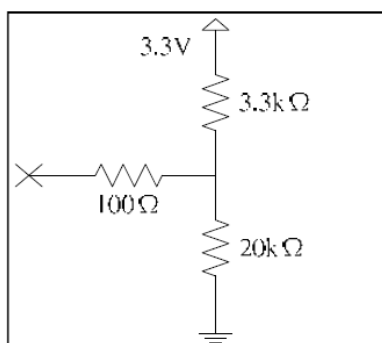
Note3. Input control signal threshold voltage definition.

Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2.7	-	3.6	V
Input Low Threshold Voltage	VIL	0	-	0.6	V

Note4. LVDS data format selection

LVDS_SEL	Mode
H or Open	NS
L	JEIDA

Input equivalent impedance of LVDS_SEL pin



LCD Back-Light Connector: PH2.0-14AWB or compatible

Pin	Symbol	Description	Note
1	VDDDB	Power Supply Input Voltage	
2	VDDDB	Power Supply Input Voltage	
3	VDDDB	Power Supply Input Voltage	
4	VDDDB	Power Supply Input Voltage	
5	VDDDB	Power Supply Input Voltage	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	GND	Ground	
10	GND	Ground	
11	NC	No Connection	
12	VBLON	BLU On-Off control	1,2
13	ADJ	Dimming Control	3
14	NC	No Connection	

Note1. Input control signal threshold voltage definition

Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2	-	5.0	V
Input Low Threshold Voltage	VIL	0	-	0.5	V

Note2. VBLON

Mode selection

VBLON	Note
VIH	BL On
VIL	BL Off

Note3. 0V=Brightness Max. 5V=Brightness Min.

Touch Control Board Pin Assignment :

FCON1,FCON2 : PCAP Input CON4 : USB Output

FCON1		FCON2		USB (CON4)	
Pin	Name	Pin	Name	Pin	Name
1	Shielding RX79	1	NC	1	VBUS
2~79	Y78~Y1	2	Shielding TX0	2	D-
80	Shielding RX0	3~4	NC	3	D+
		5~50	X1~X46	4	GND
		51~54	NC	5	GND
		55	Shielding TX47		

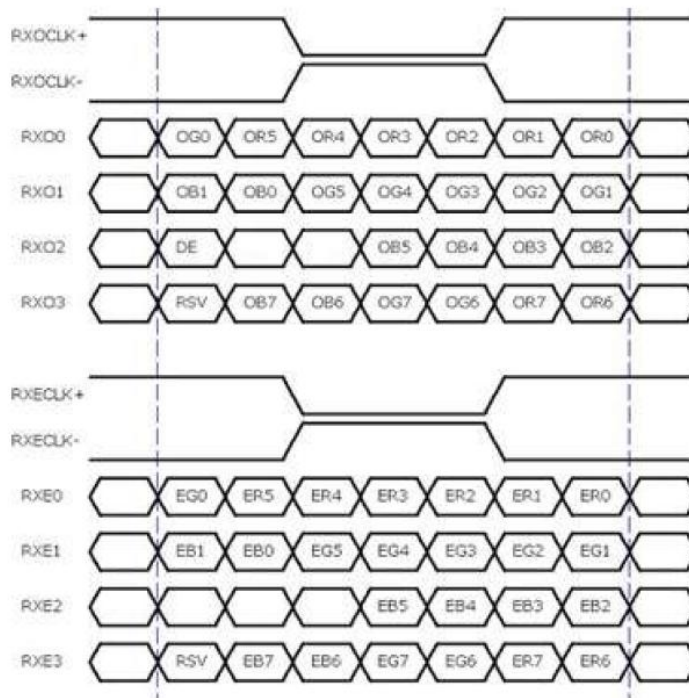


4.5. Input Data Format

4.5.1. LCD Pixel Format

	1			2													1919			1920		
1st Line	R	G	B	R	G	B											R	G	B	R	G	B
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4.5.2. LCD LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 1 :

- O="Odd Pixel Data" E="Even Pixel Data"
- Refer to 3.5.1 LCD Pixel Format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

4.5.3. LCD Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB				LSB				MSB				LSB				MSB				LSB			
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
R	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
B	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

5. Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

5.1. Input Timing

5.1.1. LCD Color Input Data Reference

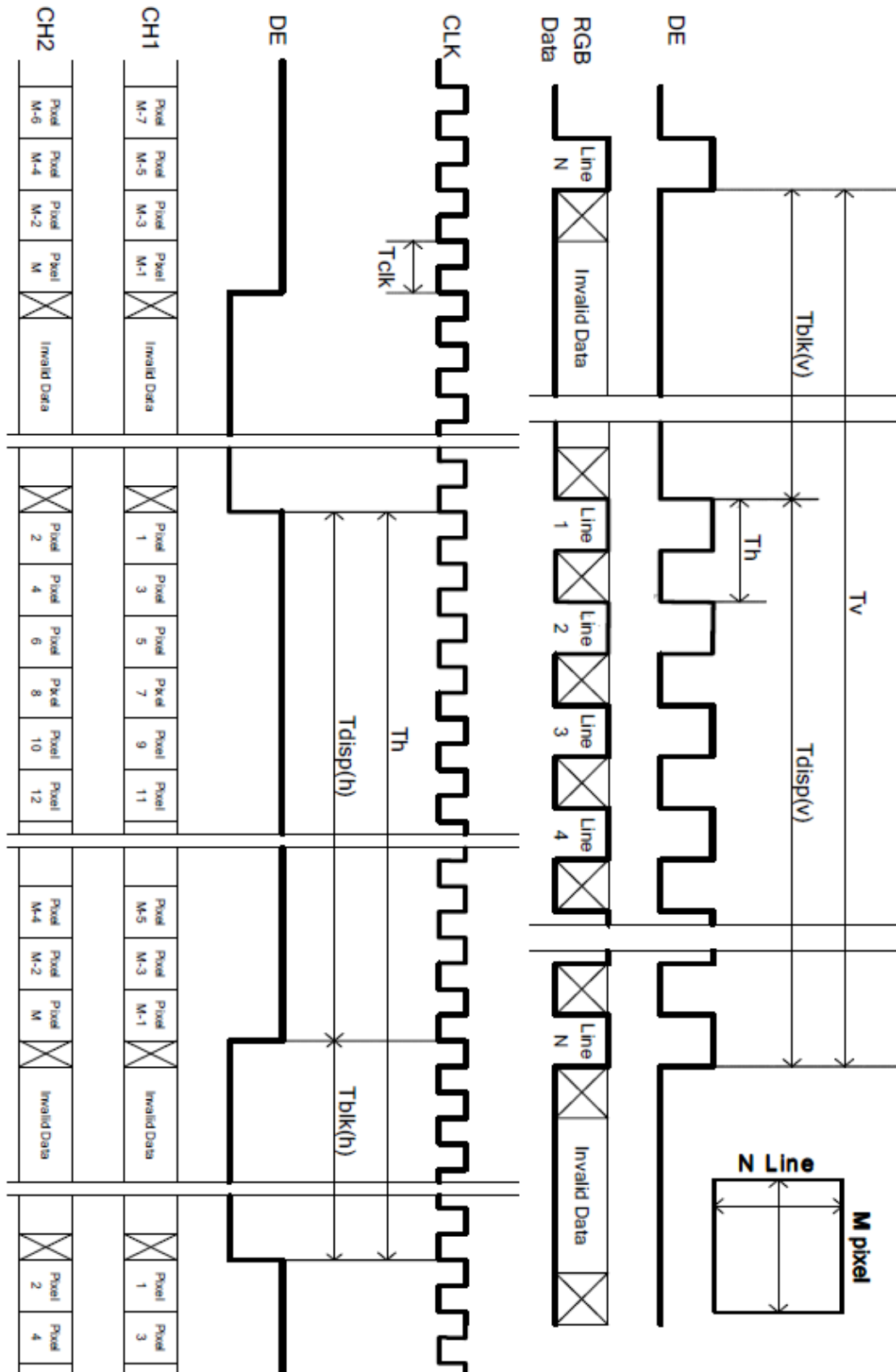
Timing Table (DE only mode)

Signal	Item	Symbol	Min.	Typ.	Max.	Unit
Vertical Section	Period	Tv	1100	1125	1480	Th
	Active	Tdisp(v)	1080			2160
	Blanking	Tblk(v)	20	45	400	Th
Horizontal Section	Period	Th	1030	1100	1325	Tclk
	Active	Tdisp(h)	960			
	Blanking	Tblk(h)	70	140	365	Tclk
Clock	Frequency	Fclk=1/Tclk	53	74.25	82	MHz
Vertical Frequency	Frequency	Fv	47	60	63	Hz
Horizontal Frequency	Frequency	Fh	60	67.5	73	KHz

Notes :

- (1) Display position is specific by the rise of DE signal only.
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.
- (2) Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.
- (3) If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.
- (4) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

5.1.2. LCD Signal Timing Waveform

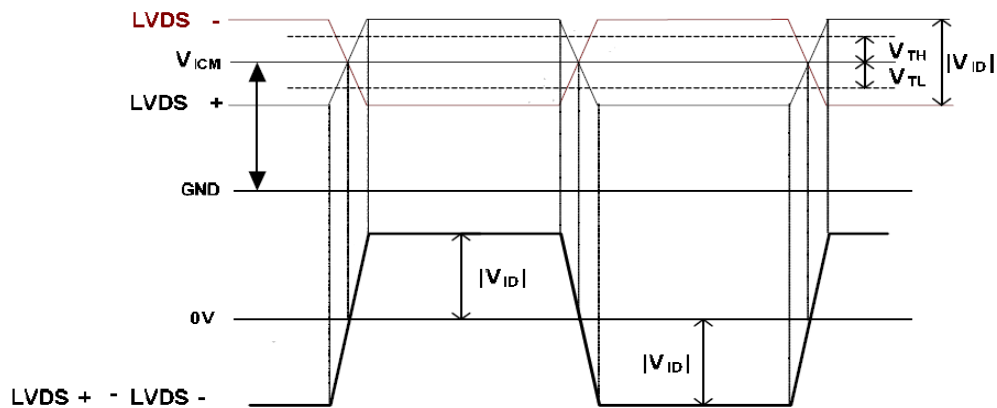


5.2. Input Interface characteristics

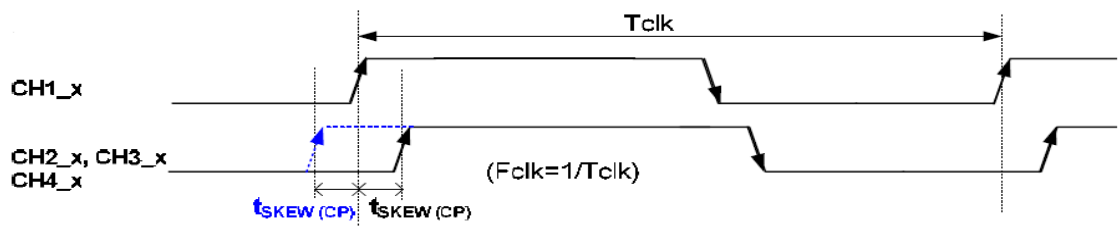
5.2.1. LVDS

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
LVDS Interface	Input Differential Voltage	$ V_{ID} $	200	400	600	mV _{DC}	1
	Differential Input High Threshold voltage	V_{TH}	+100	--	+300	mV _{DC}	1
	Differential Input Low Threshold Voltage	V_{TL}	-300	--	-100	mV _{DC}	1
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V _{DC}	1
	Input Channel Pair Skew Margin	$t_{SKEW(CP)}$	-500	--	+500	Ps	2
	Receiver Clock: Spread Spectrum Modulation range	F_{clk_ss}	Fclk -3%	--	Fclk +3%	MHz	3
	Receiver Clock: Spread Spectrum Modulation frequency	F_{ss}	30	--	200	KHz	3
	Receiver Data Input Margin Fclk=85 MHz Fclk=65 MHz	t_{RMG}	-0.4 -0.5	-- --	0.4 0.5	ns	4

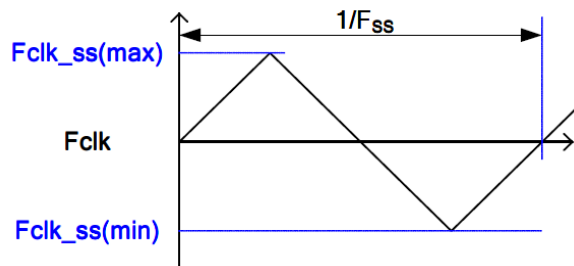
Note1. $V_{ICM}=1.25V$



Note2. Input Channel Pair Skew Margin

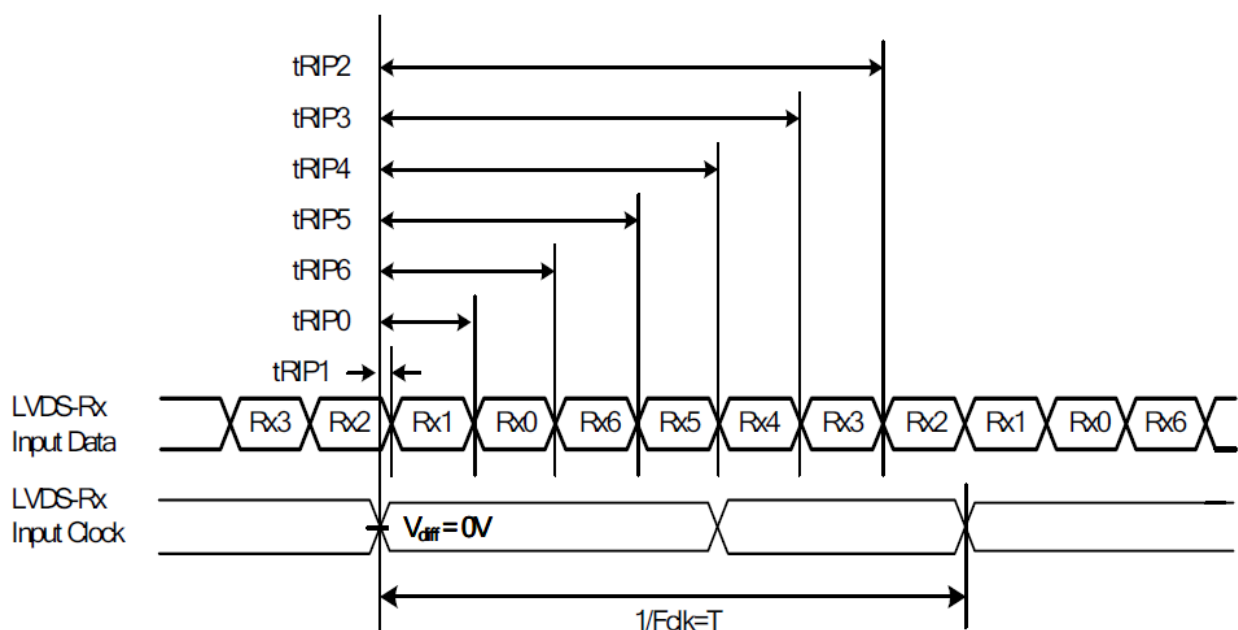


Note3. LVDS Receiver Clock SSCG (Spread Spectrum clock generator) is defined as below figures.

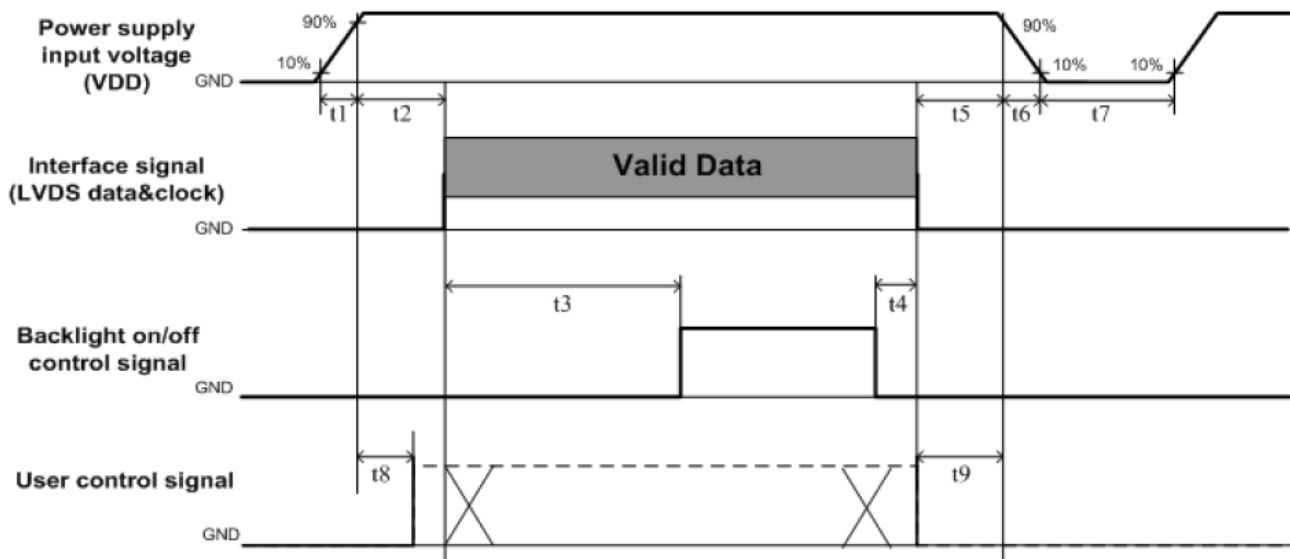


Note4. Receiver Data Input Margin

Parameter	Symbol	Rating			Unit	Note
		Min.	Typ.	Max.		
Input Clock Frequency	F_{clk}	$F_{clk} \text{ (Min.)}$	--	$F_{clk} \text{ (Max.)}$	MHz	$T=1/F_{clk}$
Input Data Position 0	t_{RIP1}	$- t_{RMG} $	0	$ t_{RMG} $	ns	
Input Data Position 1	t_{RIP0}	$T/7 - t_{RMG} $	$T/7$	$T/7 + t_{RMG} $	ns	
Input Data Position 2	t_{RIP6}	$2 T/7 - t_{RMG} $	$2 T/7$	$2 T/7 + t_{RMG} $	ns	
Input Data Position 3	t_{RIP5}	$3 T/7 - t_{RMG} $	$3 T/7$	$3 T/7 + t_{RMG} $	ns	
Input Data Position 4	t_{RIP4}	$4 T/7 - t_{RMG} $	$4 T/7$	$4 T/7 + t_{RMG} $	ns	
Input Data Position 5	t_{RIP3}	$5 T/7 - t_{RMG} $	$5 T/7$	$5 T/7 + t_{RMG} $	ns	
Input Data Position 6	t_{RIP2}	$6 T/7 - t_{RMG} $	$6 T/7$	$6 T/7 + t_{RMG} $	ns	



5.3. Power Sequence for LCD



Parameter	Values			Unit	Note
	Min.	Typ.	Max.		
t1	0.4	---	30	ms	
t2	0.1	---	50	ms	
t3	450	---	---	ms	
t4	0 ^{*1}	---	---	ms	(1)
t5	0	---	---	ms	
t6	---	---	--- ^{*2}	ms	(2)
t7	500	---	---	ms	
t8	20 ^{*3}	---	50	ms	(3)
t9	0	---	---	ms	

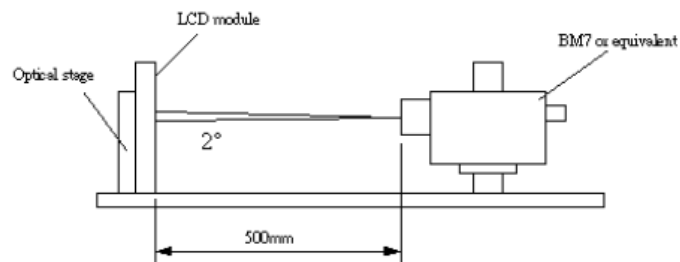
Note:

- (1) t4=0 : Concern for residual pattern before BLU turn off.
- (2) t6 : Voltage of V_{DD} must decay smoothly after power-off. (customer system decide this value)
- (3) When user control signal is N.C. (no connection), opened in Transmitted end, t8 timing spec can be

6. Optical Specification

LCD Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25°C. The values specified are at an approximate distance 500 mm from the LCD surface at a viewing angle of φ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max.		
Contrast Ratio		CR	2000	4000	--		1
Surface Luminance (White)		L_{WH}	320	400	--	Cd/m ²	2
Luminance Variation		$\delta_{WHITE(9P)}$	70	75			3
Response Time (G to G)		T_Y	--	6.5		ms	4
Color Gamut		NTSC		72		%	
Color Coordinates	Red	R_x	Typ.-0.03	0.655	Typ.+0.03		
		R_y		0.333			
	Green	G_x		0.303			
		G_y		0.636			
	Blue	B_x		0.154			
		B_y		0.060			
	White	W_x		0.280			
		W_y		0.290			
Viewing Angle	X axis, right ($\varphi=0^\circ$)	θ_r	--	89	--	degree	5
	X axis, right ($\varphi=180^\circ$)	θ_l	--	89	--	degree	
	Y axis, right ($\varphi=90^\circ$)	θ_u	--	89	--	degree	
	Y axis, right ($\varphi=270^\circ$)	θ_d	--	89	--	degree	

Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance of } L_{on1}}{\text{Surface Luminance of } L_{off1}}$$

2. Surface luminance is luminance value at point 1 across the LCD surface 50 cm from the surface with all pixels displaying white. From more information see FIG 2. LED current I_F = typical value (without driver board), $L_{WH} = L_{on5}$ where L_{on5} is the luminance with all pixels displaying white at center 5 location.
3. The variation in surface luminance, δ_{WHITE} is defined (center of Screen) as:

$$\delta_{WHITE(9P)} = \text{Minimum}(L_{on1}, L_{on2}, \dots, L_{on9}) / \text{Maximum}(L_{on1}, L_{on2}, \dots, L_{on9})$$
4. Response time T_y is the average time required for display transition by switching the input signal for five luminance ratio (0%, 25%, 50%, 75%, 100% brightness matrix) and is based on Frame rate = 60Hz to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

T_y is determined by 10% to 90% brightness difference of rising or falling period. (As illustrated)

The response time is defined as the following figure and shall be measured by switching the input signal for “any level of gray (bright)” and “any level of gray (dark)”.

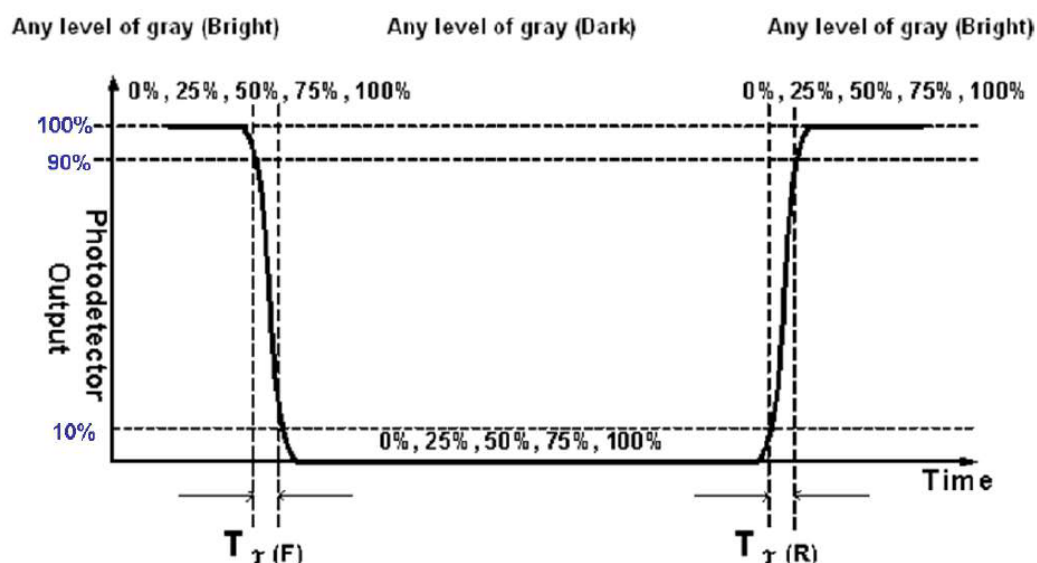
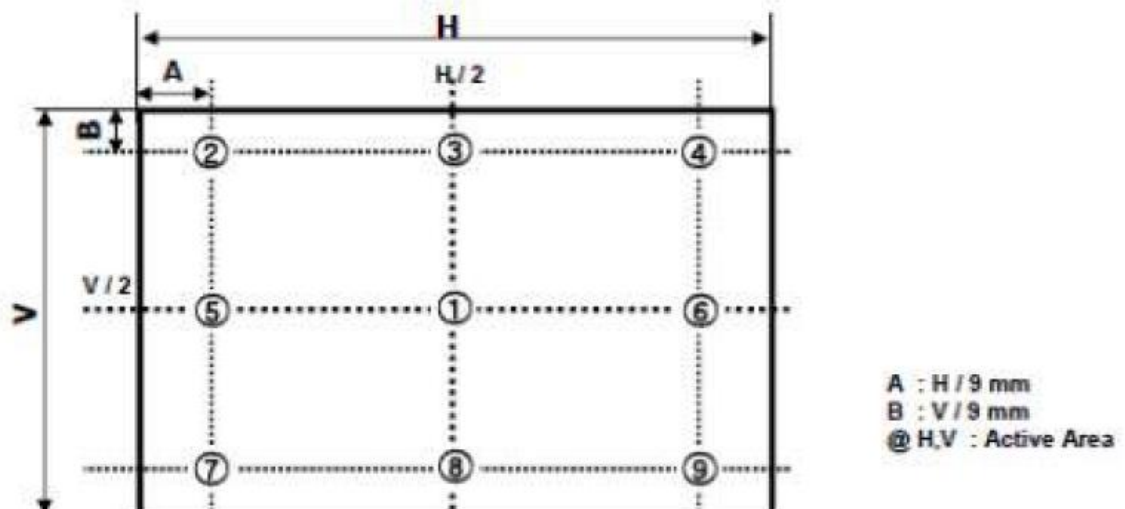


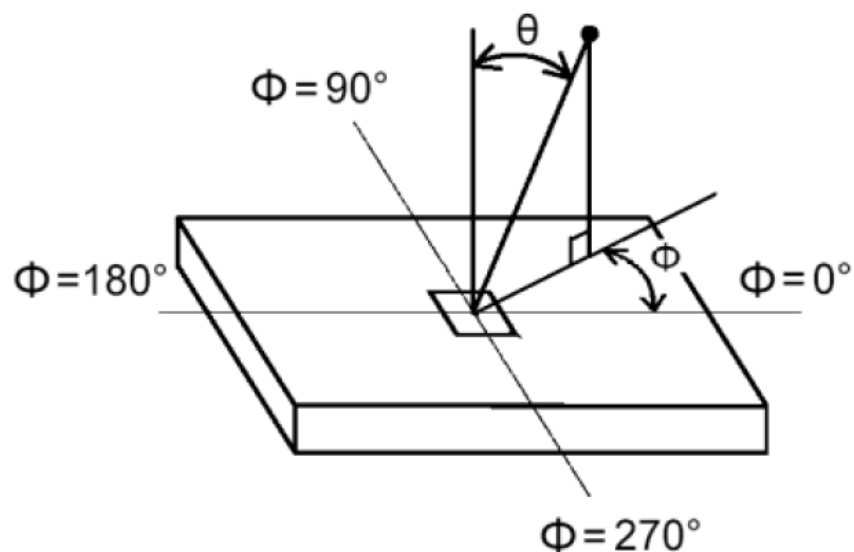
FIG. 2 Luminance

Measuring point for surface luminance & luminance variation **CA-310 ,Contact method)**



- Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing Angle



7. Reliability Test Items

Test Item		Q'ty	Condition
1	High temperature/Humidity Storage test	3	60°C, 90% 240hrs
2	Low temperature storage test	3	-20°C, 240hrs
3	High temperature/Humidity operation test	3	50°C, 80% 240hrs
4	Low temperature operation test	3	0°C, 240hrs
5	Thermal Cycle storage test	1(PKG)	20°C~40°C, 90min, 10cycle

8. International Standard

8.1. Safety

- (1) UL 62368-1 and CAN/CSA C22.2 No. 62368-1-14 - Audio/video, information and communication technology equipment Part 1: Safety requirements
- (2) IEC 62368-1:2014 (Second Edition)

9. Label

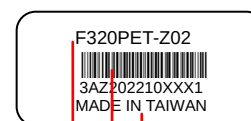
Model NO. **F320PET-Z02**

條碼

L=32mm

SN **3A Z 20 2 21 0 XXX1**

code 1,2 Production location
 code 3 Production version
 code 4,5 Production year
 code 6 Production month
 code 7,8 Production day
 code 9 Production code
 code 10,11,12,13 Serial number



W=14mm

字型:Arial (6)

code:128

字型:Arial (8)

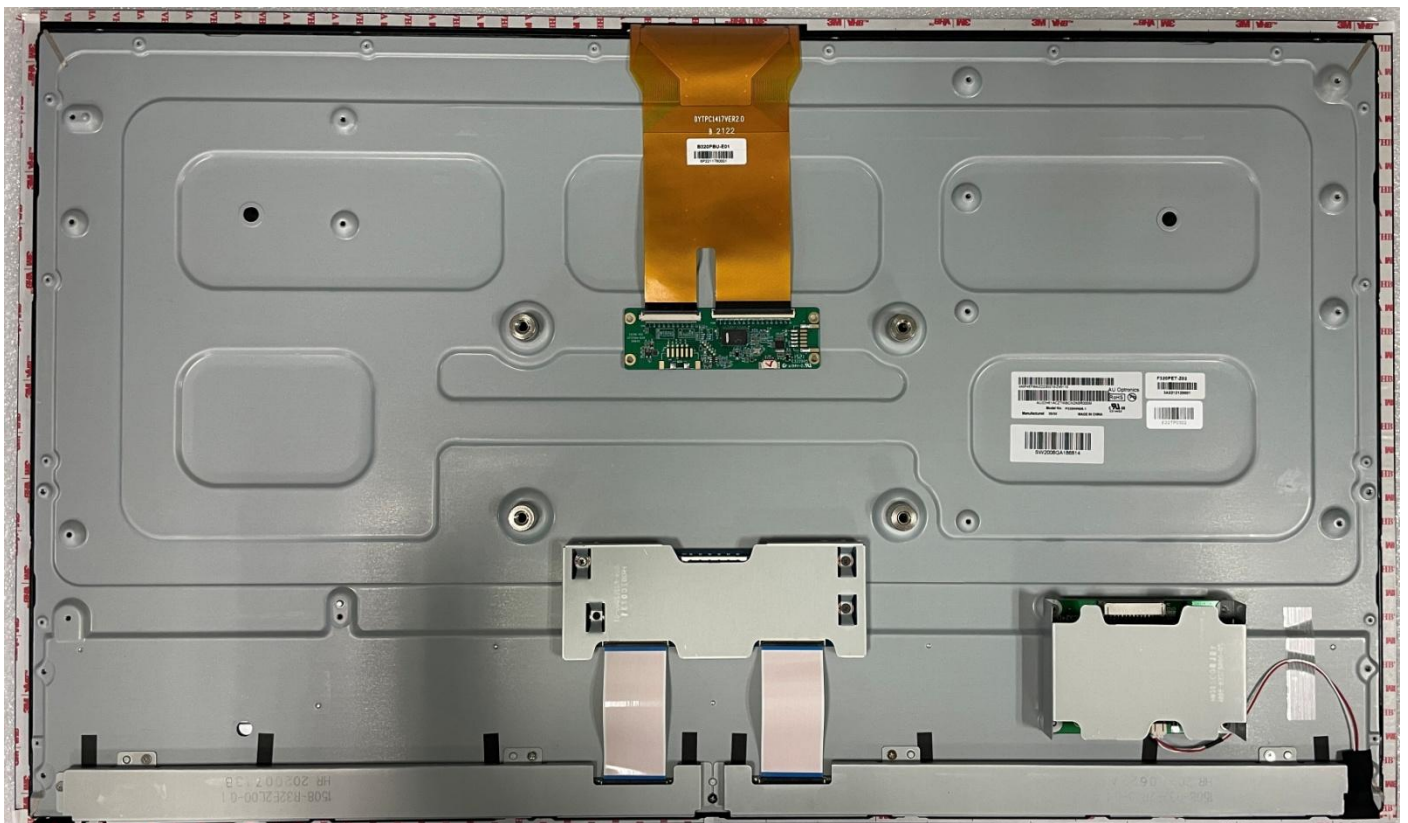
10. Mechanical Characteristics

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	744.50	mm	PCAP : B320PBU-E01
	Vertical	438.50	mm	
	Depth	4.50	mm	
	Horizontal	716.20	mm	LCD : P320HVN06.1
	Vertical	415.05	mm	
	Depth	32.15	mm	
	Horizontal	105.00	mm	Touch Driver : B000PDU-E01
	Vertical	33.00	mm	
	Depth	1.60	mm	
Weight		7200	g	F320PET-Z02
Item		Specification		Remarks
PCAP Viewing Area		699.40 mm x 393.85 mm		
PCAP Sensor Area		721.00 mm x 418.00 mm		
PCAP FPC Length		159.21 mm		
PCAP Input method		Finger or exclusive pen		
PCAP Light Transmission		≥83%		
PCAP Operation force		≤10g		Finger Input
PCAP Hardness of surface		≥6H		Pressure 500gf test
PCAP Surface Treatment		Clear Type		

Front View



Back View



11. Engineering Drawing

