AU Optronics Corp.

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Product Specification

7□color TFT-LCD module

MODEL NAME: A070FW01

Original model name:UP070W01

(.....) Preliminary Specification

(□) Final Specification

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Record of Revision

Version	Revise Date	Page	Content
1	09/Mar./2000	1	First draft.
2	23/May./2000	3	Weight => 350±20 → 190±20
		6	V_{CAC} => (5) → 5.6 ; V_{CDC} => (1.4) → 1.7
			I_{GH}, I_{GL}, I_{CC}, I_{DD} => (X) → X & I_{DD} => (18.0) → 17.0
			V_L, I_L, V_S => (X) → X & V_S =>(1100) → 1200
		7	t_{OEH} => (9) → 1.22 & t_{CPH} → μs
			t_{OEH} => (90) → 8.28 & t_{CPH} → μs
			t_{OEH} => (39) → 5.40 & t_{CPH} → μs
			t_{OEH} => (60) → 4.18 & t_{CPH} → μs
			t_{OEH} => (30) → 3.74 & t_{CPH} → μs
		8	Viewing angle : Top => (50) → 40
			Bottom => (20) → 20
			Left => (45) → 45
			Right => (45) → 45
			Note 1: I_L => (6.5) → 6.5
3	09/Oct./2000	6	V_{CAC}=> 7.6 → 6.5(Max.)
			V_{CDC}=> Min.= □□Max. = □ → Min.= 1.6□Max. = 1.8
			Add lamp life time spec. and the definition
		8	Viewing angle:
			TOP=> Min.=40□Typ.=60 → Min.=20□Typ.=30
			Bottom => Min.=20□Typ.=30 → Min.=40□Typ.=60
		12	Add dimension figure of screw into outline dimension figure.
4	30/Nov./2000	5	V_{GH}- V_{GL} =>Max. =31→Max. =33
			Topa1 => Min.=-20→Min.=-30
			Add Operating temperature Topa2
			Add Note3 and Note 4 for Absolute maximum ratings
		8	X=> Min.=0.25□Typ.=0.30□Max. =0.35 →
			Min.=0.26□Typ.=0.31□Max. =0.36

			Y=> Min.=0.30□Typ.=0.35□Max. =0.40 →
			Min.=0.28□Typ.=0.33□Max. =0.38
5	16/Apr./2001		Change lable mark location.
6	22/Oct/2001		Change the cover page and Logo due to a new company name
		5	Starting voltage => Max. 1150Vrms(25□), Max. 1400Vrms(0□), → Max. 850Vrms(25□), Max. 1150Vrms(0□), Max. 1300Vrms(-30□)
		6	Logical power => 4.8V(min). → 3V(min)
		12	BLU cable length definition(revise the drawing error)

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A. Physical specifications

NO.	Item	Specification	Remark
1	Display resolution(dot)	1440(W)×234(H)	
2	Active area(mm)	154.08W)×86.58(H)	
3	Screen size(inch)	7.0(Diagonal)	
4	Dot pitch(mm)	0.107(W)×0.370(H)	
5	Color configuration	R. G. B. stripe	
6	Overall dimension(mm)	166(W)×100(H)×6.79(D)	Note 1
7	Weight(g)	190±20	

Note 1: Refer to Fig. 1

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B. Electrical specifications

1.Pin assignment

a. TFT-LCD panel driving section

Pin no	Symbol	I/O	Description	Remark
1	GND	-	Ground for logic circuit	
2	V_{CC}	I	Supply voltage of logic control circuit for scan driver	
3	V_{GL}	I	Negative power for scan driver	
4	V_{GH}	I	Positive power for scan driver	
5	STVR	I/O	Vertical start pulse	Note 1
6	STVL	I/O	Vertical start pulse	Note 1
7	CKV	I	Shift clock input for scan driver	
8	U/D	I	UP/DOWN scan control input	Note 1,2
9	OEV	I	Output enable input for scan driver	
10	VCOM	I	Common electrode driving signal	
11	VCOM	I	Common electrode driving signal	
12	L/R	I	LEFT/RIGHT scan control input	Note 1,2
13	MOD	I	Sequential sampling and simultaneous sampling setting	Note 3
14	OEH	I	Output enable input for data driver	
15	STHL	I/O	Start pulse for horizontal scan line	Note 1
16	STHR	I/O	Start pulse for horizontal scan line	Note 1
17	CPH3	I	Sampling and shifting clock pulse for data driver	
18	CPH2	I	Sampling and shifting clock pulse for data driver	
19	CPH1	I	Sampling and shifting clock pulse for data driver	
20	V_{CC}	I	Supply voltage of logic control circuit for data driver	
21	GND	-	Ground for logic circuit	
22	VR	I	Alternated video signal input(Red)	
23	VG	I	Alternated video signal input(Green)	
24	VB	I	Alternated video signal input(Blue)	
25	AV_{DD}	I	Supply voltage for analog circuit	
26	AV_{SS}	-	Ground for analog circuit	

Note 1: Selection of scanning mode (please refer to the following table)

Setting of scan control input		IN/OUT state for start pulse				Scanning direction
U/D	L/R	STVR	STVL	STHR	STHL	
GND	V_{CC}	OUT	IN	OUT	IN	From up to down, and from left to right.
V_{CC}	GND	IN	OUT	IN	OUT	From down to up, and from right to left.
GND	GND	OUT	IN	IN	OUT	From up to down, and from right to left.
V_{CC}	V_{CC}	IN	OUT	OUT	IN	From down to up, and from left to right.

IN: Input; OUT: Output.

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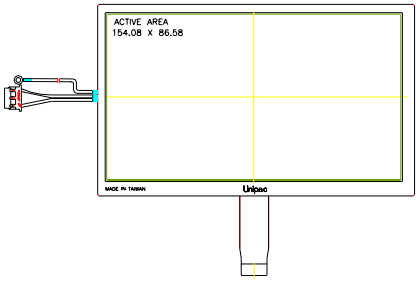
Note 2: Definition of scanning direction.
Refer to figure as below:



Note 3: MOD = H: Simultaneous sampling.
MOD = L: Sequential sampling.
Please set CPH2 and CPH3 to GND when MOD = H.

b. Backlight driving section (Refer to Fig. 1)

No.	Symbol	I/O	Description	Remark
1	HI	I	Power supply for backlight unit (High voltage)	
2	GND	-	Ground for backlight unit	

2. Absolute maximum ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	V_{CC}	GND=0	-0.3	7	V	
	AV_{DD}	AV_{SS}=0	-0.3	7	V	
	V_{GH}	GND=0	-0.3	18	V	
	V_{GL}		-15	0.3	V	
	V_{GH}□V_{GL}		-	33	V	
Input signal voltage	V_i		-0.3	AV_{DD}+0.3	V	Note 1
	V_I		-0.3	V_{CC}+0.3	V	Note 2
	VCOM		-2.9	5.2	V	
Operating Temperature 1 (Ambient temperature)	Topa1		-30	70	□	Note 3
Operating Temperature 2 (Surface temperature)	Topa2		-30	85	□	Note 4
Storage temperature (Ambient temperature)	Tstg		-30	85	□	

Note 1: VR, VG, VB.
Note 2: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.
Note 3: Under the condition of the operating temperature, the panel would be function normal for the visual display only.
Note 4: The backlight has been turned on before running about condition.

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3. Electrical characteristics

a. Typical operating conditions (GND=AVss=0V, Note 4)

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply		V_{CC}	3	5	5.2	V	
		AV_{DD}	4.8	5	5.2	V	
		V_{GH}	14.3	15	15.7	V	
		V_{GL}	-10.5	-10	-9.5	V	
Video signal amplitude (VR,VG,VB)		V_{iA}	0.4	-	AV_{DD}-0.4	V	Note 1
		V_{iAC}	-	3	-	V	AC component
		V_{iDC}	-	AV_{DD}/2	-	V	DC component
VCOM		V_{CAC}	3.5	5.6	6.5	Vp-p	AC component, Note 2
		V_{CDC}	1.6	1.7	1.8	V	DC component
Input signal voltage	H Level	V_{IH}	3	-	V_{CC}	V	Note 3
	L Level	V_{IL}	0	-	1	V	

Note 1: Refer to Fig.4- (a).

Note 2: The brightness of LCD panel could be changed by adjusting the AC component of VCOM.

Note 3: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.

Note 4: Be sure to apply GND, V_{CC} and V_{GL} to the LCD first, and then apply V_{GH}.

b. Current consumption (GND=AVss=0V)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Current for driver	I_{GH}	V_{GH}=15V	-	0.20	0.5	mA	
	I_{GL}	V_{GL}=-10V	-	0.80	1.5	mA	
	I_{CC}	V_{CC}=5V	-	3.0	6.0	mA	
	I_{DD}	AV_{DD}=5V	-	17.0	30	mA	

c. Backlight driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp voltage	V_L	-	580	638	Vrms	
Lamp current	I_L	-	6.5	7	mArms	
Frequency	F_L	-	60	80	kHz	Note 4
Lamp starting voltage	V_S	-	-	850	Vrms	Note 1,5
		-	-	1150	Vrms	Note 2,5
		-	-	1300	Vrms	Note 3,5
Lamp life time		10000	-	-	Hr	Note 6

Note 1: Ta = 25□.

Note 2: Ta = 0□.

Note 3: Ta = -30□.

Note 4: The lamp frequency should be selected as different as possible from display horizontal synchronous signal to avoid interference.

Note 5: For starting the backlight unit, the output voltage of DC/AC's transformer should be larger than the maximum lamp starting voltage.

Note 6: The" Lamp life time" is defined as the module brightness decrease to 50% original brightness at Ta=25□, I_L=6.5mA.

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4. AC Timing

a. Timing conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
Rising time	t_r	-	-	10	ns	Note 1
Falling time	t_f	-	-	10	ns	Note 1
High and low level pulse width	t_{CPH}	99	103	107	ns	CPH1~CPH3
CPH pulse duty	t_{CWH}	40	50	60	%	CPH1~CPH3
CPH pulse delay	t_{C12} t_{C23} t_{C31}	30	$t_{CPH}/3$	$t_{CPH}/2$	ns	CPH1~CPH3
STH setup time	t_{SUH}	20	-	-	ns	STHR,STHL
STH hold time	t_{HDH}	20	-	-	Ns	STHR,STHL
STH pulse width	t_{STH}	-	1	-	t_{CPH}	STHR,STHL
STH period	t_H	61.5	63.5	65.5	μs	STHR,STHL
OEH pulse width	t_{OEH}	-	1.22	-	μs	OEH
Sample and hold disable time	t_{DIS1}	-	8.28	-	μs	
OEV pulse width	t_{OEV}	-	5.40	-	μs	OEV
CKV pulse width	t_{CKV}	-	4.18	-	μs	CKV
Clean enable time	t_{DIS2}	-	3.74	-	μs	
Horizontal display start	t_{SH}	-	0	-	$T_{CPH}/3$	
Horizontal display timing range	t_{DH}	-	1440	-	$T_{CPH}/3$	
STV setup time	t_{SUV}	400	-	-	ns	STVL,STVR
STV hold time	t_{HDV}	400	-	-	ns	STVL,STVR
STV pulse width	t_{STV}	-	-	1	t_H	STVL,STVR
Horizontal lines per field	t_V	256	262	268	t_H	Note 2
Vertical display start	t_{SV}		3	-	t_H	
Vertical display timing range	t_{DV}		234	-	t_H	
VCOM rising time	t_{rCOM}		-	5	μs	
VCOM falling time	t_{fCOM}		-	5	μs	
VCOM delay time	t_{DCOM}		-	3	μs	
RGB delay time	t_{DRGB}		-	1	μs	

Note 1: For all of the logic signals.

Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even field simultaneously.

b. Timing diagram

Please refer to the attached drawing, from Fig.2 to Fig.6.

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C. Optical specification (Note 1, Note 2, Note 3)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response time	Rise Fall	Tr Tf	θ=0°	- -	25 30	50 60	ms ms	Note 4,6
Contrast ratio		CR	At optimized Viewing angle	60	150	-		Note 5,6
Viewing angle	Top Bottom Left Right		CR□10	20 40 45 45	30 60 60 60	- - - -	deg.	Note 6,7
Brightness		Y_L	θ=0°	300	400	-	nit	Note 8
White chromaticity		X	θ=0°	0.26	0.31	0.36		Note 8
		Y	θ=0°	0.28	0.33	0.38		

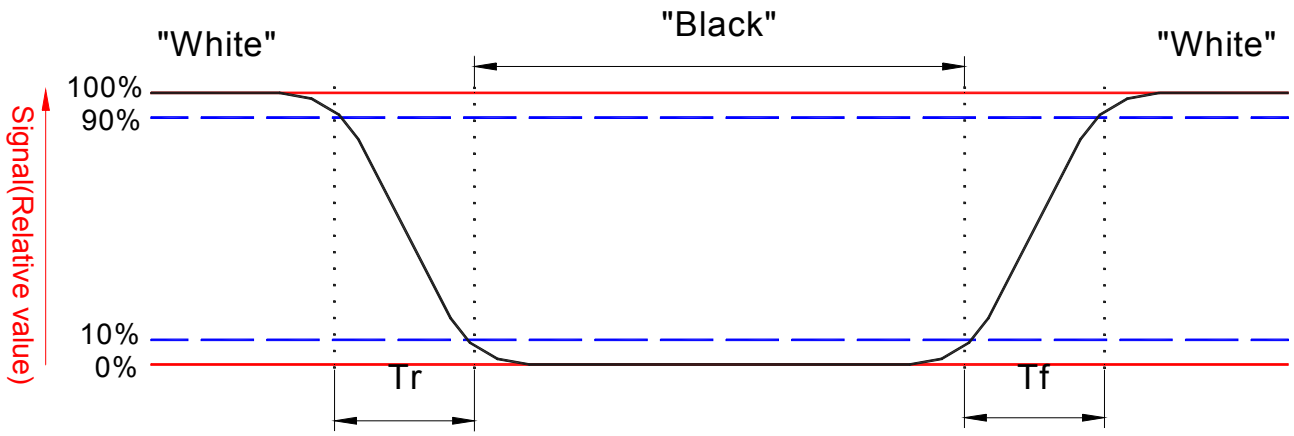
Note 1 : Ambient temperature =25□. And lamp current I_L = 6.5 mArms.

Note 2: To be measured in the dark room.

Note 3 :To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7, after 10 minutes operation.

Note 4. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



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Note 5. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector output when LCD is at “Black” state}}$$

Note 6. Note 4. White $Vi=V_{i50}$ □ 1.5V

Black $Vi=V_{i50} \pm 2.0V$

“±” means that the analog input signal swings in phase with V_{COM} signal.

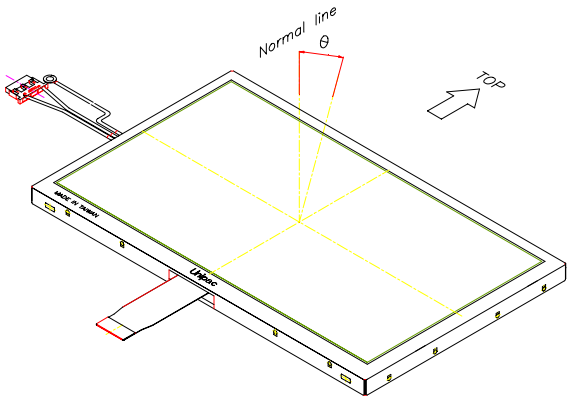
“ $\overline{+}$ ” means that the analog input signal swings out of phase with V_{COM} signal.

V_{i50} : The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 7. Definition of viewing angle:

Refer to figure as below.



Note 8. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

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D. Reliability test items:

No.	Test items	Conditions	Remark
1	High temperature storage	Ta= 85□ 240Hrs	
2	Low temperature storage	Ta= -30□ 240Hrs	
3	High temperature operation	Ta= 70□ 240Hrs	
4	Low temperature operation	Ta= -20□ 240Hrs	
5	High temperature and high humidity	Ta= 60□, 95% RH 240Hrs	Operation
6	Heat shock	-30□~85□/50 cycles 2Hrs/cycle	Non-operation
7	Electrostatic discharge	±200V,200pF(0Ω), once for each terminal	Non-operation
8	Vibration	Frequency range : 10~55Hz Stoke : 1.5mm Sweep : 10 ~ 55 ~ 10Hz 2 hours for each direction of X,Y,Z (6 hours for total)	JIS C7021, A-10 condition A
9	Mechanical shock	100G, 6ms, ±X,±Y,±Z 3 times for each direction	JIS C7021, A-7 condition C
10	Vibration (with carton)	Random vibration: $0.015G^2$/Hz from 5~200Hz –6dB/octave from 200~500Hz	IEC 68-34
11	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	JIS Z0202

Note: Ta: Ambient temperature.

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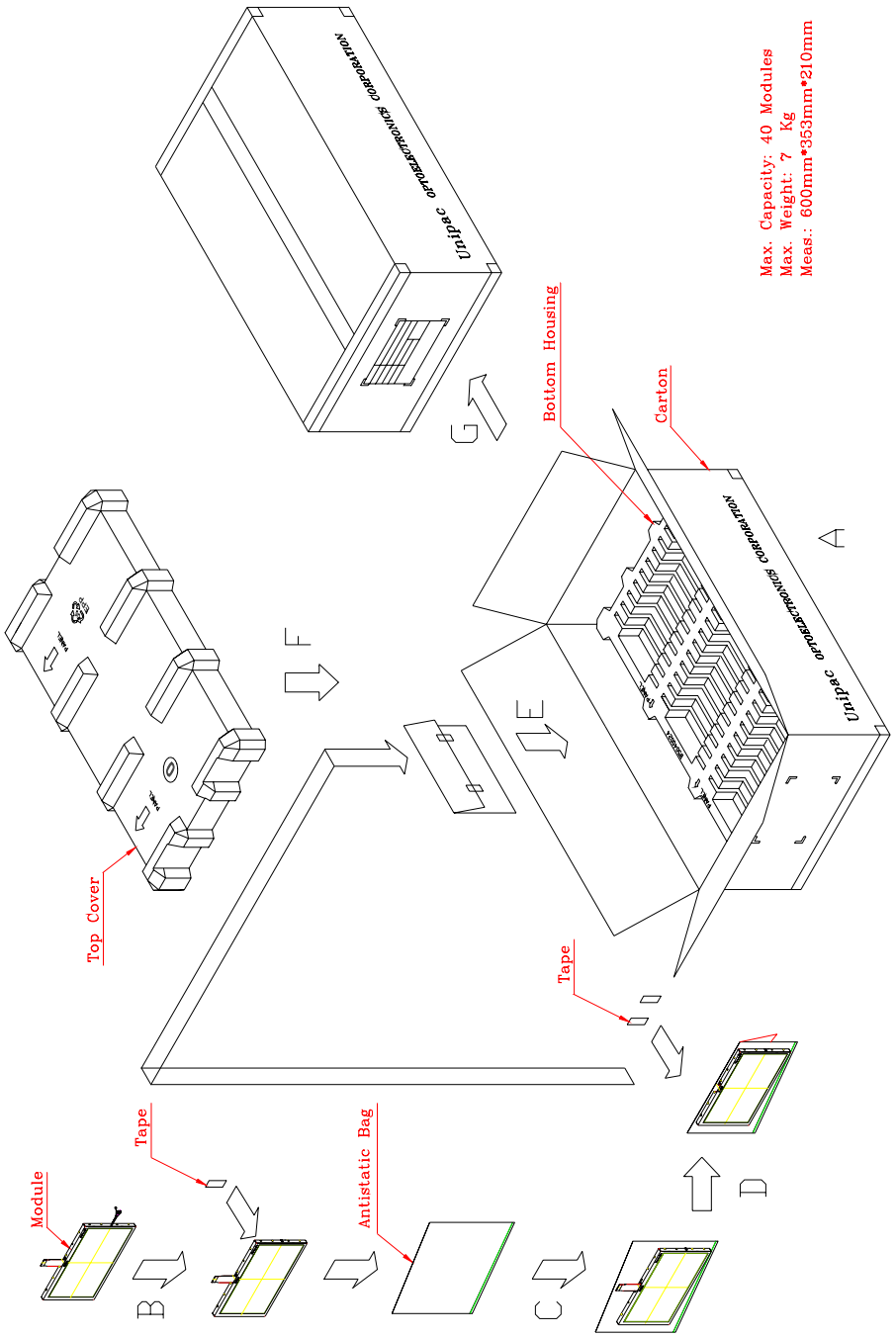
E. Packing form



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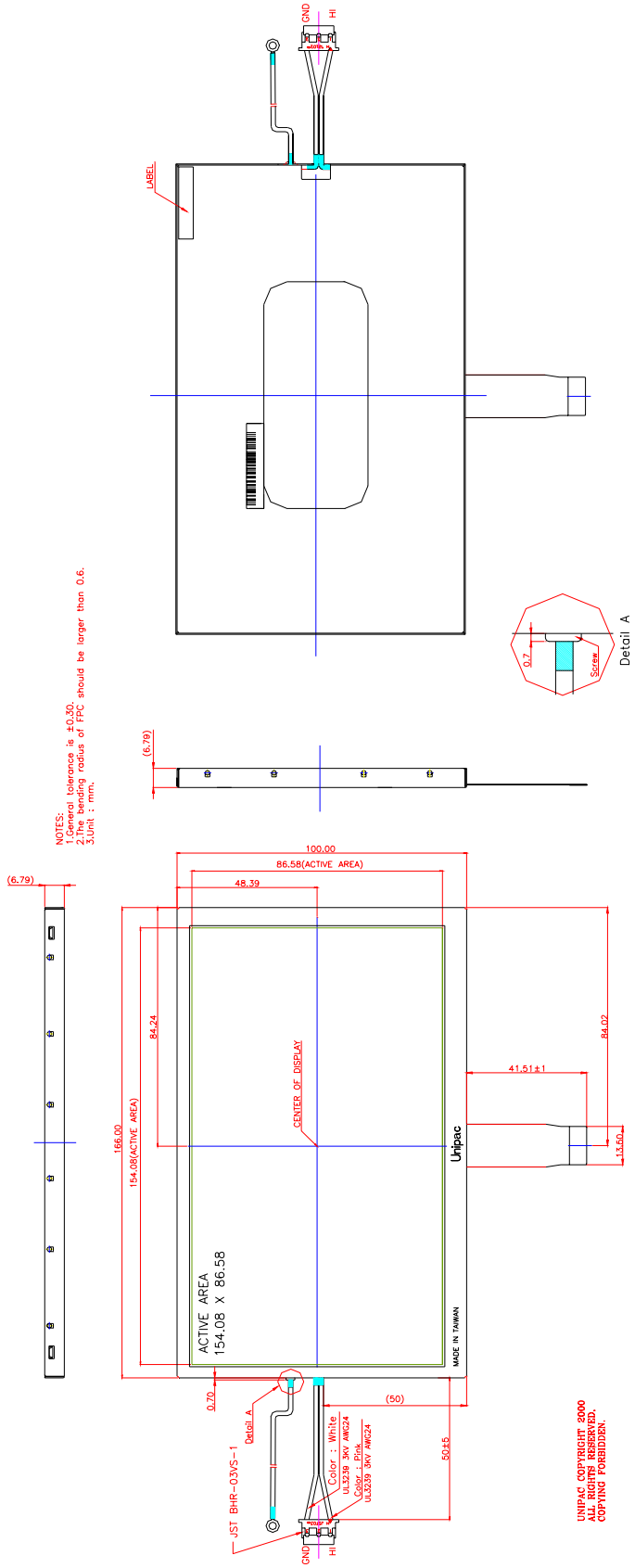

Fig.1 Outline dimension of TFT-LCD module

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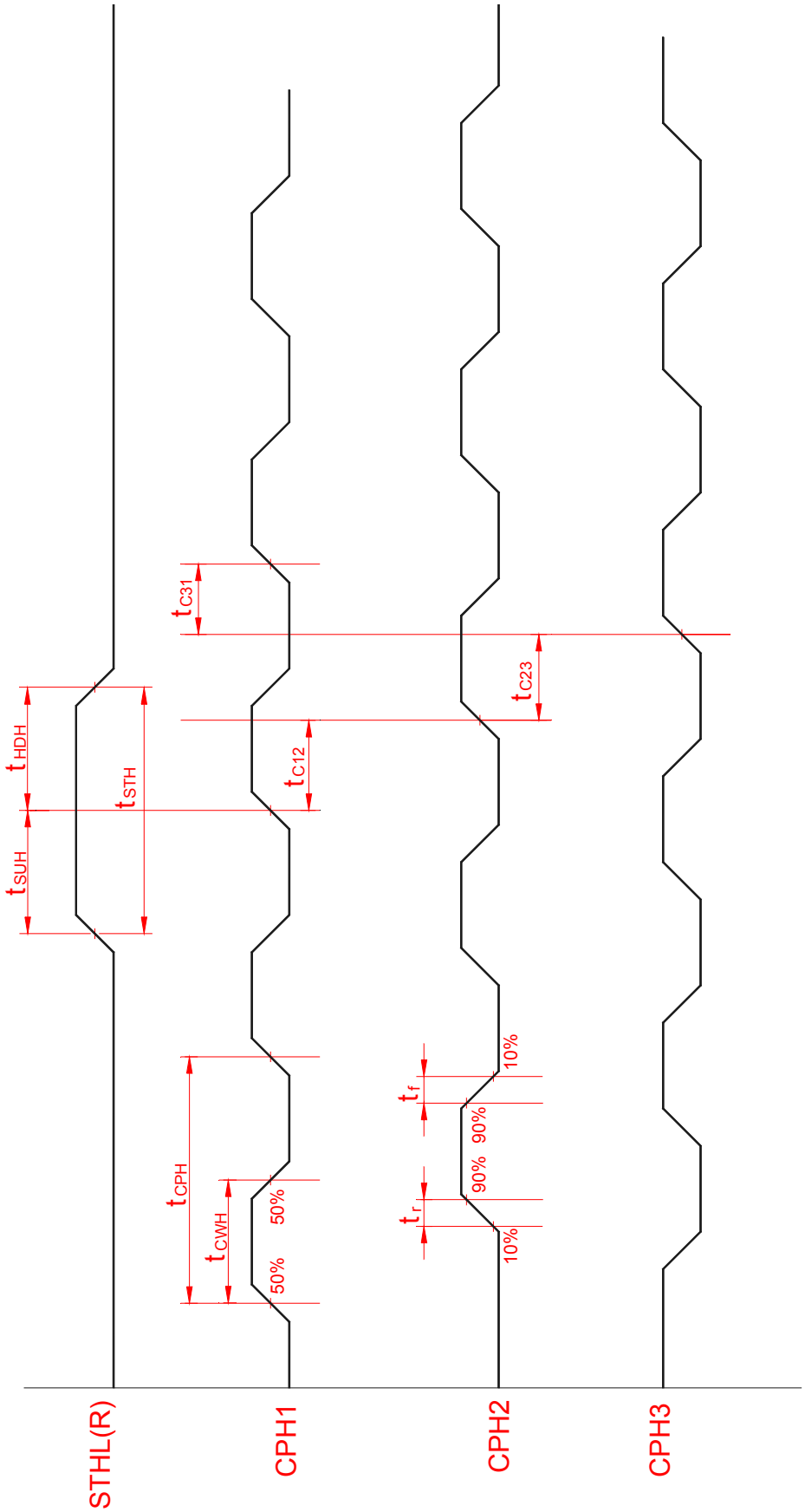

Fig.2 Sampling clock timing

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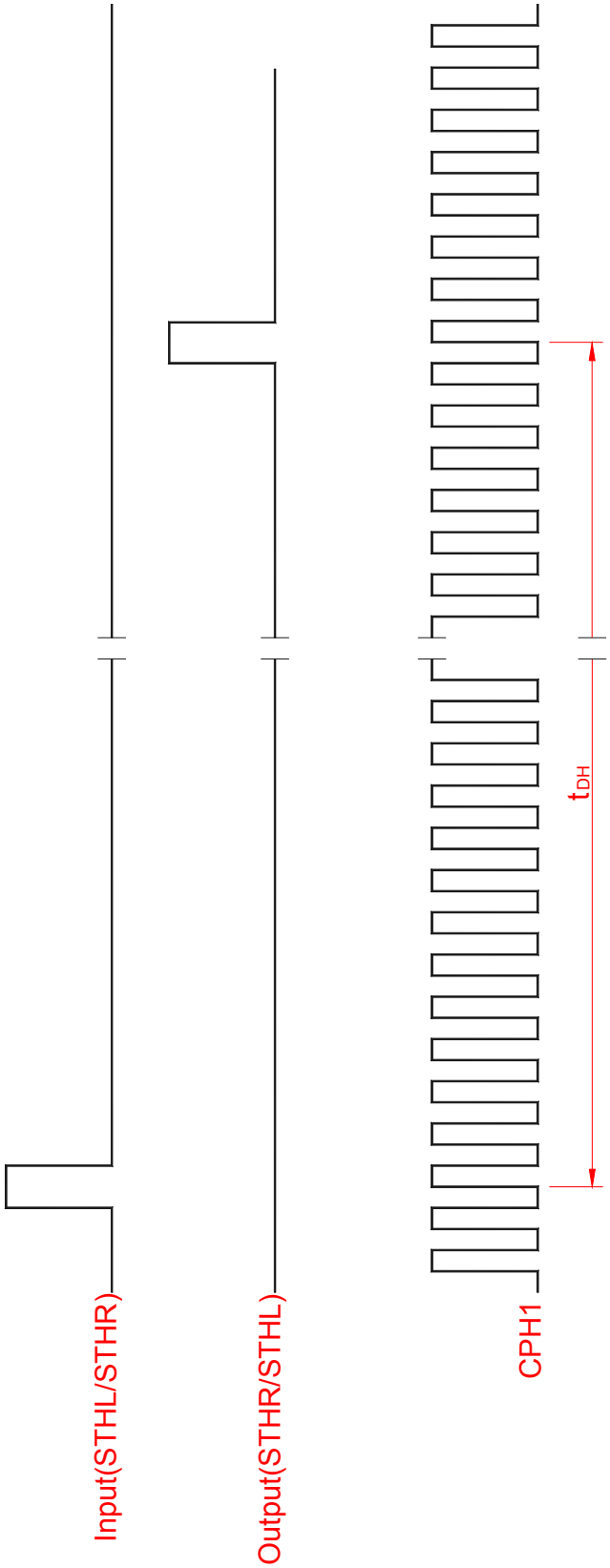

Fig.3 Horizontal display timing range

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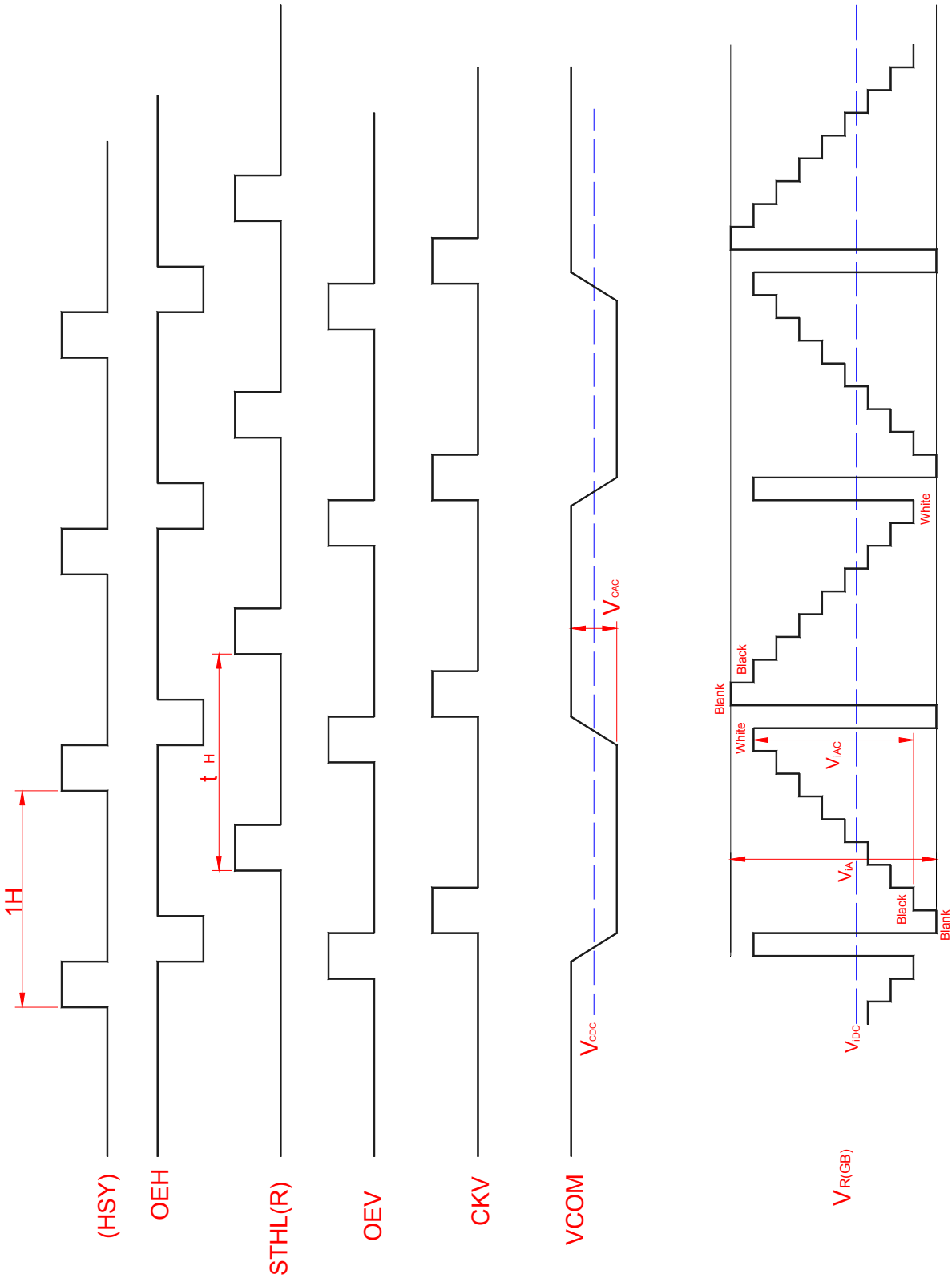

Fig.4-(a) Horizontal timing

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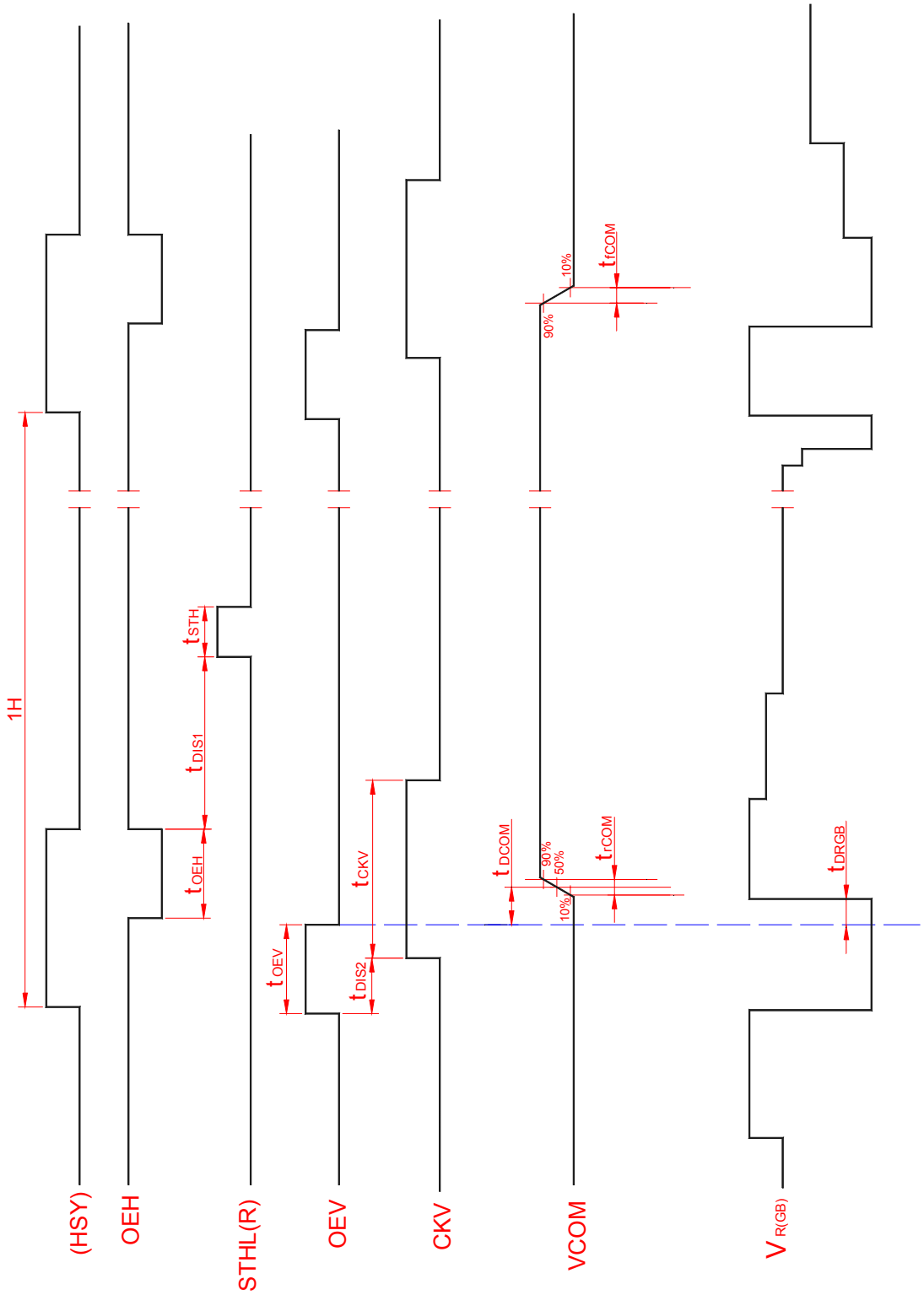

Fig.4-(b) Detail horizontal timing

Note: The falling edge of OEV should be synchronized with the falling edge of OEH

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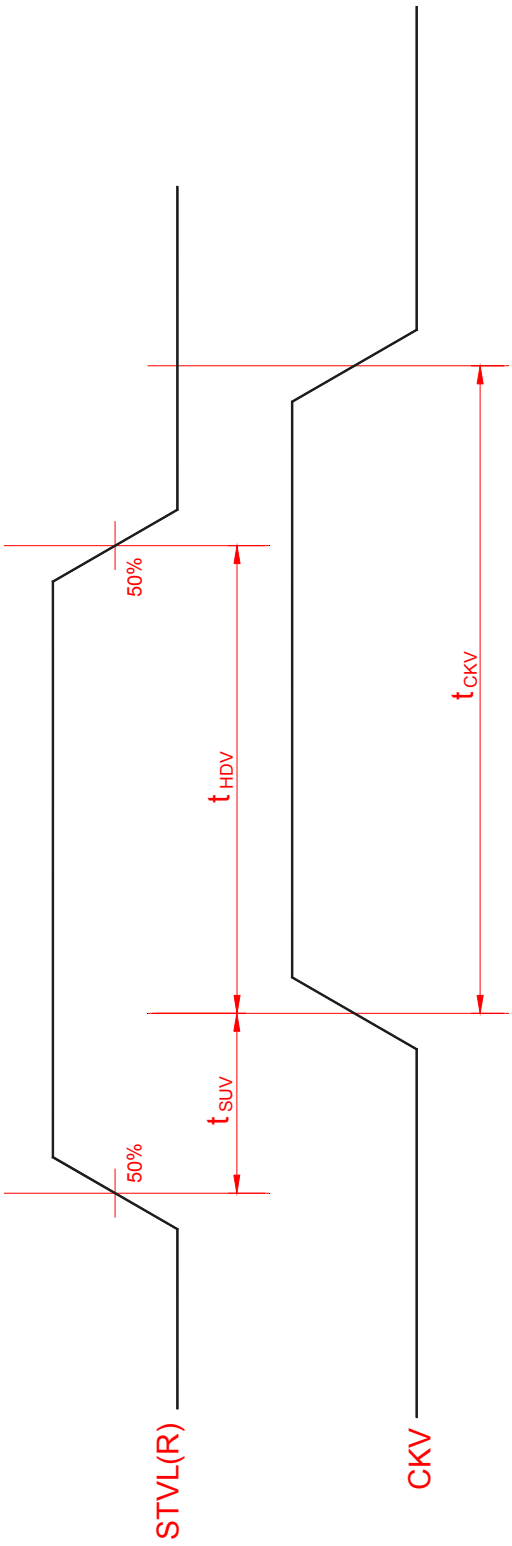

Fig.5 Vertical shift clock timing

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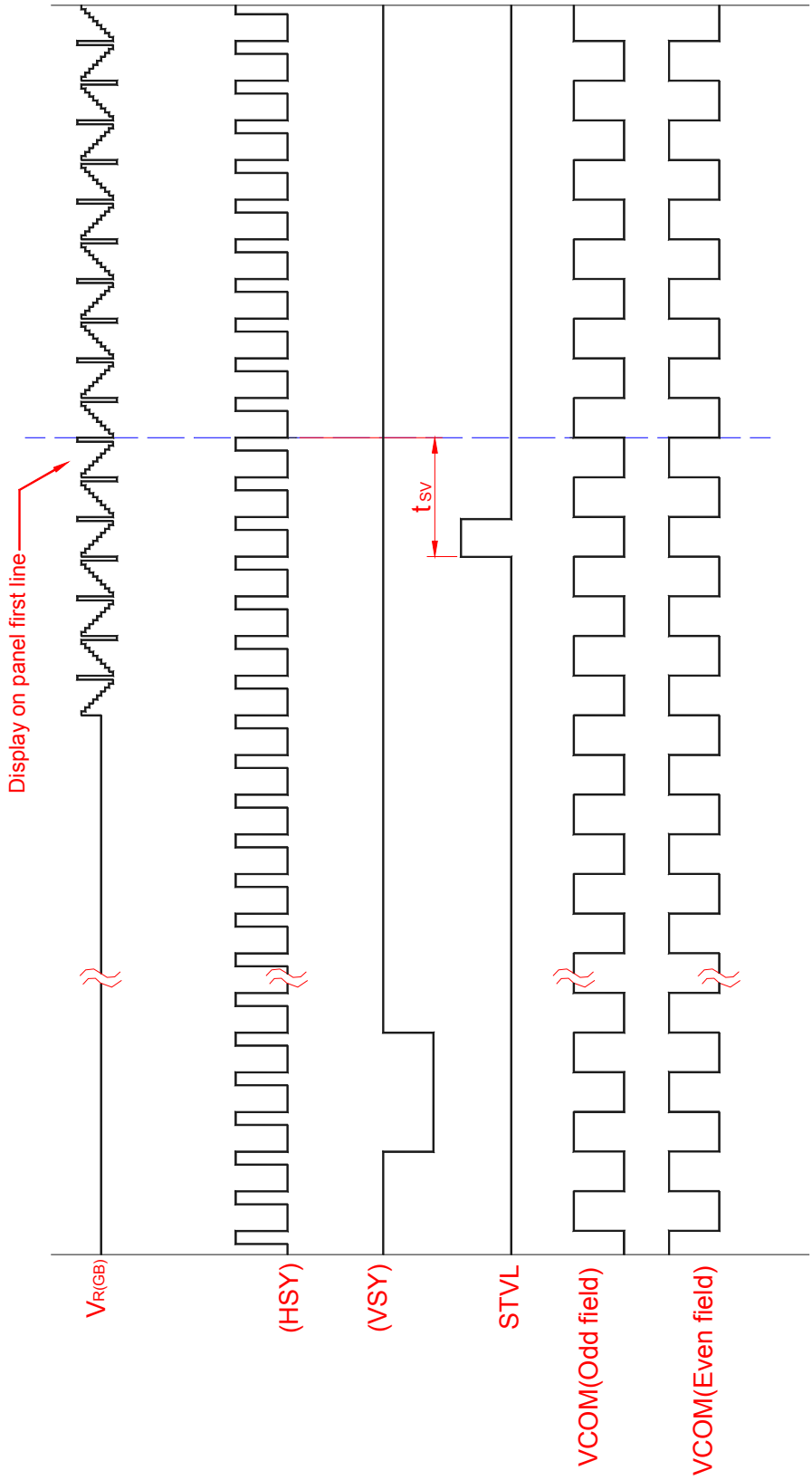

Fig.6-(a) Vertical timing (From up to down)

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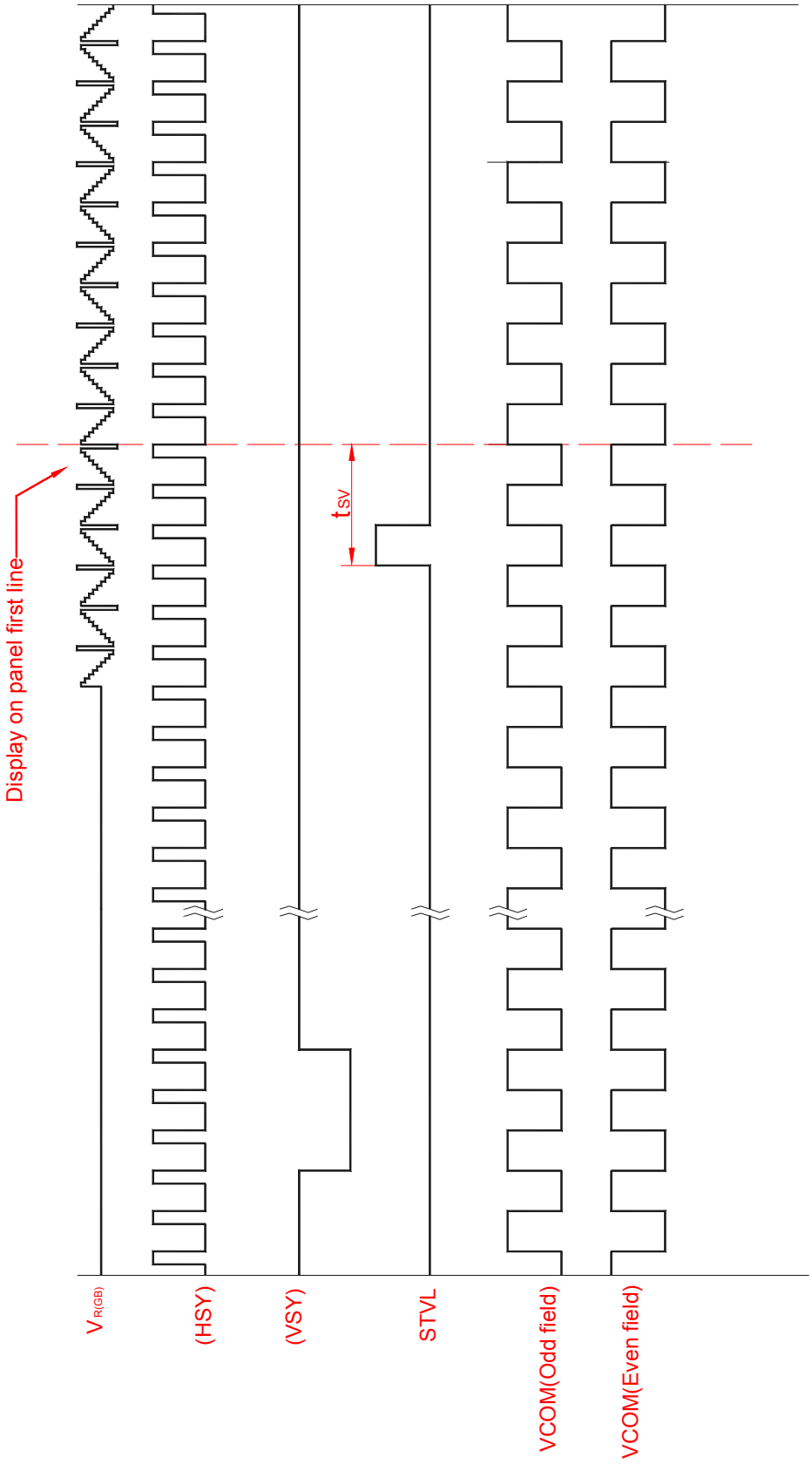

Fig.6-(b) Horizontal timing (From down to up)

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