Chunghwa Picture Tubes, Ltd.
Technical Specification

To ： SCL

Date ： 2011/05/31

CPT TFT-LCD

CLAA101NC05

ACCEPTED BY：

(TENTATIVE)

APPROVED BY	CHECKED BY	PREPARED BY
		Product Application Division

Niche Business Division

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Modification Record List

Revision Notice	Description	Rev. Date
1.0	First revision(Tentative)	2011/05/31

Table of Content

1. OVERVIEW

CLAA101NC05 is 10.1" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, LVDS driver ICs, control circuit and backlight. By applying 6 bit digital data, 1024×RGB (3) ×600, 262K-color images are displayed on the 10.1" diagonal screen. general specifications are summarized in the following table :

ITEM	SPECIFICATION
Display Area	222.72(H)×125.28(V)(mm) (10.1-inch diagonal)
Number of Pixels	1024×3(H)×600 (V)
Pixel Pitch	0.2175 (H)×0.2088 (V) (mm)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white
Number of Colors	262,144(6bits) (LVDS)
Gamut	(45)%(Min)/ (50)%(Typ)
Optimum Viewing Angle	6 o'clock
Response Time	(20)ms (Typ)/(40) ms(Max)
Surface Treatment	Anti Glare 3H
Viewing Angle	40°、40°/ 15°、30°(Min)
Brightness	(160) cd/m^2 (5point) (Min)/(200) cd/m^2 (Typ) (5point)
Uniformity	5point：80%(Min) 13point：70 %(Min)
Consumption of Power	(2.1) W (Typ)
Module Size	235.5 (W)×143.5 (H)×5.2 (D) (mm) (Max)
Module Weight	(190) g (Max)

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cable, and nuclear reactor control system and life support systems. If customers intend to use these LCD products for applications listed above or those not included in the "Standard" list as follows, please contact our sales in advance.

Standard：Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tool, Industrial robot, Audio and Visual equipment, Other consumer products.

2. ABSOLUTE MAXIMUM RATINGS

The following are maximum value, which if exceeded, may cause faulty operation or damage to the unit.

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE
LCD Power Voltage	VCC	0	4.0	V	
Operation Temperature	Top	0	50	℃	*1).*2).* 3).*4)
Storage Temperature	Tstg	-25	65	℃	*1).*2).*3)

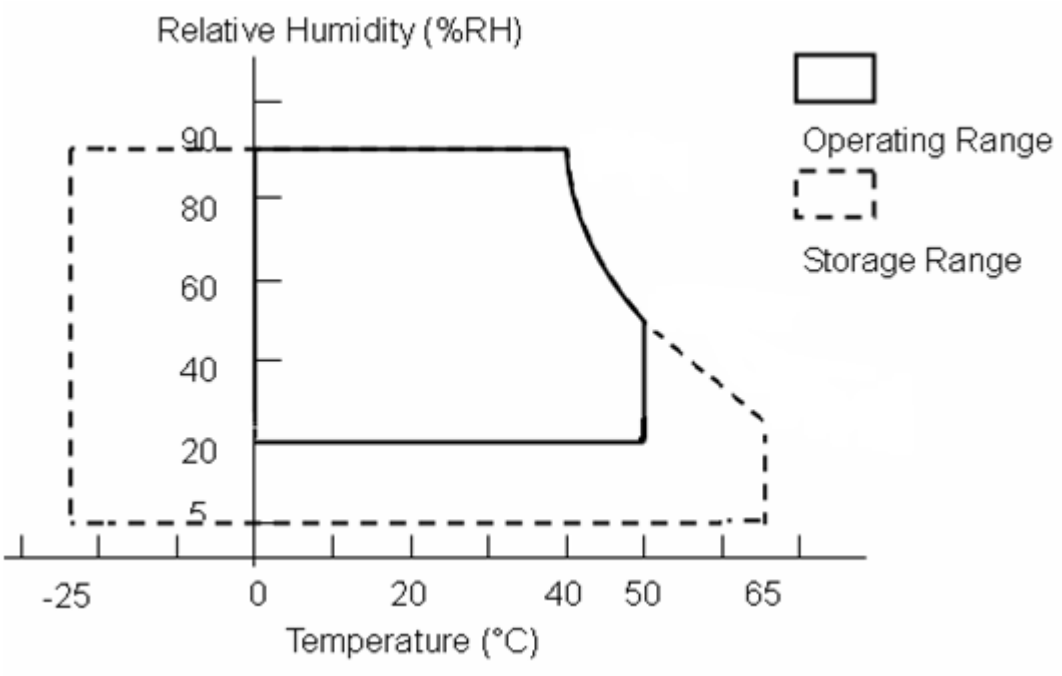
【Note】

*1) The relative temperature and humidity range are as below sketch, 90%RH Max. (Ta≦40℃)

*2) The maximum wet bulb temperature ≦39℃(Ta＞40℃) and without dewing.

*3) If product in environment which over the definition of the relative temperature and humidity out of range too long, it will affect visual of LCD.

*4) If you operate LCD in normal temperature range, the center surface of panel should be under 50℃.



3. ELECTRICAL CHARACTERISTICS

(A) TFT LCD

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LCD Power Voltage		VCC	3.0	3.3	3.6	V	*1)
LCD Power Current		ICC	-	(160)	(170)	mA	*2)
Rush Current		Irush	-	-	2	A	*4)
Logic Input Voltage (LVDS: IN+,IN-)	Common Voltage	VCM	$\frac{\lvert VID \rvert}{2}$	-	$2.4-\frac{\lvert VID \rvert}{2}$	V	*3)
	Differential Input Voltage	｜VID｜	200	-	600	mV	*3)
	Threshold Voltage (HIGH)	VTH	-	-	100	mV	*3) When VCM = +1.2V
	Threshold Voltage (LOW)	VTL	-100	-	-	mV	

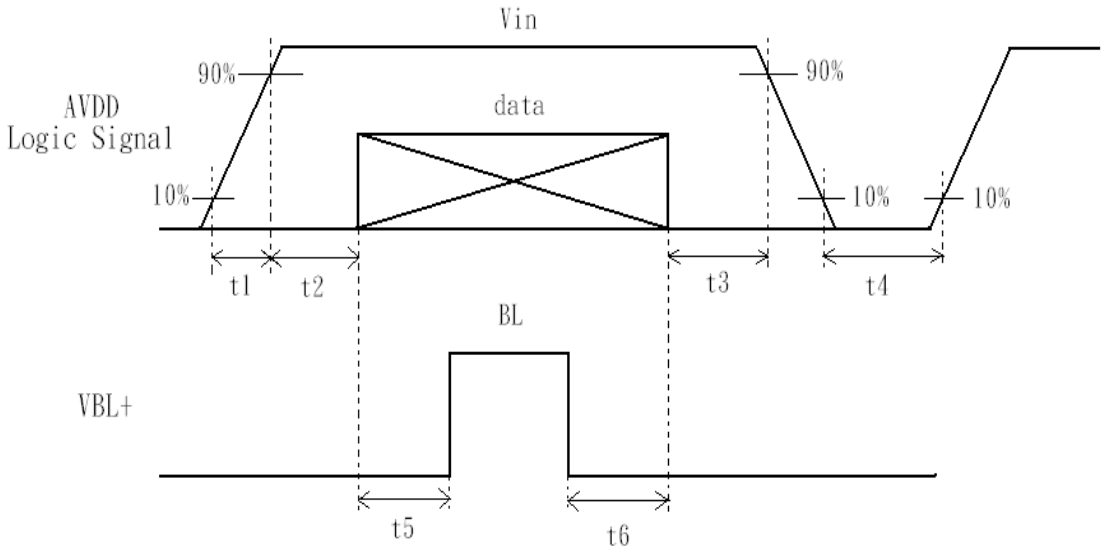
【Note】

*1) Power Sequence：

0.50 ms ≦ t1 ≦ 10 ms　　500 ms≦t4

0.01 ms ＜ t2 ≦ 50 ms　　200 ms≦t5

0.01 ms ＜ t3 ≦ 50 ms　　200 ms≦t6

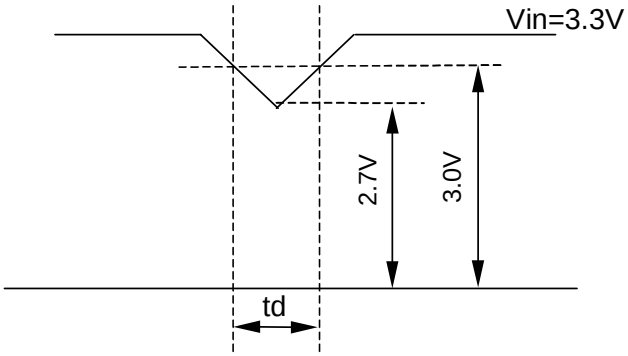


data:RGB DATA, DCLK, HD, VD, DENA

VCC-dip state

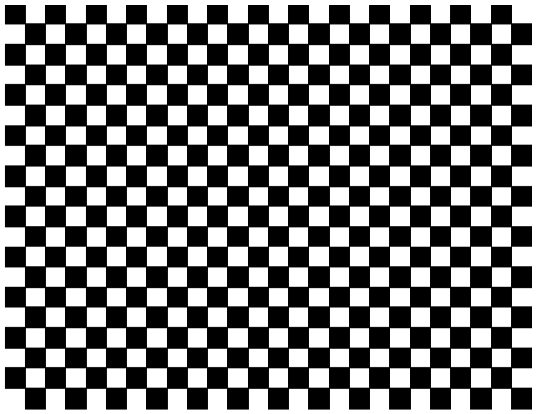
(1)when 3.0V ＞ VCC ≧ 2.7V，td≦10 ms.

(2)when VCC ＜ 2.7V，VCC-dip condition should as the VCC-turn-off condition.



*2) Typical value is Mosaic (32*36 Checker board) Pattern：768 line mode.

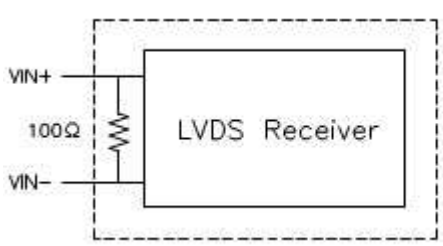
Circuit condition (Typ)：VCC=3.3 V，f_V=60 Hz，f_H=48.35 kHz，f_{CLK}=58.03 MHz.

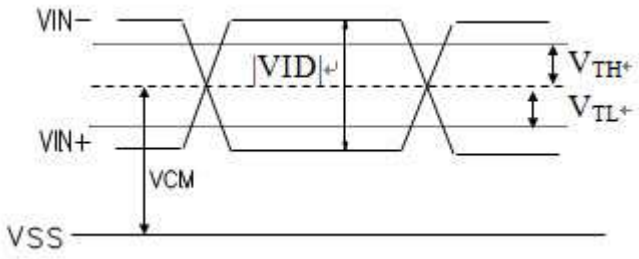


Max value is Black Pattern：768 line mode.

Circuit condition (Max)：VDD=3.3 V，f_V=60 Hz，f_H=48.35 kHz，f_{CLK}=58.03 MHz.

*3) LVDS Signal Definite：

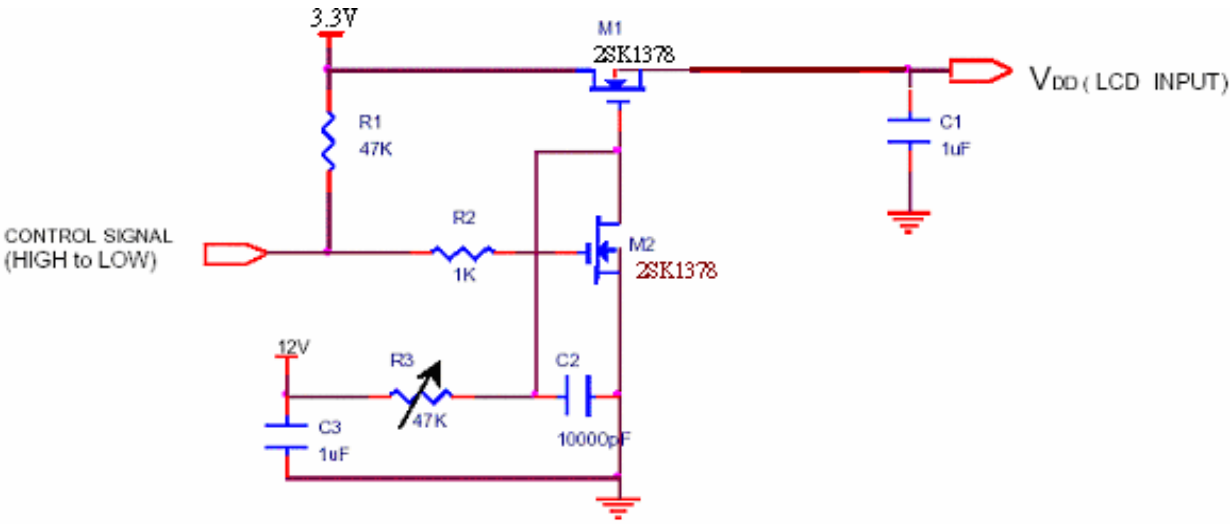


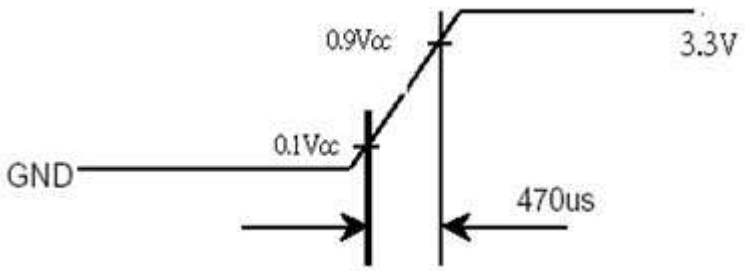


VIN+：Positive differential DATA & CLK Input

VIN-：Negative differential DATA & CLK Input

*4) Irush measure condition





(B) BACK LIGHT

(a.) ELECTRICAL CHARACTERISTICS

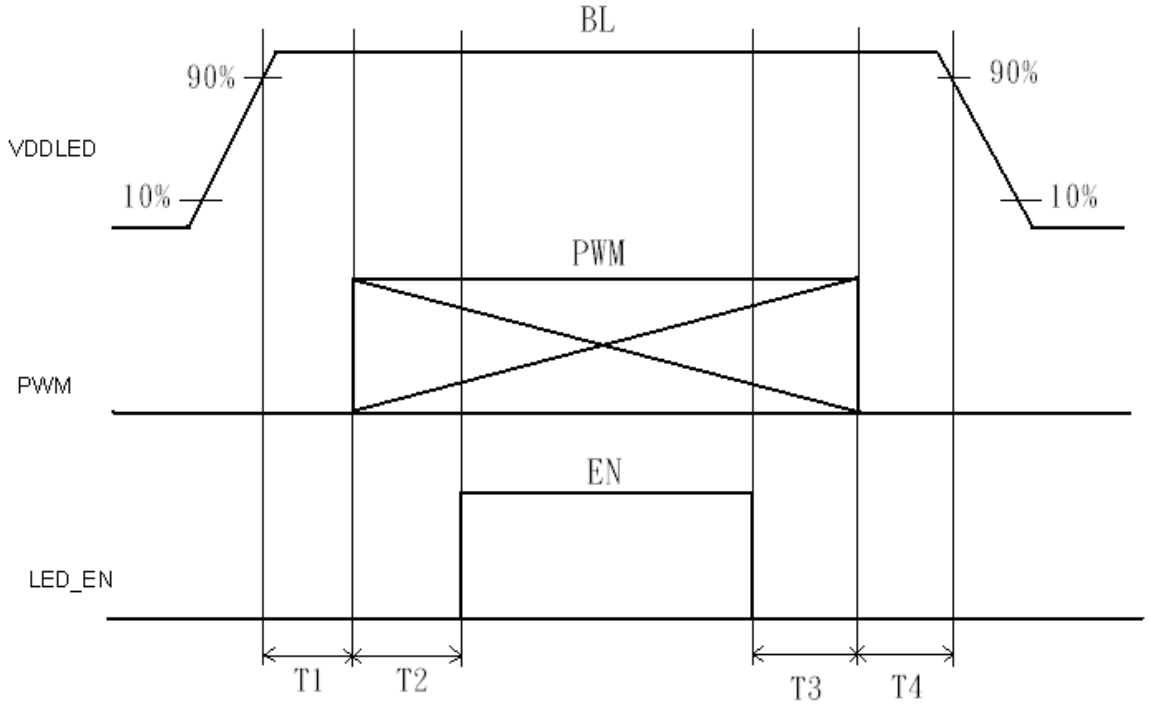
Ta=25℃

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LED Driver Input Voltage	VBL+	5	12	19	V	
LED Driver Input Current	IBL+	-	133	360	mA	*1)
Forward Voltage	V_F	2.9	3.2	3.5	V	*2) I_F=20mA
Forward Current	I_F	19.5	20	20.5	mA	*2) I_F=20mA
Power Consumption	PLED	1.43	1.58	1.73	W	*2)*3) I_F=20mA
PWM Frequency	PWM_BL	180	200	220	Hz	
Duty ratio	Dim	5	-	100	%	

(b.) LED LIFE – TIME

ITEM	CONDITION	MIN	TYP	MAX	UNIT	NOTE
Life Time	I_F=20mA、Ta=25℃	15000			hrs	*4)

(c.) LED ON/OFF Sequence：



10ms ≦ T1　　0ms ≦ T3

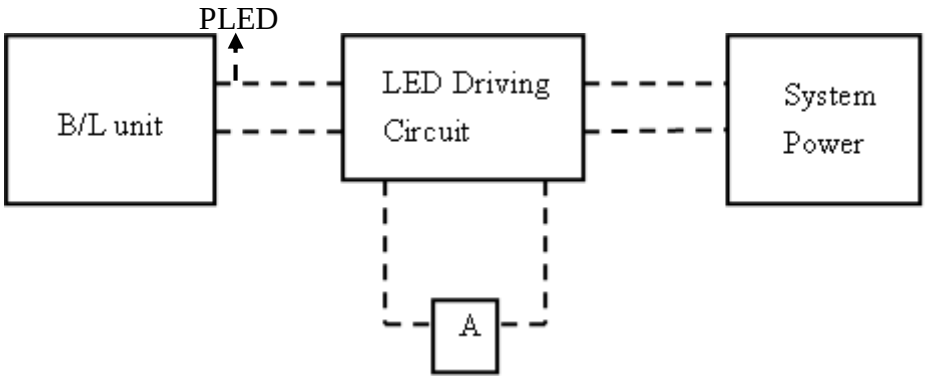
10ms ≦ T2　　10ms ≦ T4

*1)Typical value；VBL+ =12V，Duty=100%。

Max value；VBL+=5V，Duty=100%。

*2)Measure method：a. LED current is measured by utilizing a current meter as show below.

b. we set up system power input voltage at 12v to measurement PLED.



*3) Calculator value for reference IF*VF=PLED

*4)Life time means that estimated time to 50% degradation of initial luminous intensity.

4. Connector Interface PIN & Function

CN (Interface signal)

Connector type： CN1(Input signal) JAE_HD1S04HA 1-R6000 / TYCO5-2069716-3 or compatible

Pin No.	SYMBOL	FUNCTION
1	NC	NC
2	V_{CC}	+3.3V Power
3	V_{CC}	+3.3V Power
4	V_EDID	EDID 3.3V Power
5	NC	NC
6	CLK_EDID	EDID Clock
7	DATA_EDID	EDID Data
8	RXIN0-	LVDS Signal(-)—channel 0
9	RXIN0+	LVDS Signal(+)—channel 0
10	GND	Ground
11	RXIN1-	LVDS Signal(-)—channel 1
12	RXIN1+	LVDS Signal(+)—channel 1
13	GND	Ground
14	RXIN2-	LVDS Signal(-)—channel 2
15	RXIN2+	LVDS Signal(+)—channel 2
16	GND	Ground
17	RXCLKIN-	LVDS Clock Signal(-)
18	RXCLKIN+	LVDS Clock Signal(+)
19	GND	Ground
20	NC	NC
21	NC	NC
22	GND	Ground
23	NC	NC
24	NC	NC
25	GND	Ground
26	NC	NC
27	NC	NC
28	GND	Ground
29	NC	NC
30	NC	NC
31	VSSLED	Ground – LED
32	VSSLED	Ground – LED
33	VSSLED	Ground – LED
34	NC	NC
35	PWM	System PWM Signal Input (+3.3V Swing)
36	LED_EN	LED enable pin (+3.3V Input)
37	NC	NC(Please let it floating for CPT test only)
38	V_{LED}	Power Supply for LED(V_{LED} =5V ~ 19V)
39	V_{LED}	Power Supply for LED(V_{LED} =5V ~ 19V)
40	V_{LED}	Power Supply for LED(V_{LED} =5V ~ 19V)

5. INTERFACE TIMING CHART

(A) Timing Chart

ITEM				SYNBOL	MIN	TYP	MAX	UNIT
LCD Timing	Frame Rate			-	(60)	(60)	(60)	Hz
	DCLK		Frequency	f_{CLK}	(40.8)	(51.2)	(67.2)	MHz
	DENA	Horizontal	Horizontal Total time	t_H	(1114)	(1344)	(1400)	t_{CLK}
			Horizontal Active time	t_{HA}	1024			t_{CLK}
			Horizontal Blank time	t_{HB}	(90)	(320)	(376)	t_{CLK}
		Vertical	Vertical Total time	t_V	(610)	(635)	(800)	t_H
			Vertical Active time	t_{VA}	600			t_H
			Vertical Blank time	t_{VB}	(10)	(35)	(200)	t_H

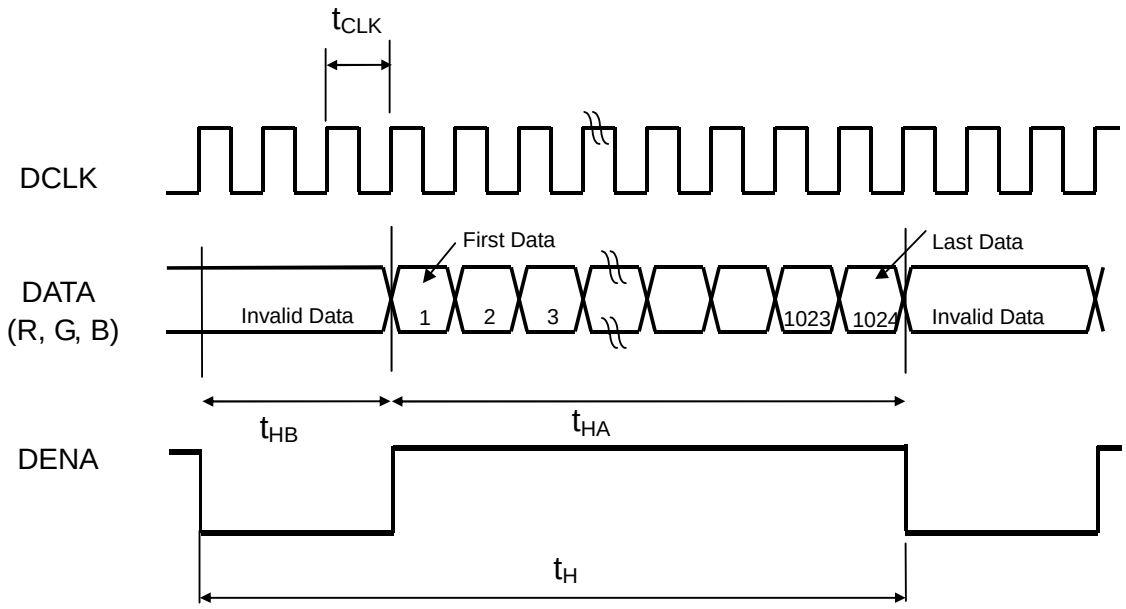
【Note】

*1) DENA (DATA ENABLE) usually is positive.

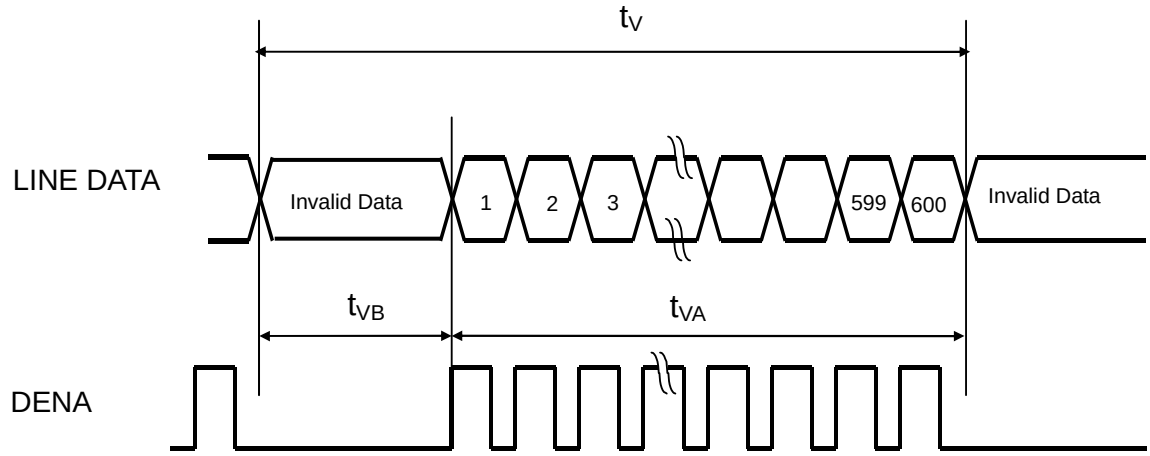
*2) During the whole blank period, DCLK should keep input.

(B) Time Sequence

(a.) Horizontal sequence



(b.) Vertical sequence



(3) DATA mapping

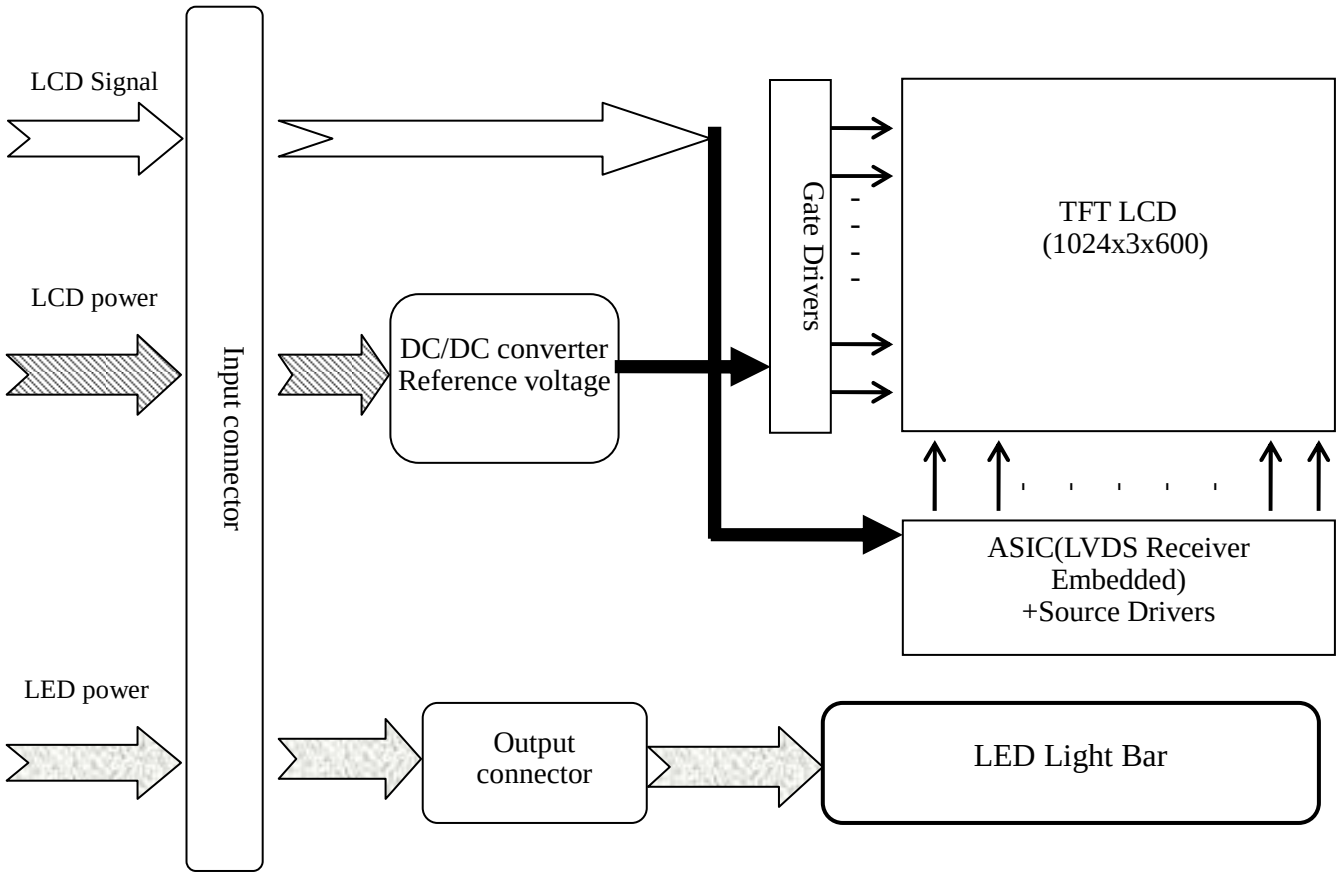
COLOR	INPUT DATA	R DATA						G DATA						B DATA					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(254)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	GREEN(254)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

【Note】

1) Gray level：Color(n)：n is level order; higher n means brighter level.

2) DATA：1: high，0: low

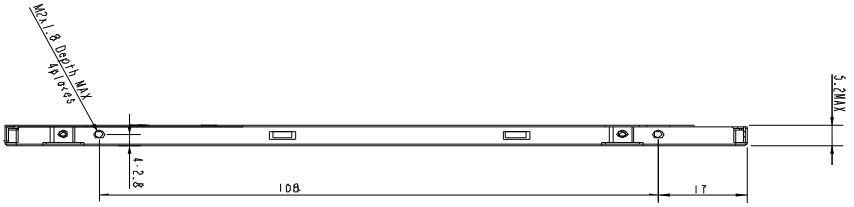
6. BLOCK DIAGRAM

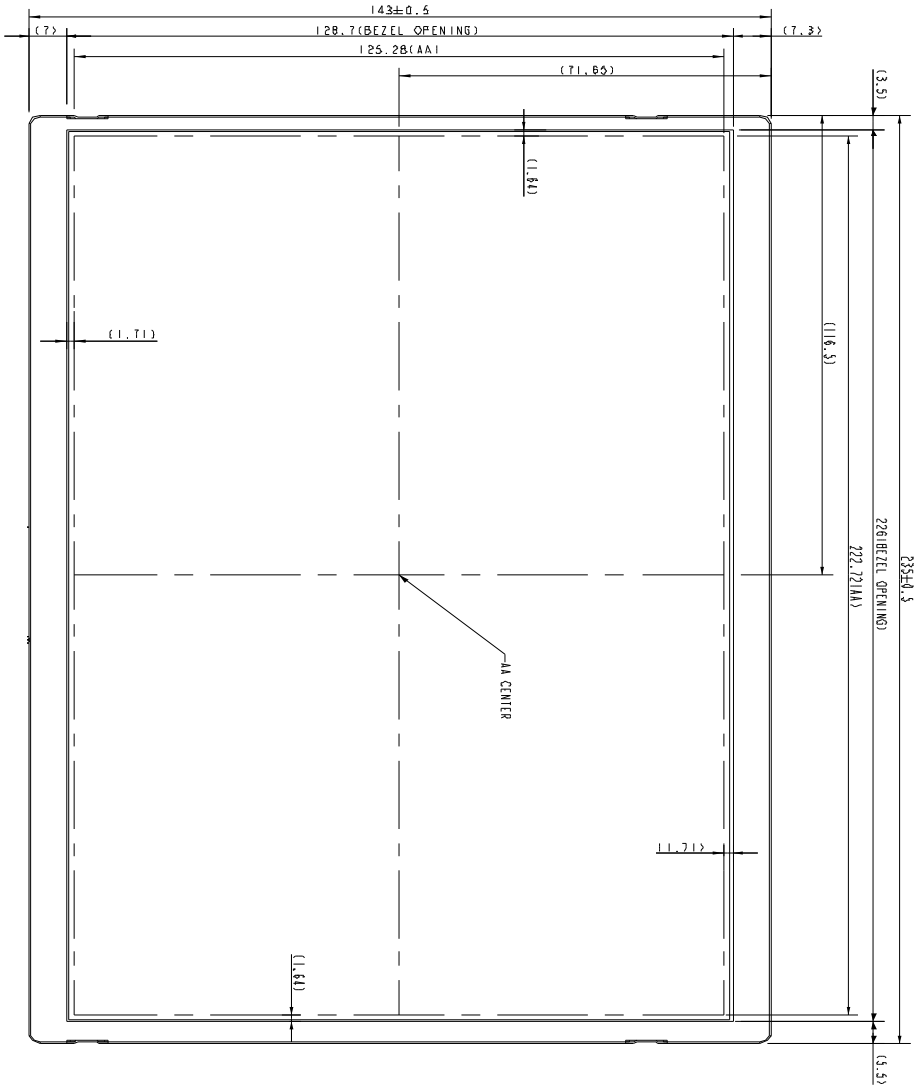


7. MECHANICAL SPECIFICATION

(1) Front side

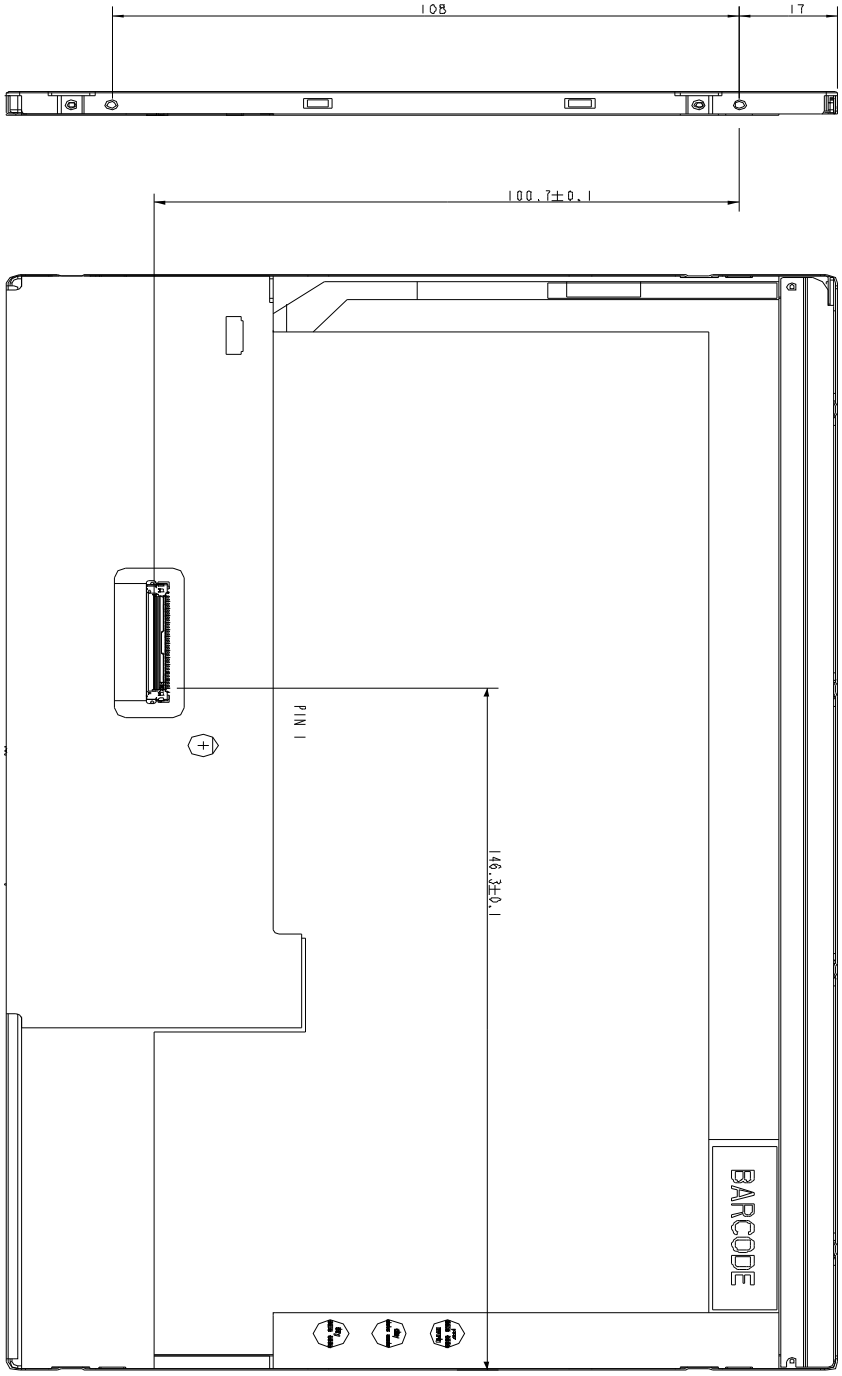
The tolerance, not show in the figure, is ±0.5 mm. **[Unit：mm]**





(2) Rear side

The tolerance, not show in the figure, is ±0.5 mm. **[Unit：mm]**



8. OPTICAL CHARACTERISTICS

Ta=25℃，VDD=3.3V

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	NOTE
Contrast Ratio		CR	θ=ψ= 0°	(400)	(500)	--	--	*1) 2)
Luminance (5P)		L	θ=ψ= 0°	(160)	(200)	--	cd/m^2	*1) 3)
Uniformity(5P)		ΔL	θ=ψ= 0°	(80)	--	--	%	*1) 3)
Uniformity(13P)		ΔL	θ=ψ= 0°	(70)	--	--	%	*1) 3)
Response Time		Tr	θ=ψ= 0°	--	(20)	(40)	ms	*5)
		Tf						
Cross Talk		CT	θ=ψ= 0°	--	--	1	%	*6)
View Angle	Horizontal	ψ	CR≧10	(40)/(-40)		--	°	*4)
	Vertical	θ		(15)/(-30)		--	°	*4)
Color Coordinate	W	x	θ=ψ= 0°	(0.283)	(0.313)	(0.343)		*3)
		y		(0.299)	(0.329)	(0.359)		
	R	x		TBD	(0.581)	TBD		
		y		TBD	(0.340)	TBD		
	G	x		TBD	(0.340)	TBD		
		y		TBD	(0.588)	TBD		
	B	x		TBD	(0.154)	TBD		
		y		TBD	(0.111)	TBD		
Gamut			θ=ψ= 0°	(45)	(50)	--	%	
Gamma		γ	GL	2.0	2.2	2.4		*7)

Color coordinate and color gamut are measured by SRUL1R, response time is measured by TRD-100, and all the other items are measured by BM-5A (TOPCON). All these items are measured under the dark room condition (no ambient light).

Measurement Condition: IL= 20mA (each LED)

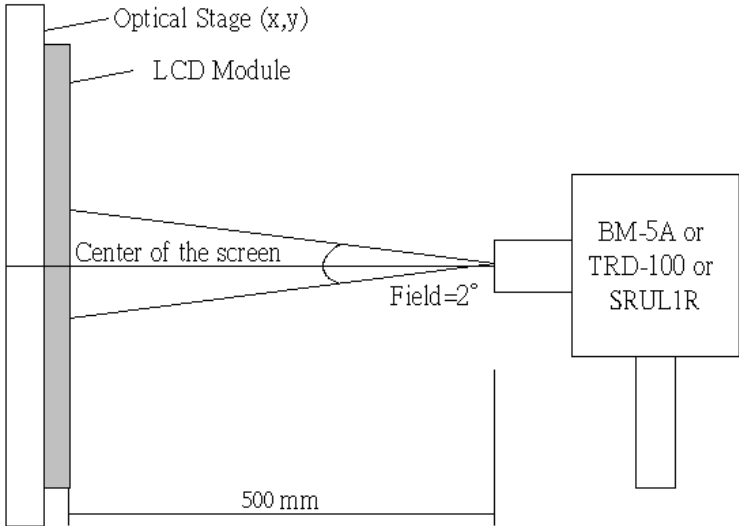
Definition of these measurement items is as follows:

***1) Setup of Measurement Equipment**

The LCD module should be turn-on to a stable luminance level to be reached. The measurement should be executed after lighting Backlight for 20 minutes and in a dark room.

***2) Definition of Contrast Ratio**

CR=ON (White) Luminance/OFF (Black) Luminance



***3) Definition of Luminance and Luminance uniformity**

- Central luminance：The white luminance is measured at the center position "5" on the screen, see Fig.1 below.
- 5P Luminance (AVG): The white luminance is measured at measuring points 5、10、11、12、13, see Fig.1 below.
- 5P Uniformity: Δ L = (Lmin / Lmax) ×100%
- 13P Uniformity: Δ L = (Lmin / Lmax) ×100%

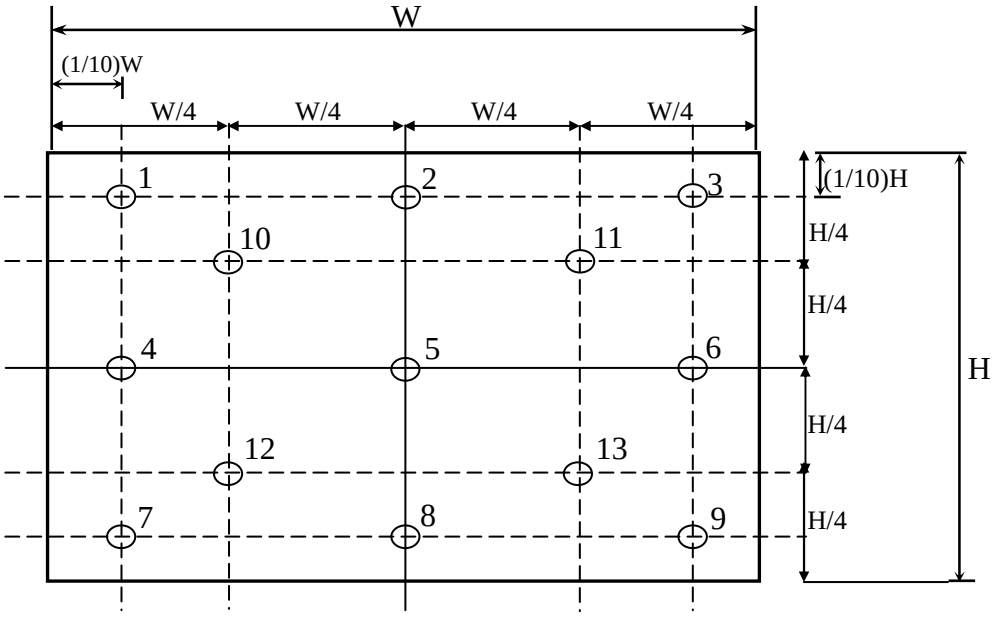
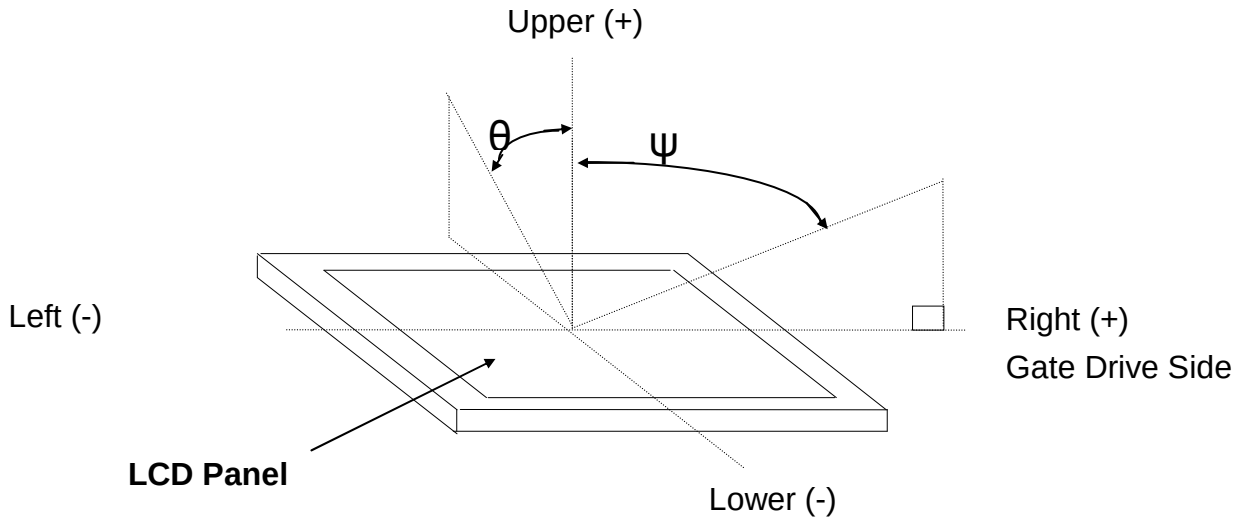
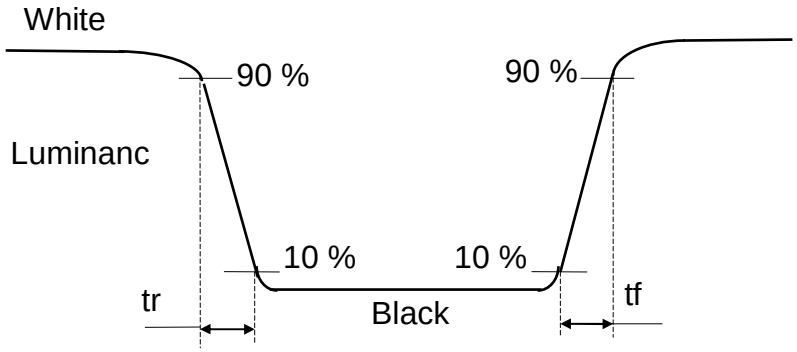


Fig.1 Measure point (Active area)

***4) Definition of view angle(θ，ψ)**



***5) Definition of response time**



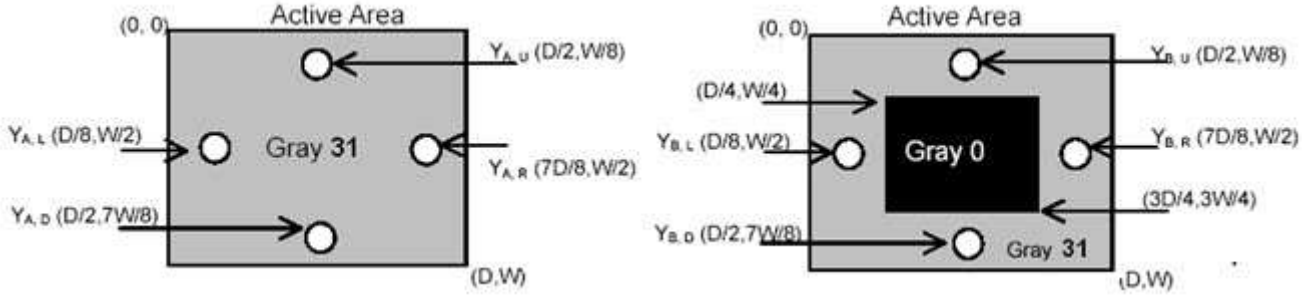
***6) Crosstalk Modulation Ratio**

CT=｜Y_B-Y_A｜ / Y_A×× 100%

Y_A、Y_B measure position and definition

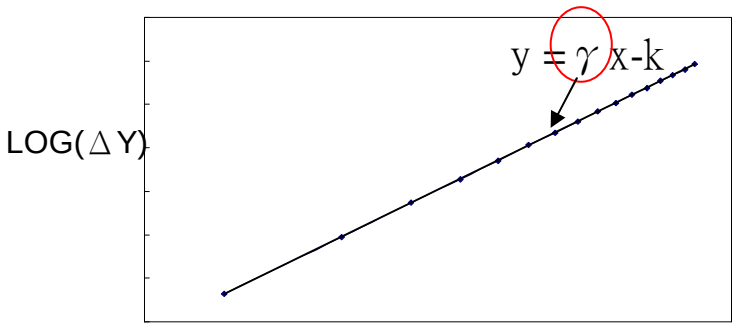
Y_A means luminance at gray level 31(exclude gray level 0 pattern)

Y_B means luminance at gray level 31(include gray level 0 pattern)



***7) Definition of Gamma (VESA)**

Based on Customer Sample, take the average value as a standard center value and the variation range of gamma value caused by loop voltage error should be between +/- 0.2. the bellow figure shows how to obtain the gamma curve and γ (from gray level: 0、4、8-----60、63).



9. RELIABILITY TEST CONDITIONS

(1) Temperature and Humidity

TEST ITEMS	CONDITIONS
High Temperature Operation	50°C；250Hrs
High Temperature Storage	65°C；250Hrs
High Temperature High Humidity Operation	40°C；95% RH；240Hrs
High Temperature High Humidity Storage	60°C；90% RH；48 Hrs
Low Temperature Operation	0°C；250 Hrs
Low Temperature Storage	-25°C；250 Hrs
Thermal Shock	-40°C(0.5 Hr)～65°C(0.5 Hr), Ramp<20°C, 100 CYCLES
Temperature & Pressure Storage	-0°C；260hPa，24 Hrs

(2) Shock & Vibration

TEST ITEMS	CONDITIONS
Shock (Non-Operation)	210G, 3ms, half sin ewave, ± X,± Y,± Z 1time each
Vibration (Non-Operation)	Vibration level: 14.7m/s2, 1.5G, sinusoidal wave (each x, y, z axis: 1hr, total 3 hrs) Frequency range: 5Hz to 500 Hz Sweep speed : 0.5 Octave/min

(3) ESD

	Surface discharge(Panel display area、Frame、PWB、Panel back side)		Electrics capacity of Connector
	Contact	Air	Contact
Capacity	150 pF	150 pF	200 pF
Resistance	330 Ω	330 Ω	0 Ω
Voltage	±8kV/±15kV	±8kV/±15kV	±250 V
Interval	1 sec	1 sec	1 sec
Times(single point)	25	25	1

(4) MTBF without B/L: 200,000 Hrs (min) lifetimes.

(5) Judgment standard

The judgment of the above test should be made as follow:

Pass：Normal display image with no obvious non-uniformity and no line defect.

Partial transformation of the module parts should be ignored.

Fail：No display image, obvious non-uniformity, or line defects.