1. OVERVIEW

CLAA141XC01(with LVDS interface) is 14.1" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel , driver ICs , control circuit , and backlight.

By applying 6 bits digital data, 1024× 768, 262K color images are displayed on the 14.1" diagonal screen. Input power voltage is single 3.3V for LCD driving.

Inverter for backlight is not included in this module. General specifications are summarized in the following table:

ITEM	SPECIFICATION
Display Area(mm)	285.696(H) x 214.272(V) (14.1-inch diagonal)
Number of Pixels	1024(H) x 768(V)
Pixel Pitch(mm)	0.279(H) x 0.279(V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	normally white TN
Number of Colors	262144 colors
Optimum Viewing Angle	6 o'clock
Brightness(cd/m^2)	150
Power consumption(W)	4.7
Module Size(mm)	298.5(W) x 227.5(H) x 5.8(D)
Module Weight(g)	550 typ
Backlight Unit	CCFL , 1 tube

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cables, nuclear reactor control system and life support systems. If customers intend to use these LCD products for above application or not listed in "Standard" as follows , please contact our sales people in advance.

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment , Machine tool , Industrial robot , Audio and Visual equipment , Other consumer products.

2. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT
Power Supply Voltage for LCD	VCC	-0.3	4.0	V
LVDS input Voltage	VI	-0.3	VCC+0.3	V
Static Electricity *1)	VESDt	-250	250	V
	VESDc	-15	15	KV
ICC Rush Current *2)	I_{RUSH}		3.75	A
Operation Temperature *3)	Top	0	50	℃
Storage Temperature *3)	Tstg	-20	60	℃
Starting Lamp Voltage	V_{SL}	0	2000	V

[Note] ： *1) Test Condition: IEC 1000-4-2 ,
VESDt ：Contact discharge to input connector
VESDc：Contact discharge to module
***2)** 50 μ sec , If Vcc rise time increase then I_{RUSH} decrease.
*3) Humidity ≦85% RH. without condensation.

3. ELECTRICAL CHARACTERISTICS

(1)TFT-LCD

Ta = 25°C

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	Remark
Power Supply Voltage for LCD		VCC	3.0	3.3	3.6	V	*[Note 1]*
Power Supply Current for LCD		ICC	-	450	800	mA	*[Note 2]*
Logic input Voltage *[Note 3]*	Input Voltage	VIN	0	-	VCC	V	
	Common Mode Voltage	VCM	1.125	-	1.375	V	
	Differential Input Voltage	|VID|	250	345	450	mV	
	Threshold Voltage(High)	VTH	-	-	100	mV	When VCM=+1.2V
	Threshold Voltage(Low)	VTL	-100	-	-	mV	
Tolerance of VID		ΔVID	-	-	35	mV	
Tolerance of VCM		ΔVCM	-	-	35	mV	

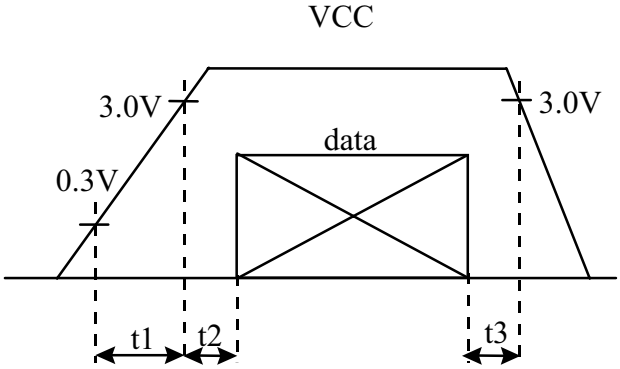
[Note 1]

● VCC turn on conditions :

t1 ≦ 10 ms

0ms < t2 ≦ 50ms

0ms ≦ t3 ≦ 50ms

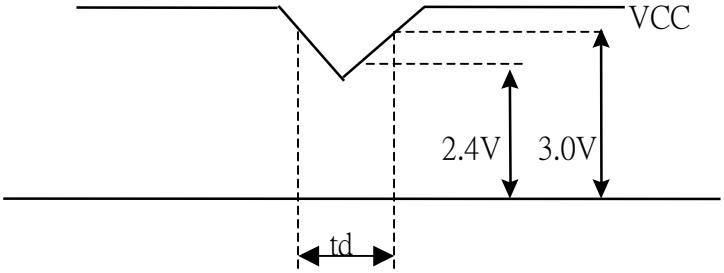


● VCC dip conditions :

1) When 2.4V ≦ VCC<3.0V, td ≦ 15 ms

2) When VCC<2.4V

VCC dip conditions should follow VCC turn on conditions.



[Note 2]

● Typical value is measured when displaying horizontal gray scale line pattern

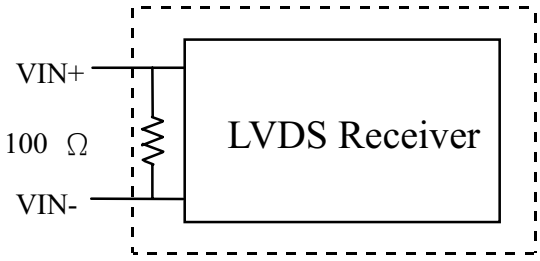
64 gray level

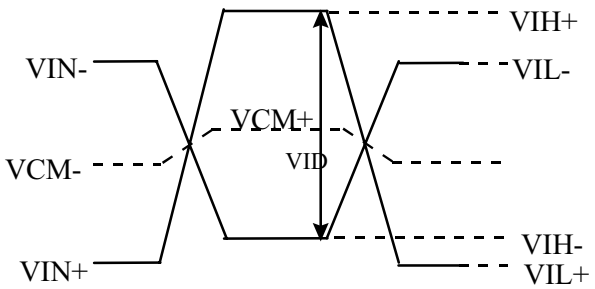
768 line mode

VCC = +3.3V

[Note 3]

● LVDS Signal definition :





VID = VIN_+ - VIN-

△VCM = | VCM_+ - VCM- |

△VID = | VID_+ - VID- |

VID_+ = | VIH_+ - VIH- |

VID- = | VIL_+ - VIL- |

VCM = (VIN_+ - VIN-) / 2

VCM_+ = (VIH_+ - VIH-) / 2

VCM- = (VIL_+ - VIL-) / 2

VIN_+ = Positive differential DATA & CLK Input

VIN- = Negative differential DATA & CLK Input

(2) Backlight system

Ta = 25°C

ITEM		SYMBOL	MIN	TYP	MAX	UNIT
Lamp Voltage		VL	630	700	770	V
Lamp Current *1)*		IL	3.0	5.0	6.0	mA
Inverter Frequency		FI	40	50	60	KHz
Lamp life time *2)*		Life L	10000	-	-	hr
Starting Lamp Voltage	Ta = 25℃	Vs	-	-	1000	V
	Tb = 0℃		-	-	1300	

[Note]

*1) Standard inverter : IM 4201 , typical luminance = 150 cd/m^2 .

*2) The time that module luminance reduced to 50% of initial value .
Base on Vs = 1000 V , Ta = 25°C , IL=5.0 mA continuous.

4. INTERFACE CONNECTION

(1) CN1 (INTERFACE SIGNAL)

- Connector type : FI-SEB20P-HF13 (JAE made)
- Corresponding connector type : (reference)
  FI-SE20M (JAE made , FPC type) , FI-S20S (JAE made , wire)

pin	Symbol	Function
1	VCC	+3.3V power supply
2	VCC	+3.3V power supply
3	GND	
4	GND	
5	Link 0-	R0, R1, R2, R3, R4, R5,G0 ***[Note]***
6	Link 0+	R0, R1, R2, R3, R4, R5,G0 ***[Note]***
7	GND	
8	Link 1-	G1, G2, G3, G4, G5,B0,B1 ***[Note]***
9	Link 1+	G1, G2, G3, G4, G5,B0,B1 ***[Note]***
10	GND	
11	Link 2-	B2, B3, B4, B5,HD,VD,DENA ***[Note]***
12	Link 2+	B2, B3, B4, B5,HD,VD,DENA ***[Note]***
13	GND	
14	CLKIN-	Clock-
15	CLKIN+	Clock+
16	GND	
17	TEST	Should be open during operation
18	TEST	Should be open during operation
19	GND	
20	GND	

[Note] Refer to page 4 , 5 , 6 (Data Mapping)

(2) CN2 (BACK LIGHT)

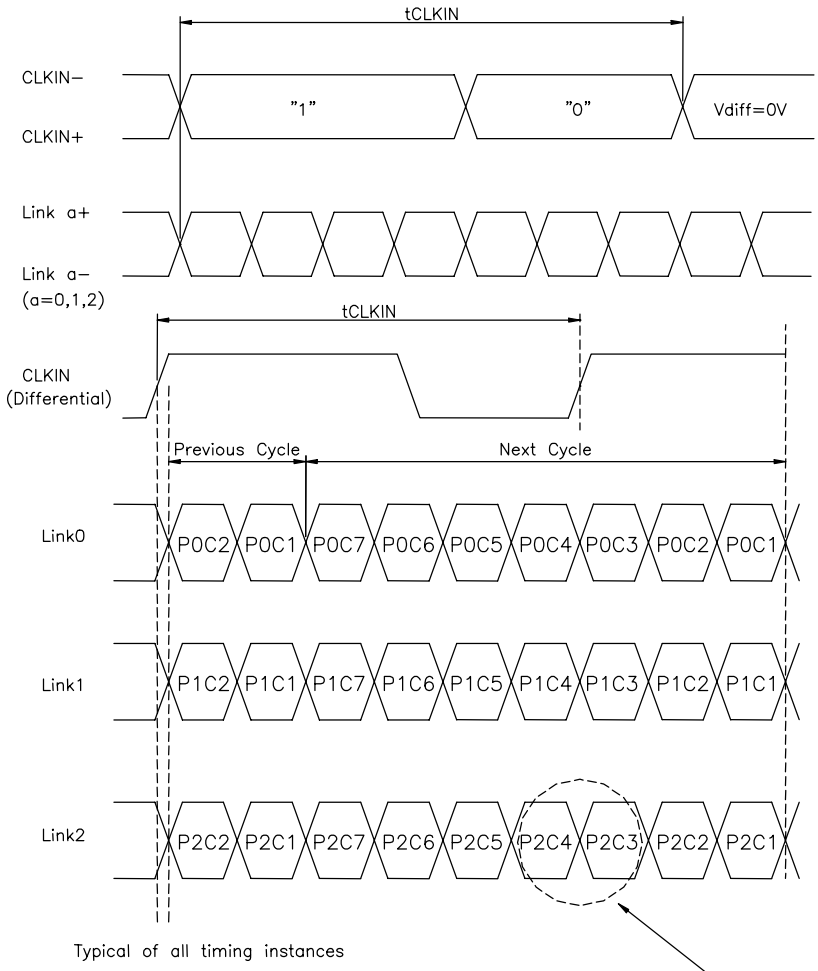
- Backlight-side connector : BHSR-02VS-1(JST made)
- Inverter-side connector : SM02B-BHSS-1(JST made)

Pin No.	Symbol	Function
1	CTH	VBLH (High voltage)
2	CTL	VBLL (Low voltage)

[Note] VBLH-VBLL = VL

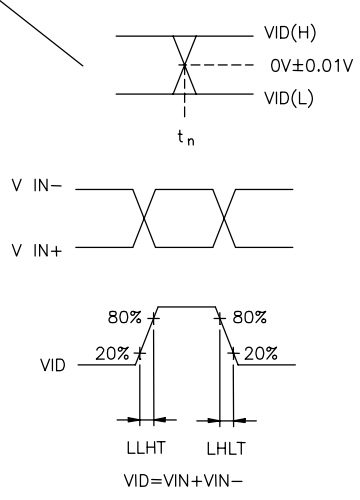
5. Input Signal Timing

(1)LVDS (Rx) Input Signal Timing Chart



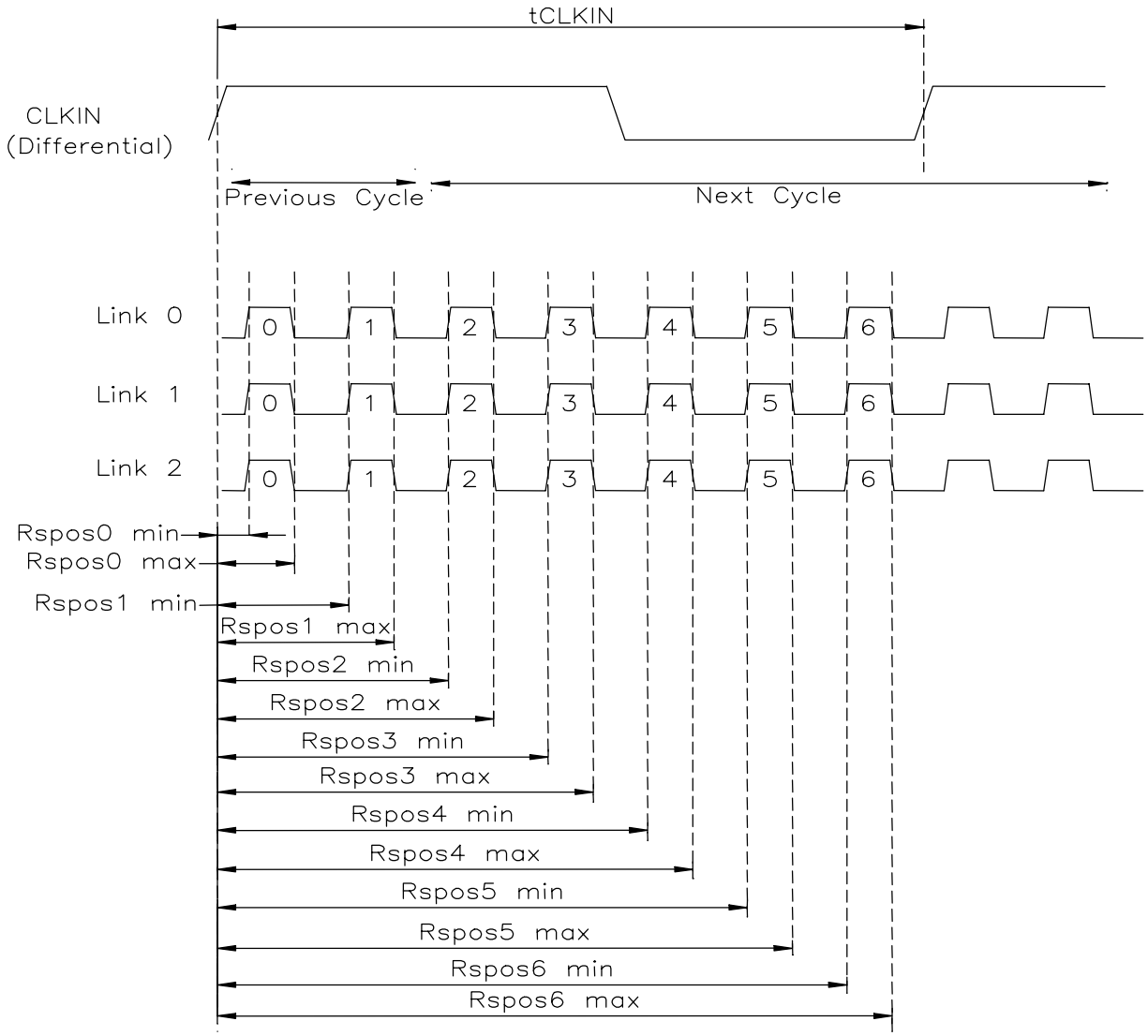
Cell	IN *)	DATA
POC1	TxIN 0	R0
POC2	TxIN 1	R1
POC3	TxIN 2	R2
POC4	TxIN 3	R3
POC5	TxIN 4	R4
POC6	TxIN 6	R5
POC7	TxIN 7	G0
P1C1	TxIN 8	G1
P1C2	TxIN 9	G2
P1C3	TxIN 12	G3
P1C4	TxIN 13	G4

Cell	IN *)	DATA
P1C5	TxIN 14	G5
P1C6	TxIN 15	B0
P1C7	TxIN 18	B1
P2C1	TxIN 19	B2
P2C2	TxIN 20	B3
P2C3	TxIN 21	B4
P2C4	TxIN 22	B5
P2C5	TxIN 24	HD
P2C6	TxIN 25	VD
P2C7	TxIN 26	DENA
Ref-CLK	TxCLK IN	DCLK

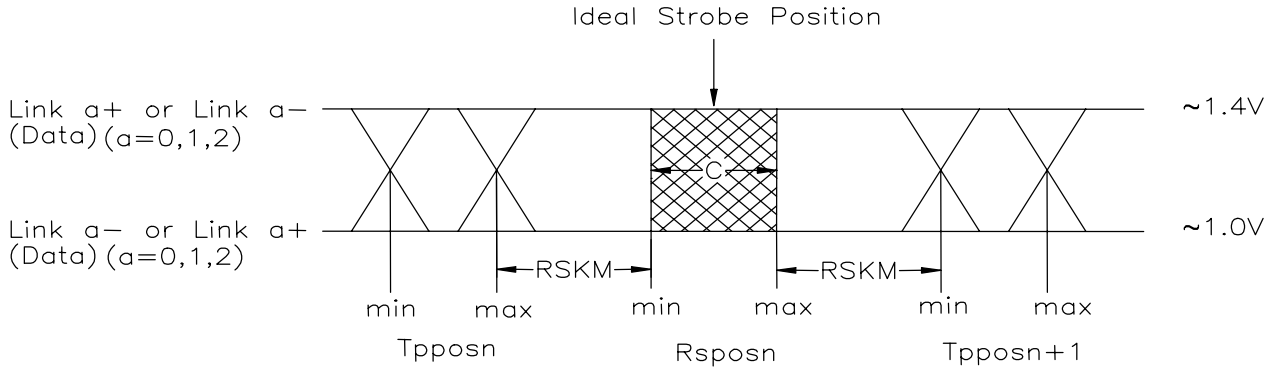


Parallel TTL Data Mapped to LVDS Receiver Inputs−DS90CF384A

*)DS90C383(N.S.) LVDS Transmiter Pin Diagram

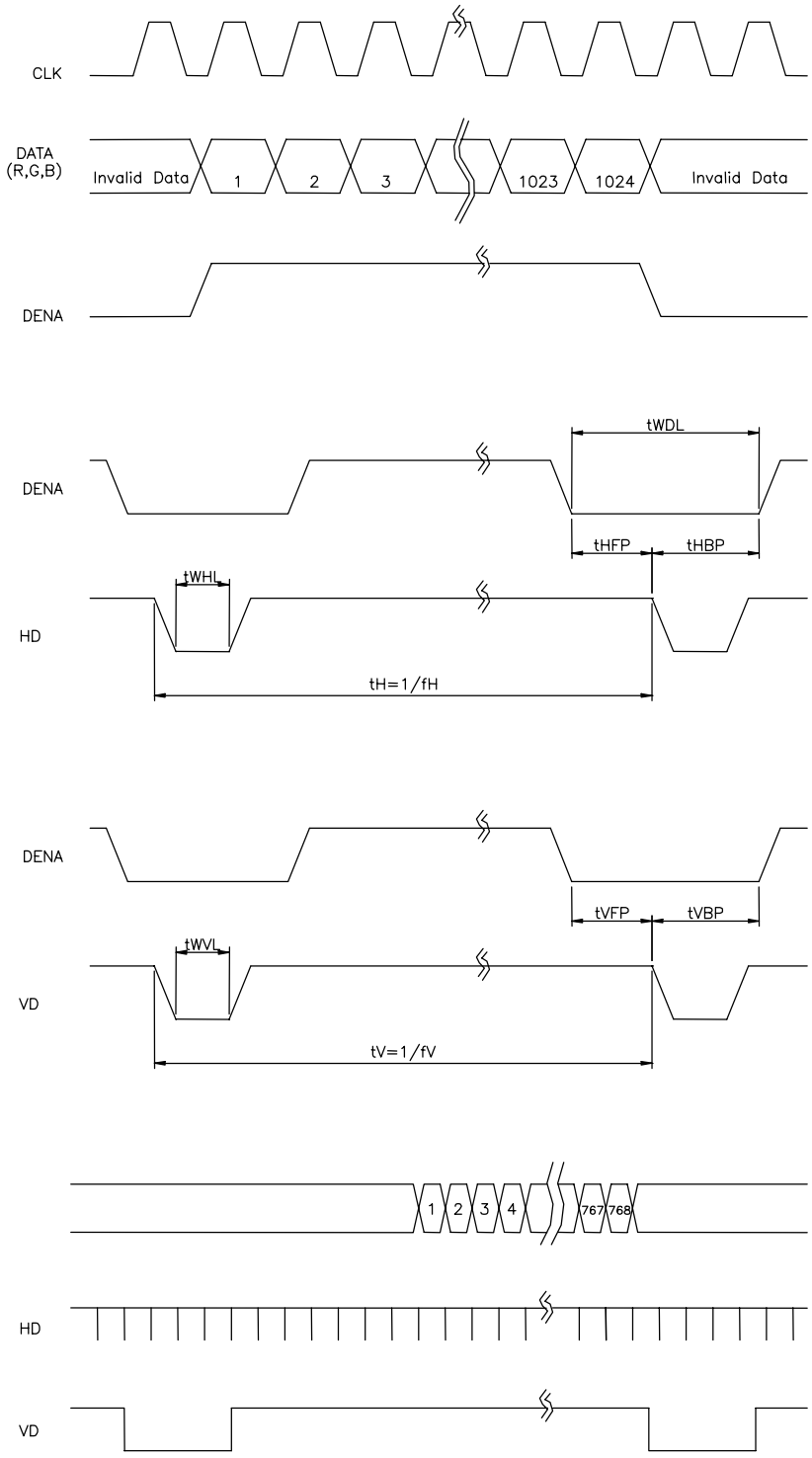


Receiver LVDS Input Strobe Position



Receiver LVDS Input Skew Margin

(2) LCD (Tx) Input Signal Timing Chart : (= Rx output)



(3)Timing Specifications

ITEM				SYMBOL	MIN	TYP	MAX	UNIT
LVDS Input Timing	CLK frequency			FCLKin	-	65	66.6	MHz
	CLK period			tCLKin	15	15.4	-	ns
	LVDS High to Low transition time			LLHT	-	0.75	1.5	ns
	LVDS Low to High transition time			LHLT	-	0.75	1.5	ns
	Strobe position of Bit 0		f = 65MHz	Rspos0	0.7	1.1	1.4	ns
	Strobe position of Bit 1			Rspos1	2.9	3.3	3.6	ns
	Strobe position of Bit 2			Rspos2	5.1	5.5	5.8	ns
	Strobe position of Bit 3			Rspos3	7.3	7.7	8.0	ns
	Strobe position of Bit 4			Rspos4	9.5	9.9	10.2	ns
	Strobe position of Bit 5			Rspos5	11.7	12.1	12.4	ns
	Strobe position of Bit 6			Rspos6	13.9	14.3	14.6	ns
	RxIN Skew Margin			RSKM	400	-	-	ps
LCD input signal (LVDS Tx Input , Rx output)	DENA	Low width		tWDL	100	320	-	tCLK
		Horizontal Front Porch		tHFP	10	24	-	TCLK
		Horizontal Back Porch		tHBP	4	296	-	TCLK
		Vertical Front Porch		tVFP	2	3	-	TH
		Vertical Back Porch		tVBP	1	35	-	tH
	HD	Frequency		fH	-	48.5	55.9	kHz
		Period		tH	1100	1340	-	tCLK
		Low width		tWHL	1	136	-	tCLK
	VD	Frequency		fV	-	60	62	Hz
		Period		tV	772	806	-	tH
		Low width		tWVL	1	6	-	tH

[Note]

1) Polarities of HD and VD are negative in this specification.
2) DENA (Data Enable) should always be positive polarity as shown in the timing specification.
3) CLKIN should appear during all invalid period, and HD should appear during invalid period of frame cycle.
4) LVDS Receiver model : DS90C384(NS made).

(4) Color data definition

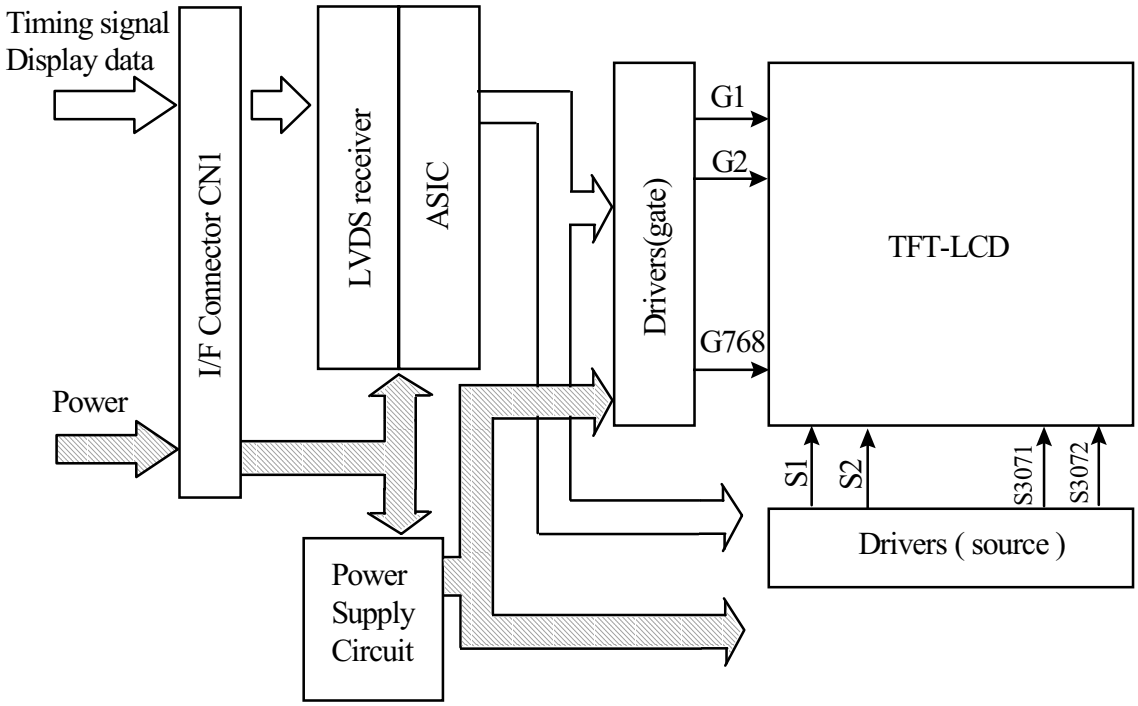
COLOR	INPUT DATA	R_DATA						G_DATA						B_DATA					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

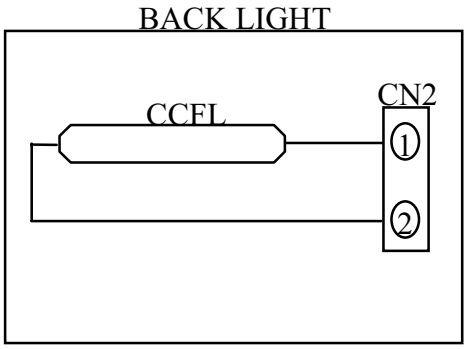
[Note]

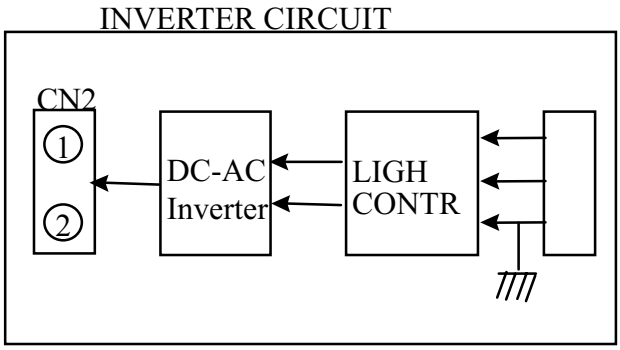
(1) Definition of gray scale:
Color(n) : n means level of gray scale .
Bigger n means brighter level.

(2) Data : 1= High , 0 = Low

6. BLOCK DIAGRAM

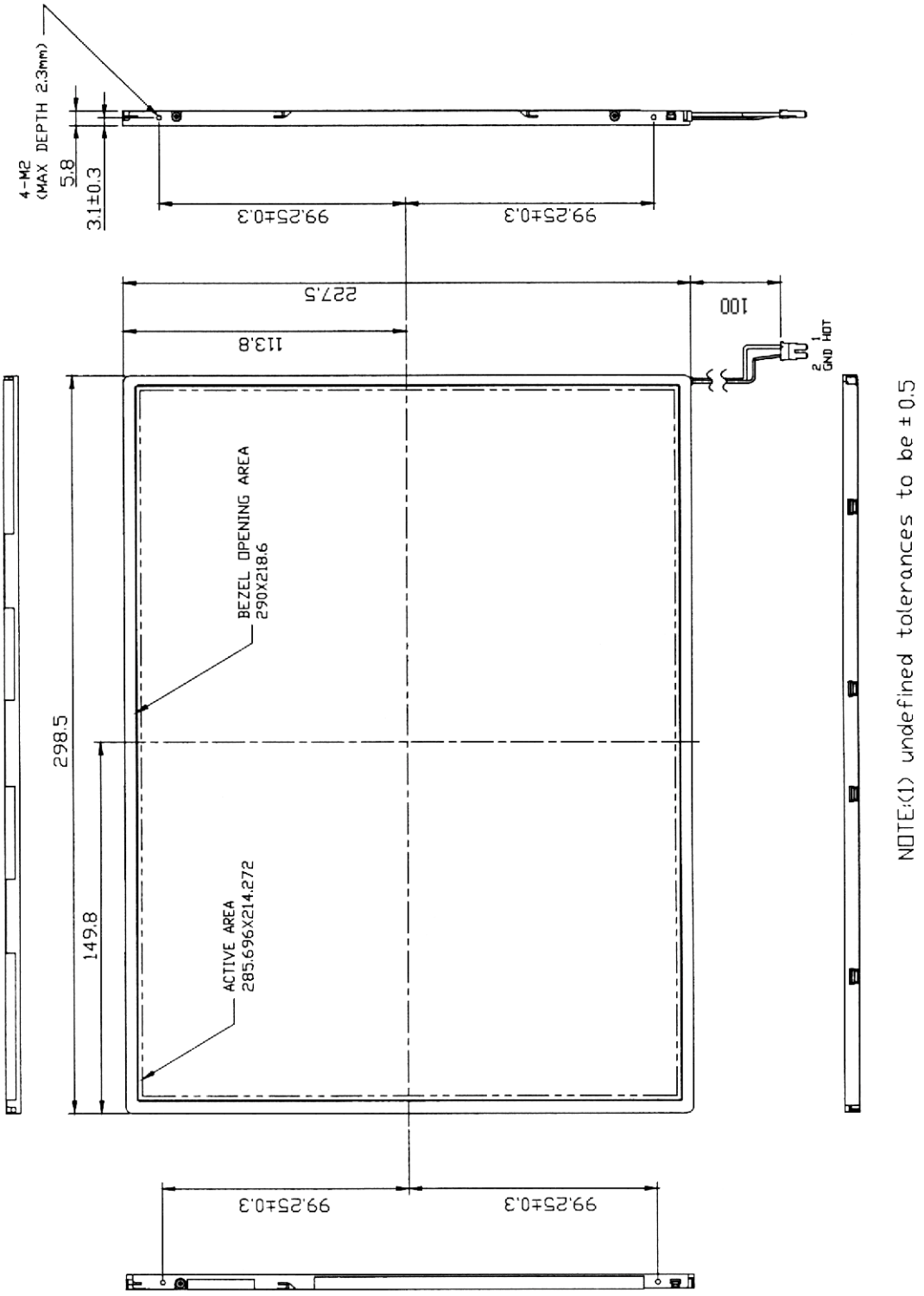






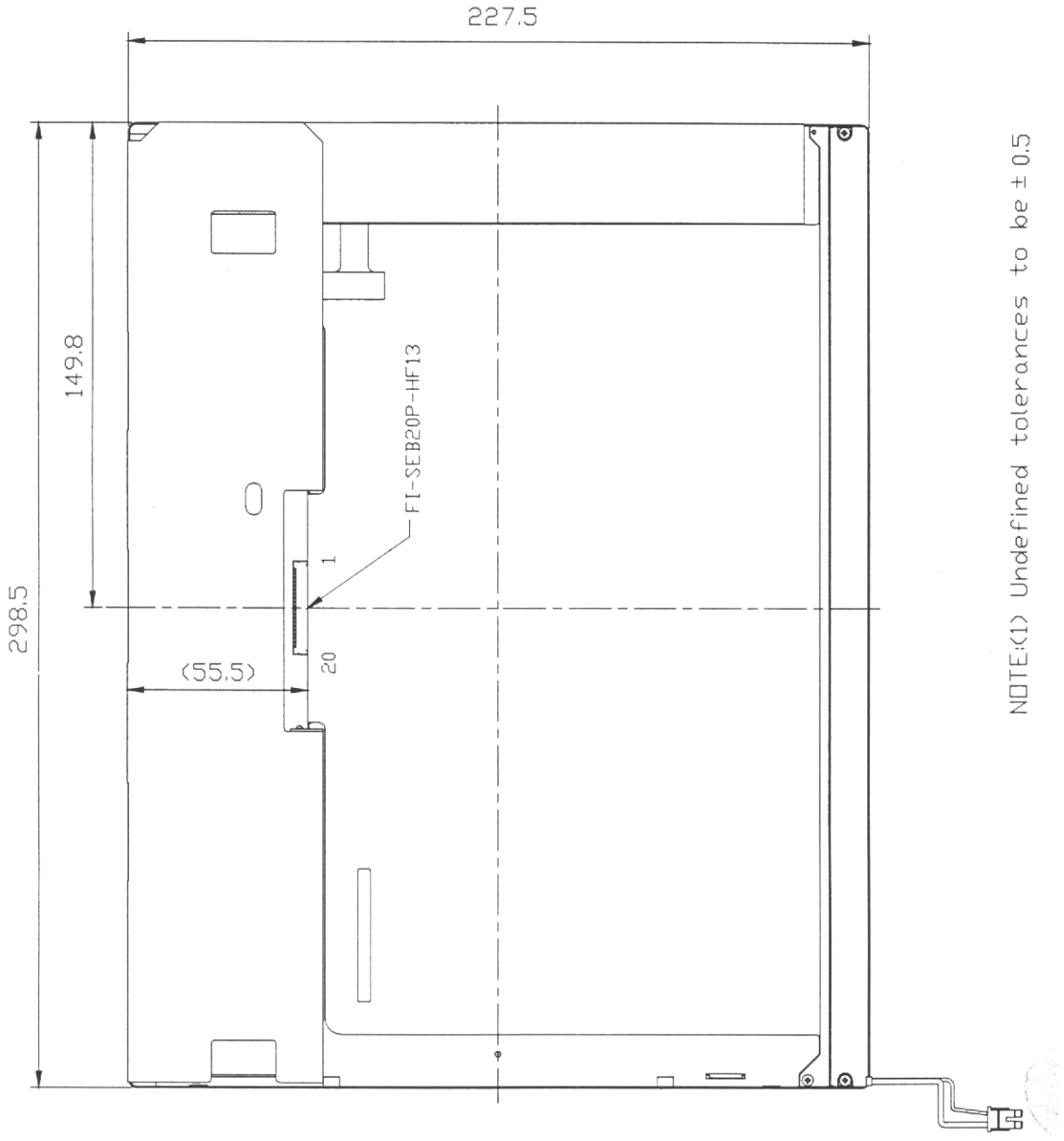
7. MECHANICAL DIMENSION

(1) Front side



(2) Rear side

Unit : mm



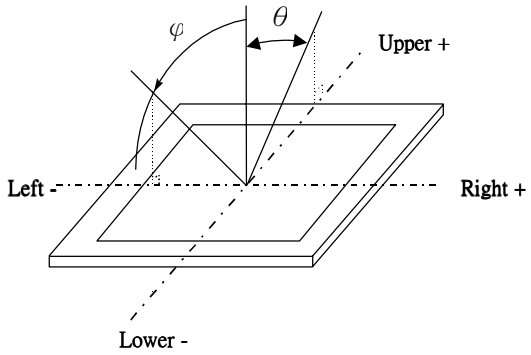
8. OPTICAL CHARACTERISTICS

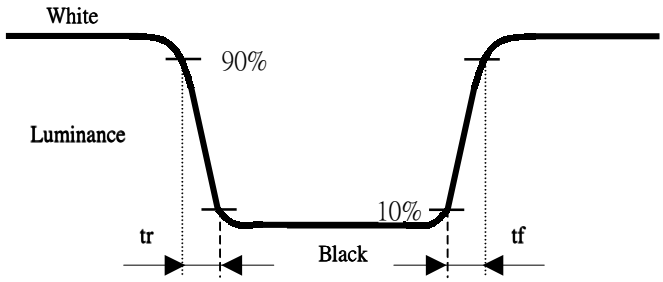
Ta = 25°C , Vcc=3.3V

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Contrast Ratio		CR	$\varphi=\theta=0^\circ$		200	-	-
Luminance *)		L	$\varphi=\theta=0^\circ$	120	150	-	cd/m^2
Luminance Uniformity		△L	*)	-	-	30	%
Contrast Ratio Uniformity		△CR	*)	-	-	70	%
Response Time		tr	$\varphi=\theta=0^\circ$	-	**10**	20	ms
		tf	$\varphi=\theta=0^\circ$	-	**20**	40	ms
Viewing Angle	Horizontal	φ	CR≧10	-	-45 ~ 45	-	°
	Vertical	θ		-	-30 ~ 10	-	°
Image Sticking		tis	2 hrs	-	-	2	sec
Crosstalk modulation Ratio		CMR	*)	0	-	5	%
Color Coordinate	Red	X y	$\varphi=\theta=0^\circ$	0.548 0.311	0.578 0.341	0.608 0.371	-
	Green	x y		0.270 0.505	0.300 0.535	0.330 0.565	
	Blue	x y		0.110 0.109	0.140 0.139	0.170 0.169	
	White	x y		0.271 0.302	0.301 0.332	0.331 0.362	

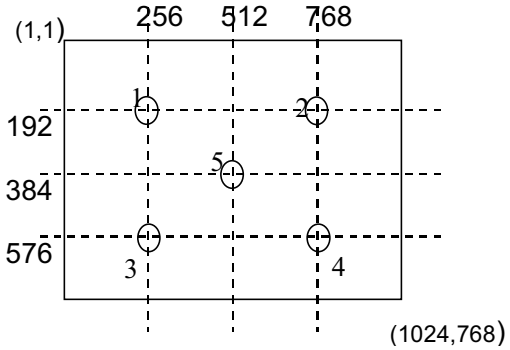
[Note]

- These items are measured by BM-5A(TOPCON) or LCD-7000 (Otsuak Electronic) in the dark room .(no ambient light).
- Brightness conditions : IL= 5.0 mA , HIU-742A CCFL (Harison made)
- Definition of these measurement items are as follows:
  (a) Definition of Viewing Angle(θ , ϕ) (b) Definition of Response Time





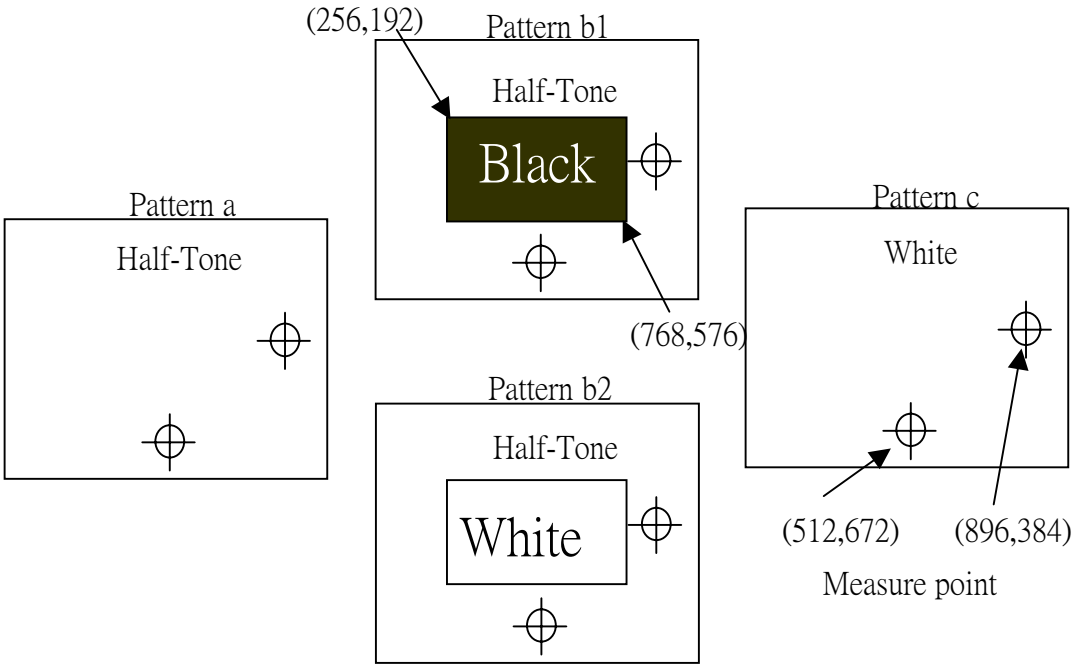
- Definition of luminance and CR measured positions :
  (a) Measure White Luminance on the below 5 points and take the average value.
  (b) CR : measures the same 5 points and take the average value. The Definition of Contrast Ratio is as follows:
  CR= (White)Luminance of ON / (Black)Luminance OFF



- Definition of Contrast Ratio Uniformity
  △ CR = [CR(MAX) / CR(MIN) - 1] x 100

- Definition of Luminance Uniformity
  △ L = [L(MAX) / L(MIN)-1] x 100

- Definition of Cross talk Modulation Ratio
  CMR = MAX (***(| (Lb1-La) / Lc |) ×100*** , ***(| (Lb2 – La) / Lc |) ×100***)
  Back ground : 32 gray level



9. RELIABILITY TEST CONDITIONS

(1)Temperature and Humidity

TEST ITEMS	CONDITIONS
HIGH TEMPERATURE OPERATION	50℃,240h
HIGH TEMPERATURE STORAGE	60℃,240h and 65℃,48h
LOW TEMPERATURE OPERATION	0℃,240h
LOW TEMPERATURE STORAGE	-20℃,240h
HIGH TEMPERATURE HIGH HUMIDITY OPERATION	50℃,90%RH,240h
HIGH TEMPERATURE HIGH HUMIDITY STORAGE	60℃,90%RH(Max),48h
LOW PRESSURE STORAGE	260 hPa,24 h
THERMAL SHOCK(No operation)	BETWEEN -20℃(1h)AND 60℃(1h),100 CYCLES

(2)Shock & Vibration

ITEMS	CONDITIONS
SHOCK (NON-OPERATION)	● Shock level: 1764 m/s^2 (180G) ● Waveform: half sinusoidal wave, 2ms ● Number of shocks: one shock input in each direction of three mutually perpendicular axes for a total of six shock inputs.
VIBRATION (NON-OPERATION)	● Vibration level: 9.8 m/s^2 (1.0G) , perpendicular axis(each x,y,z axis: 1hr , total 3 hrs) ● Frequency range: 5 to 500 Hz ● Sweep speed : 9 Hz / min

(3)Judgment standard

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

10. Specification of Optical-Mechanical defects

(1) Inspection condition is as follows :

Viewing distance is approximately 35cm.

Viewing angle is normal to the LCD panel.

Ambient temperature is approximately 25℃

Ambient light is from 300 to 500 lux

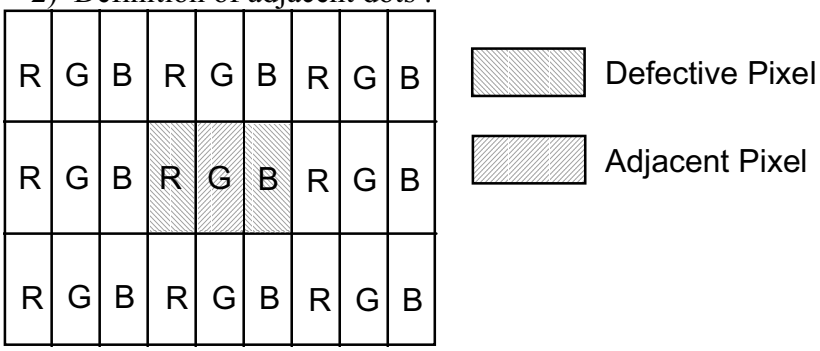
(2) Bright Dot is defined as " Visible through 5% transmission ND filter " .

DEFECT TYPE		LIMIT	
VISUAL DEFECT	SCRATCH	0.01mm≦W≦0.05mm L≦10mm	N≦4
		0.01mm≦W 10mm<L	N=0
		0.05mm<W	N=0
	DENT	0.15mm≦ φ ≦0.4mm	N≦4
		0.4mm< φ	N=0
	BLACK SPOT	0.15mm≦ φ ≦0.5mm	N≦4
		0.5mm< φ	N=0
	LINT	L≦3mm	N≦4
		3mm<L	N=0
	NEWTON RINGS	NOT ALLOWED	

ELECTRICAL DEFECT	BRIGHT DOT	N≦ 8
	DARK DOT	N≦ 8
	TOTAL DOT	N≦ 10
	TWO ADJACENT DOT BRIGHT DOT DARK DOT	≦2 PAIRS ≦2 PAIRS
	THREE OR MORE ADJACENT DOT	NOT ALLOWED
	DISTANCE BETWEEN DEFECTS BRIGHT DOT DARK DOT	≧15mm ≧15mm
	LINE DEFECT	NOT ALLOWED

*1) W: width , L:Lenth , N:number

*2) Definition of adjacent dots :



The defects that are not defined above and considered to be problem shall be reviewed and discussed by both parties.

11. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling- TFT-LCD products;

(A) ASSEMBLY PRECAUTION

(1) Please use the mounting hole on the module side in installing and do not beading or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.

(2) Please design display housing in accordance with the following guide lines.

(2.1) Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.

(2.2) Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0 mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.

(2.3) When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.

(2.4) Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.

(2.5) Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately

1.0mm of the clearance in the design is recommended.

(3) Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)

(4) Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.

(5) Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.

(6) Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.

(7) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

(8) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.

(9) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting wit inverter.

(B) OPERATING PRECAUTIONS

(1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.

(2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.

(3) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.

(4) A condensation might happen on the surface and inside of LCD module in case of sudden charge of ambient temperature.

(5) Please pay attention to displaying the same pattern for very long time. Image might stick on LCD. If then, time going on can make LCD work well.

(6) Please obey the same caution descriptions as ones that need to pay attention to ordinary electronic parts.

(C) PRECAUTFONSWITHELECTROSTATICS

(1) This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.

(2) Please remove protection film very slowly on the surface of LCD module to prevent from electrostatics occurrence.

(D) STORAGE PRECAUTIONS

(1) When you store LCDs for a long time, it is recommended to keep the temperature between 0°C-40°C without the exposure of sunlight and to keep the humidity less than 90%RH.

(2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C 90%RH.

(3) Please do not leave the LCDs in the environment of low temperature; below -20°C.

(E) SAFETY PRECAUTIONS

(1) When you waste LCDS, it is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged-glass cell and comes in contact with the hands,

wash off throughly with soap and water.

(F) OTHERS

(1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays.

(2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.

(3) For the. packaging box, please pay attention to the followings:

(3.1) Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.

(3.2) Please do not pile them up more than 5 boxes. (They are not designed so.) And please do not turn over.

(3.3) Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.

(3.4) Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)