SHARP

No.	**LD -D2113Y04D**
DATE	July. 2. 2014

TECHNICAL LITERATURE

For

TFT-LCD module

These parts have corresponded with the RoHS directive.

MODEL No. LQ133M1JW03

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DEVELOPMENT DEPARTMENT
DISPLAY DEVICE UNITⅡ
DISPLAY DEVICE BUSINESS DIVISIN
SHARP CORPORATION

RECORDS OF REVISION

LQ133M1JW03

SPEC No.	DATE	REVISED No.	PAGE	SUMMARY	NOTE
LD-D2113Y04A	Dec.10.2013	-	-	-	1st Issue
LD-D2113Y04B	Feb.26.2014	△1	P.3	[Active area size] 239.76(H)×165.24 (V) -> 293.76(H)×165.24 (V)	
			P.8	[DC Electrical Characteristics] High / Low Level Input Voltage -> High / Low Level PWM Voltage	
			P.9	t3 Timing MAX 100ms -> 200ms	
			P.10	[Note 6-1-5] PWM : BL_PWM_DIM signal input.	
			P.15-18	EDID data add	
LD-D2113Y04C	Apr.07.2014	△2	P.8	[TFT-LCD panel driving] Current dissipation Power dissipation	
			P.11	[Backlight driving] Current dissipation Power dissipation	
LD-D2113Y04D	July.2.2014	△3	P.3	[Outline dimensions] Mass	
			P.5	[Note 4-1-7]	
			P.6	Fig4-2-2 Main Link differential pair	
			P.19	[Optical Characteristics] Chromaticity of red,green,blue White uniformity	
			P.21	[Handling Precautions]	
			P.23	[Precautions for Handling Tray]	
			P.24	[Packaging Condition] Piling number of cartons. Package quantity in one carton Carton size Total mass of one carton filed with full modules Packing form	
			P.24-25	[Label] 1) Module Bar code label 2) Packing bar code label	

LD-D2113Y04D	July.2.2014	△3	P.26	[Reliability Test Items] 8. ESD	
			P.27	Fig.1 Packing Condition	
			P.28	Fig.2 Outline Dimensions	

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1. Application

This specification applies to a color TFT-LCD Module, LQ133M1JW03.

2. Overview

This module is a color active matrix LCD module incorporating Oxide TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, a control circuit and power supply circuit. Graphics and texts can be displayed on a 1920×3×1080 dots panel with 16,777,216 colors by using eDP (Embedded Display Port) Ver1.3 interface and supplying +3.3V DC supply voltage for TFT-LCD panel driving.

In this TFT-LCD panel, color filters for excellent color performance is incorporated to realize brighter and clearer pictures, making this model optimum for use in multi-media applications.

Optimum viewings are in all directions.

8serial 8paralel LED structure.

Backlight-driving LED controller is built in this Module

eDP Transfer rate Specification : 2.7Gbps / 2 lane

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	33.70 (13.3") Diagonal	cm
Active area	293.76(H)×165.24 (V) △1	mm
Pixel format	1920 (H)×1080 (V)	pixel
	(1 pixel = R+G+B dots)	
Pixel pitch	0.153 (H) × 0.153 (V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally black	
Surface treatment of front polarizer	Anti-Glare, Hard coating (3H)	

Outline dimensions

Parameter		Min.	Typ.	Max.	Unit	Remark
Unit outline dimensions [Note 3-1]	Width	305.05	305.35	305.65	mm	
	Height	177.81	178.11	178.41	mm	w/o FPC, tape
	Depth			5	mm	w/ Shielding tape, PCB
Mass			225 △3	245 △3	g	

[Note 3-1] Outline dimensions is shown in Fig.2

4. Input Terminals

4 - 1. Symbol

CN1 (eDP signals, +3.3V DC power supply, and B/L power supply)

Pin No.	Symbol	I/O	Function	Remark
1	NC	-	Reserved for LCD	[Note4-1-1]
2	H_GND	P	High Speed Ground	[Note 4-1-2]
3	Lane1_N	I	Complement Signal Link Lane 1	
4	Lane1_P	I	True Signal Link Lane 1	
5	H_GND	P	High Speed Ground	[Note 4-1-2]
6	Lane0_N	I	Complement Signal Link Lane 0	
7	Lane0_P	I	True Signal Link Lane 0	
8	H_GND	P	High Speed Ground	[Note 4-1-2]
9	AUX_CH_P	I	True Signal Auxiliary Channel	
10	AUX_CH_N	I	Complement Signal Auxiliary Channel	
11	H_GND	P	High Speed Ground	[Note 4-1-2]
12	VDD	P	LCD logic and driver power(3.3V)	
13	VDD	P	LCD logic and driver power(3.3V)	
14	LCD Self Test	I	LCD Panel Self Test Enable	[Note 4-1-6]
15	LCD_GND	P	LCD logic and driver ground	
16	LCD_GND	P	LCD logic and driver ground	
17	HPD	O	HPD signal pin	[Note 4-1-3]
18	BL_GND	P	Backlight ground	
19	BL_GND	P	Backlight ground	
20	BL_GND	P	Backlight ground	
21	BL_GND	P	Backlight ground	
22	BL_ENABLE	I	Backlight On/Off	[Note 4-1-4]
23	BL_PWM_DIM	I	System PWM	[Note 4-1-5]
24	NC	-	Reserved for LCD manufacturer’s use	[Note4-1-1]
25	NC	-	Reserved for LCD manufacturer’s use	[Note4-1-1]
26	VBL	P	Backlight power	
27	VBL	P	Backlight power	
28	VBL	P	Backlight power	
29	VBL	P	Backlight power	
30	NC	-	Reserved for LCD manufacturer’s use	[Note4-1-1]

*1 P : Power , I : Input , O : Output

[Note 4-1-1] Don’t input any signals or any powers into a NC pin. Keep the NC pin open.

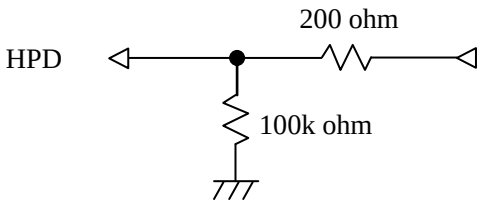
[Note 4-1-2] The shielding case is connected with signal GND.

・Connector used :20455-030E-12 (I-PEX)

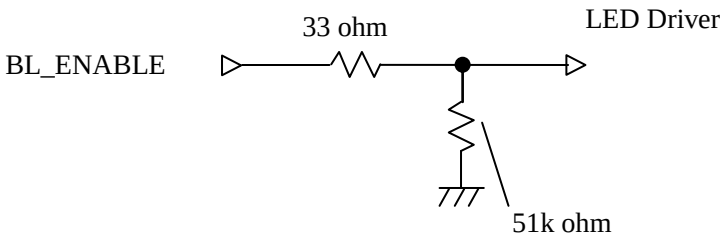
・Corresponding connector : 20454-030T (I-PEX)

(Sharp is not responsible to its product quality, if the user applies a connector not corresponding to the above model.)

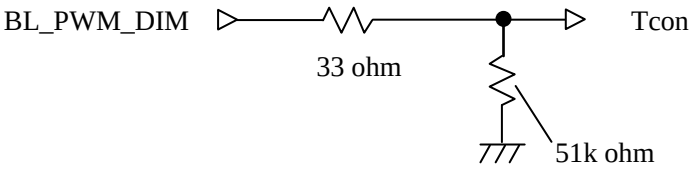
[Note 4-1-3] Output circuit is as below.



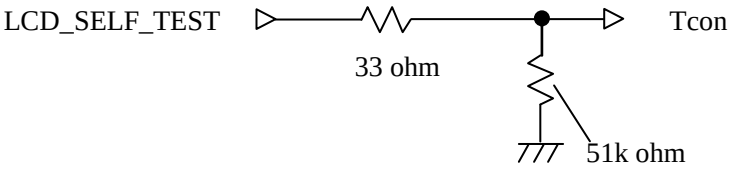
[Note 4-1-4] Input circuit is as below.



[Note 4-1-5] Input circuit is as below.



[Note 4-1-6] Input circuit is as below.



△3

[Note 4-1-7] All terminals except NC terminal must be connected to input signal described as above or supply voltage or GND each.

4 - 2. eDP interface

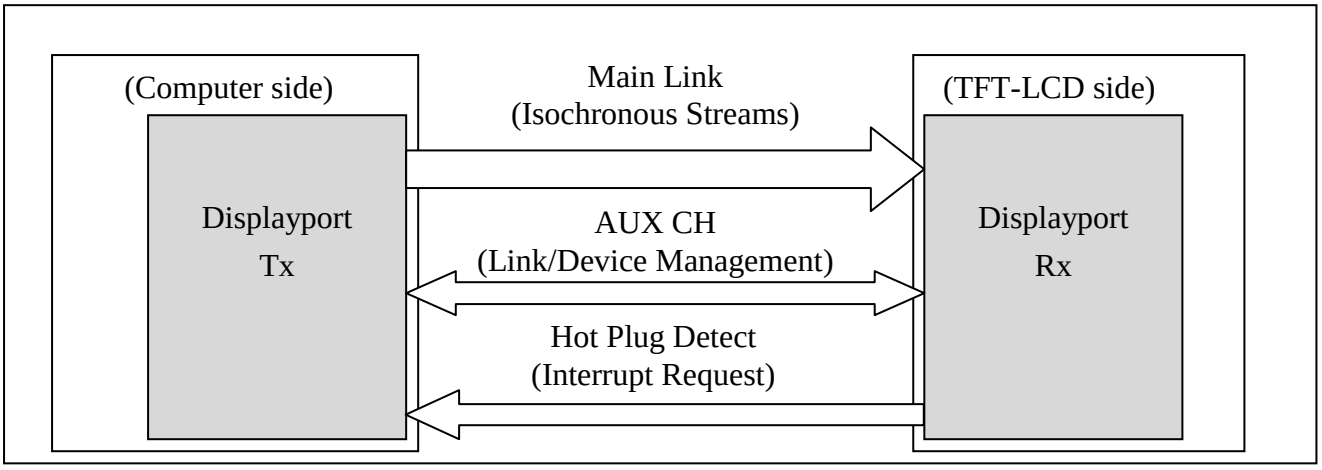


Fig.4-2-1 DP architecture.

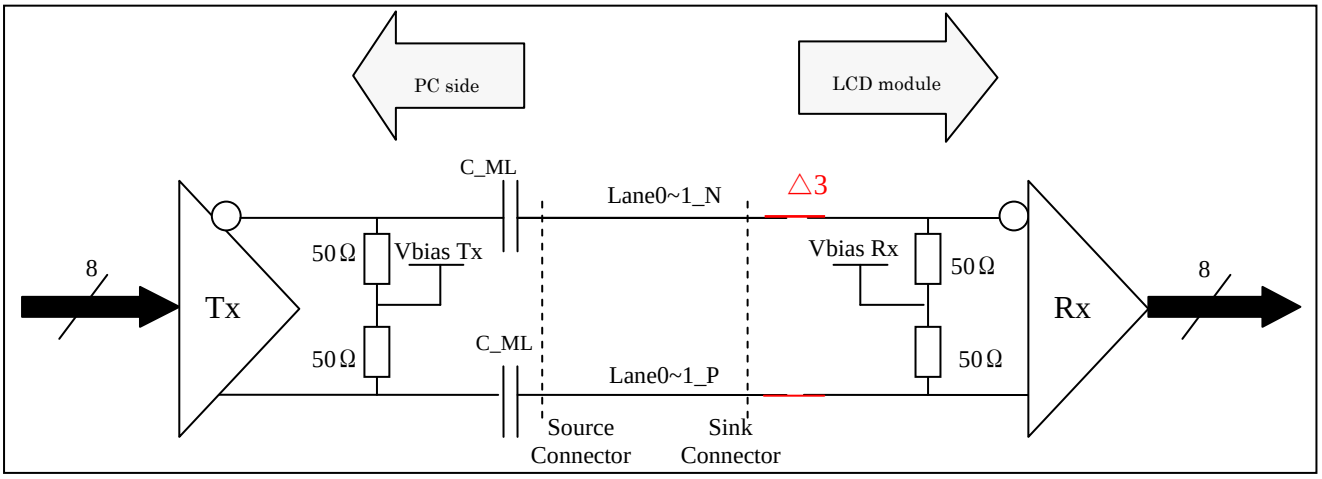


Fig.4-2-2 Main Link differential pair.

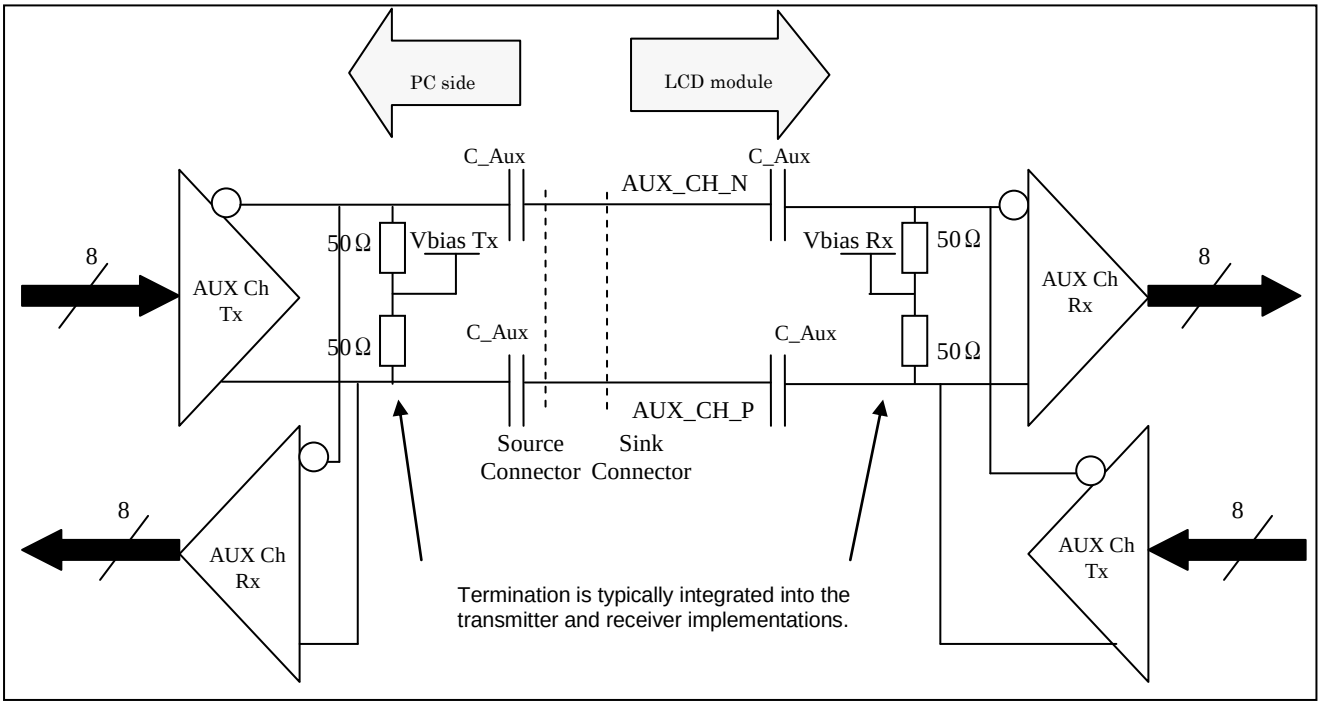


Fig.4-2-3 AUX Link differential pair.

Lane0	Lane1
R0-7:0	R1-7:0
G0-7:0	G1-7:0
B0-7:0	B1-7:0
R2-7:0	R3-7:0
G2-7:0	G3-7:0
B2-7:0	B3-7:0
R4-7:0	R5-7:0
G4-7:0	G5-7:0
B4-7:0	B5-7:0

Fig.4-2-4 eDP 2 lane 8 bit input data mapping.

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings		Unit	Remark
			Min.	Max.		
+3.3V supply voltage	VDD	Ta=25℃	-0.3	+4.0	V	
Back Light supply voltage	VBL	Ta=25℃	-0.3	+26.5	V	
Input voltage (eDP)	VI	Ta=25℃	-0.3	+2.8	V	[Note 5-1]
Input voltage (LCD_SELF_TEST)	VBIST	Ta=25℃	-0.3	VDD+0.3	V	[Note 5-2]
Input voltage (BL_PWM_DIM)	VPWM	Ta=25℃	-0.3	VDD+0.3	V	[Note 5-3]
Input voltage (BL_ENABLE)	VBLEN	Ta=25℃	-0.3	VDD+0.3	V	[Note 5-4]
Storage temperature (ambient)	Tstg	—	-25	+60	℃	[Note 5-5]
Operating temperature(ambient)	Topa	—	0	+50	℃	

[Note 5-1] eDP signals

[Note 5-2] LCD SELF TEST signal

[Note 5-3] Back light control signal (PWM_DIM)

[Note 5-4] Back light control signal (BL_ENABLE)

[Note 5-5] Humidity：90%RH Max. at Ta≦+40℃.

Maximum wet-bulb temperature at +39℃ or less at Ta>+40℃.

No condensation.

6. Electrical Characteristics

6-1. TFT-LCD panel driving

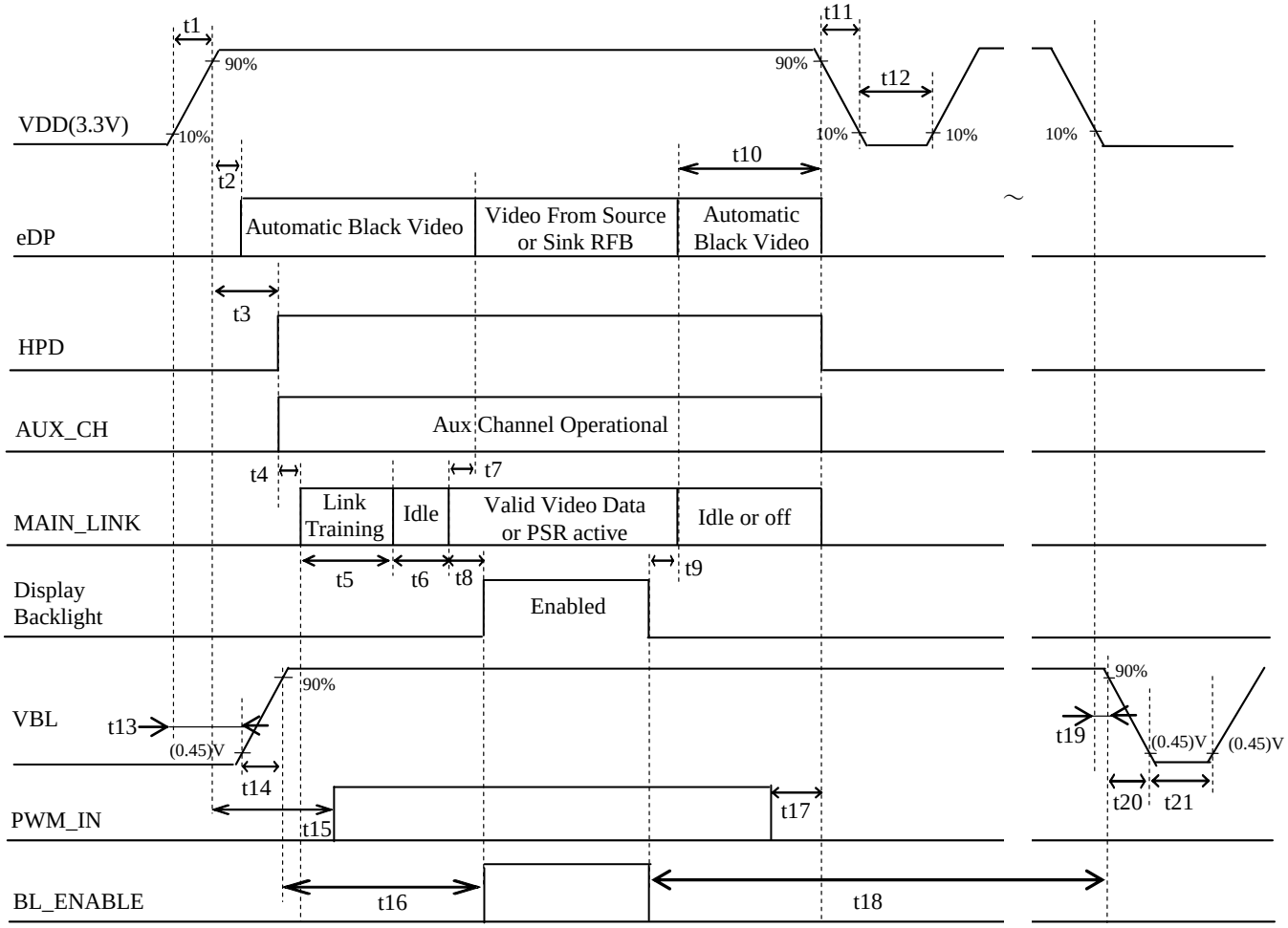
Ta＝+25℃

DC Electrical Characteristics						
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Supply voltage	VDD	+3.0	+3.3	+3.6	V	[Note 6-1-1]
Current dissipation △2	IDD	—	242	303	mA	[Note 6-1-4]
Power dissipation △2	PDD	—	0.8	1.0	W	[Note 6-1-4]
High level PWM voltage △1	-	VDD*0.7	-	VDD	V	[Note 6-1-5]
Low level PWM voltage △1	-	0	-	VDD*0.3	V	[Note 6-1-5]
High level BIST voltage	V_{BIST_H}	+1.26	-	VDD	V	[Note 6-1-6]
Low level BIST voltage	V_{BIST_L}	0	-	+0.54	V	[Note 6-1-6]
Hot plug Detection	HPD	+2.25	+2.5	+2.75	V	
Permissive input ripple voltage	V_{RP}	—	—	100	mV_{P-P}	VDD = +3.3V

eDP AUX Channel Characteristics						
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Unit Interval for AUX channel	UI_{AUX}	0.4	0.5	0.6	μs	
peak-to-peak voltage at TP1	$V_{AUX-DIFF-pp}$	0.32	-	1.36	V	
AUX DC Common Mode Voltage	$V_{AUX-DC-CM}$	0	-	2.0	V	
AUX Short Circuit Current Limit	I_{AUX_SHORT}	-	-	90	mA	
AUX CH termination DC resistance	R_{AUX-_TERM}	-	100	-	Ω	
AUX AC Coupling Capacitor	C_{AUX}	75	-	200	nF	
Number of pre-charge pulses	Pre-charge pulses	10	-	16	-	

eDP Main Link Receiver Characteristics						
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Link clock down spreading	Down_Spread_ Amplitude	0		0.5	%	
Differential Peak-to-peak Input Voltage at RX package pins	$V_{RX-DIFFp-p}$	120	-	1200	mV	
Differential Return Loss at 1.35 GHz at RX package pins	$RL_{RX-DIFF}$	9	-	-	dB	
Differential termination resistance	$V_{RX-TERM}$	-	100	-	Ω	
RX Short Circuit Current Limit	$I_{RX-SHORT}$	-	-	50	mA	
Lane Intra-pair Skew at RX package pins	$L_{RX-SKEW-INTRA-PAIR-High-Bit-Rate}$	-	-	100	ps	

[Note 6-1-1] ON-OFF conditions for supply voltage



[Note6-1-2] Do not keep the interface signal high-impedance or unusual signal when power is on.

Symbol	Min	Max	Unit	Note
t1	0.5	10	ms	
t2	0	200	ms	
t3	0	200 △1	ms	
t4	—	—	ms	
t5	—	—	ms	
t6	—	—	ms	
t7	0	50	ms	
t8	—	—	ms	
t9	—	—	ms	
t10	0	500	ms	
t11	1	50	ms	[Note 6-1-3]
t12	500	—	ms	
t13	-	-	ms	
t14	0.5	10	ms	

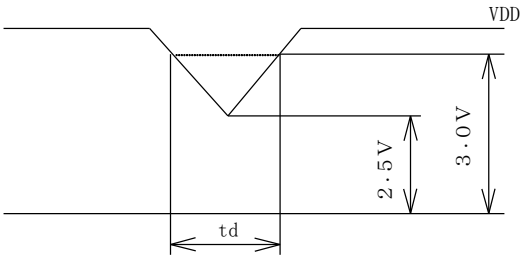
t15	100		ms	
t16	0	-	ms	
t17	0	-	ms	
t18		-		
t19	-	-	ms	
t20	0.1	-	ms	
t21	100		ms	

[Note 6-1-3] As for the power off sequence for VDD (t11), Be sure to keep above mentioned timing.
If the VDD power off sequence timing is other than shown above, LCD may cause permanent damage.
*1 : As for the power sequence for backlight, it is recommended to apply above mentioned input timing.
If the backlight is light on and off at a timing other than shown above, displaying image may get disturbed.

VDD-dip conditions

1) 2.5 V≦VDD＜3.0 V
td≦10 ms
Under above condition, the display image should return to an appropriate figure after VDD voltage recovers.

2) VDD＜2.5 V
VDD-dip conditions should also follow the ON-OFF conditions for supply voltage



[Note 6-1-4] Current and Power dissipation condition: White pattern.
VDD=+3.3V

[Note 6-1-5] PWM : BL_PWM_DIM signal input. △1

[Note 6-1-6] LCD TEST MODE signal input

6 - 2. Backlight driving

The backlight system is an edge-lighting type with white-LED.

(It is usually required to measure under the following condition. : Ta=25℃±2℃)

Parameter	Symbol	Min.	Typ.	Max	Unit	Remark
Supply voltage	V_{BL}	5.0	12.0	21.0	V	
Current dissipation △2	I_{BL}	—	215.8	220.8	mA	V_{BL}= 12.0V
Power dissipation △2	P_{BL}	—	2.59	2.65	W	V_{BL}= 12.0V
Modulated light signal voltage	$V_{PWM}H$	1.85	—	VDD	V	
	$V_{PWM}L$	0	—	0.7	V	
Brightness Control Duty Ratio	Duty	5	—	100	%	【Note6-2-1】
Brightness Control pulse width	T_{PWM}	5	—	—	us	【Note6-2-2】
Brightness Control frequency	f_{PWM}	200	—	2,000	Hz	
LED-BL ON/OFF High voltage	$V_{BLEN}H$	1.3	—	VDD	V	【Note6-2-3】
LED-BL ON/OFF Low voltage	$V_{BLEN}L$	0	—	0.5	V	
Input signal pin current	I_{IN}	—	—	10	μA	BL_ENABLE, BL_PWM_DIM
LED lifetime	-	—	15,000	—	h	LED

【Note6-2-1】 V_{PWM} Input : 100%= Max luminance 5%= Min luminance

【Note6-2-2】 The minimum value of the dimming signal pulse width is assumed regulations of the width of high and the width of low.

【Note6-2-3】 BL_ENABLE Input : High = BL turn on, Low or OPEN =BL turn off

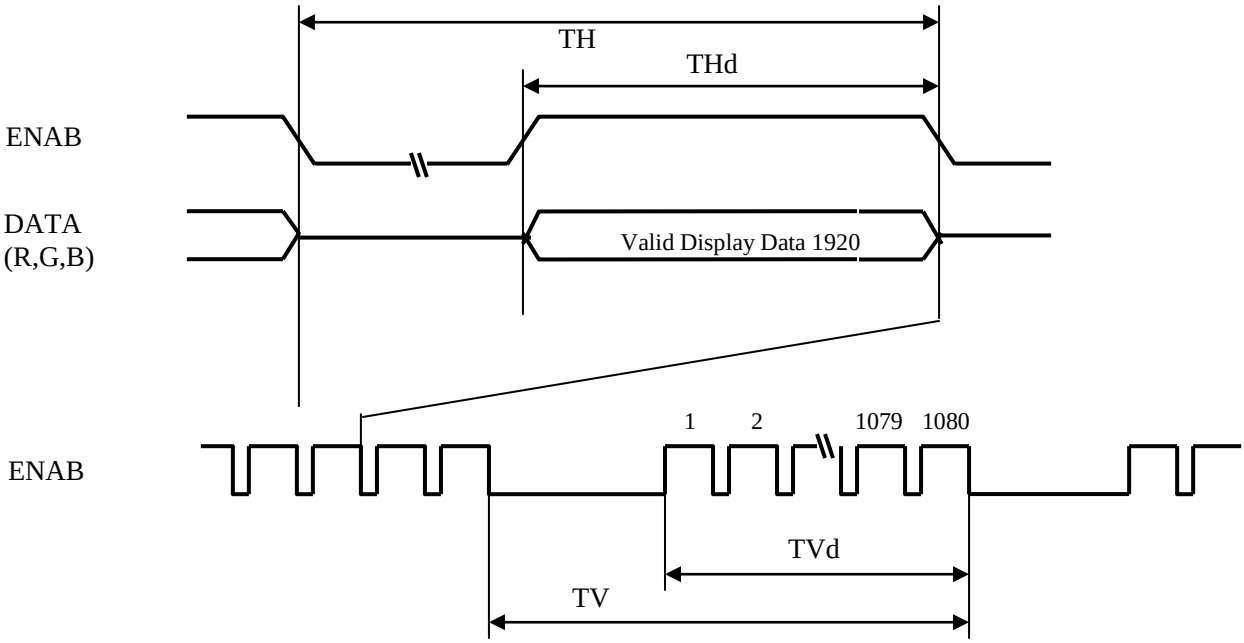
7. Timing Characteristics of Input Signals

7-1. Timing characteristics

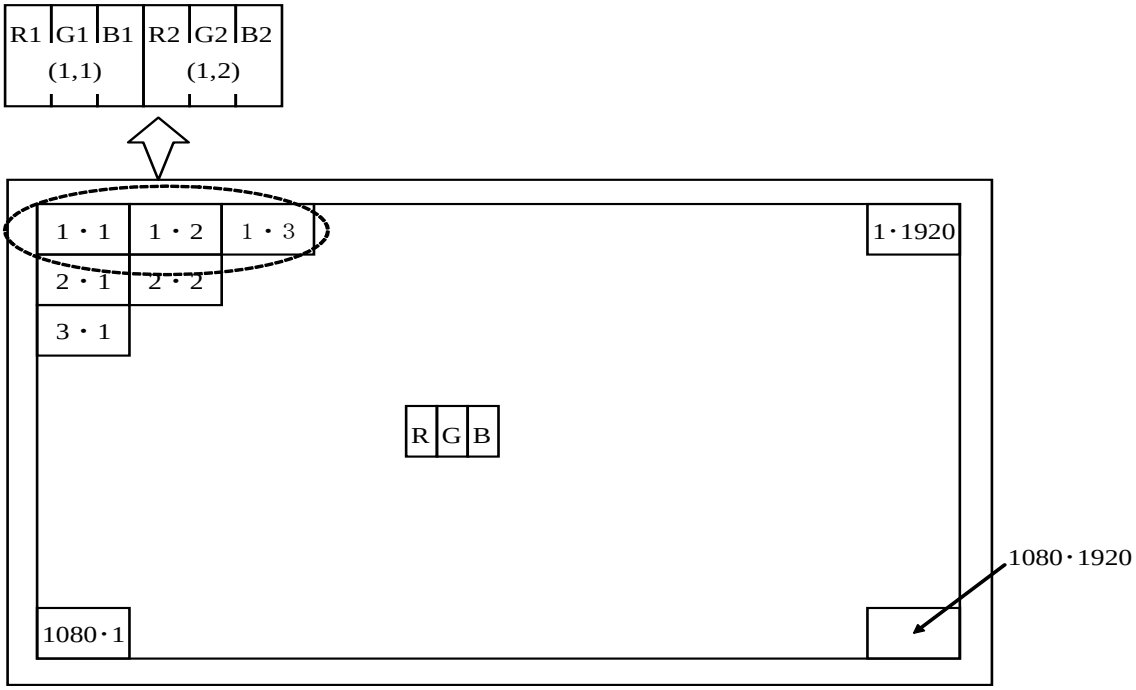
VDD=+3.0V～+3.6V, Ta＝0℃～+50℃

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Clock	Frequency	1/Tc	－	138.5	－	MHz	[Note 7-1-1]
Data enable Signal	Horizontal period	TH	－	2080	－	clock	
				15.02		μs	
	Horizontal period (High)	THd	－	1920	－	clock	
	Vertical period	TV	－	1111	－	line	
			－	16.685	－	ms	
	Vertical period (High)	TVd	－	1080	－	line	

[Note 7-1-1] In case of using the long vertical period, the deterioration of display quality, flicker, etc, may occur.



7-2. Input data signals and display position on the screen



Display position of input data(V・H)

8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors & Gray Scale	Gray Scale	Date signal																							
			R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7
			LSB							MSB	LSB							MSB	LSB							MSB
Basic Color	Black	–	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	–	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	–	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	–	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	–	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	–	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	–	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	–	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓					↓							↓								↓				
	⇩	↓					↓							↓								↓				
	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇩	GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓					↓							↓								↓				
	⇩	↓					↓							↓								↓				
	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	⇩	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	⇧	↓					↓							↓								↓				
	⇩	↓					↓							↓								↓				
	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
	⇩	GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Blue	GS255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage

Each basic color can be displayed in 256 gray scales from 8 bit data signals.

According to the combination of 24 bit data signals, the 16.7M color display can be achieved on the screen.

9. EDID Specifications

9 - 1. EDID data structure △1

This is the EDID(Extended Display Identification Data) data formats to support displays as defined in the VESA Plug & Display

Byte (decimal)	Byte (hex)	Field Name and Comments	Value (hex)	Value (binary)
0	00	Header	00	00000000
1	01	Header	FF	11111111
2	02	Header	FF	11111111
3	03	Header	FF	11111111
4	04	Header	FF	11111111
5	05	Header	FF	11111111
6	06	Header	FF	11111111
7	07	Header	00	00000000
8	08	EISA manufacture code =3 Character ID " SHP"	4D	01001101
9	09	EISA manufacture code (Compressed ASCII)	10	00010000
10	0A	Panel Supplier Reserved - Product code (LQ133M1JW03 : 5151)	1F	00011111
11	0B	Panel Supplier Reserved - Product code (hex,LSB first)	14	00010100
12	0C	LCD module Serial No	00	00000000
13	0D	LCD module Serial No	00	00000000
14	0E	LCD module Serial No	00	00000000
15	0F	LCD module Serial No	00	00000000
16	10	Week of manufacture	23	00100011
17	11	Year of manufacture	18	00011000
18	12	EDID structure version # = 1	01	00000001
19	13	EDID revision # = 4	04	00000100
20	14	Video I/P definition = Digital I/P(A5h)	A5	10100101
21	15	Max H image size = (Rounded to cm)=29.376cm=1Dh	1D	00011101
22	16	Max V image size = (Rounded to cm)=16.524cm=11h	11	00010001
23	17	Display gamma=(gamma×100)-100= (2.2×100) – 100 = 120=78h	78	01111000
24	18	Feature support(no DPMS,Active off, sRGB,Prefer Time)	0E	00001110
25	19	Red/Green Low bit(RxRy/GxGy)	DE	11011110
26	1A	Blue/White Low bit(BxBy/WxWy)	50	01010000
27	1B	Red X Rx = 0.64	A3	10100011
28	1C	Red Y Ry = 0.33	54	01010100
29	1D	Green X Gx = 0.3	4C	01001100
30	1E	Green Y Gy = 0.6	99	10011001

31	1F	Blue X Bx = 0.15	26	00100110
32	20	Blue Y By = 0.06	0F	00001111
33	21	White X Wx = 0.313	50	01010000
34	22	White Y Wy = 0.329	54	01010100
35	23	Established timings 1 (00h if not used)	00	00000000
36	24	Established timings 2 (00h if not used)	00	00000000
37	25	Manufacture's timings (00h if not used)	00	00000000
38	26	Standard timing ID1 (01h if not used)	01	00000001
39	27	Standard timing ID1 (01h if not used)	01	00000001
40	28	Standard timing ID2 (01h if not used)	01	00000001
41	29	Standard timing ID2 (01h if not used)	01	00000001
42	2A	Standard timing ID3 (01h if not used)	01	00000001
43	2B	Standard timing ID3 (01h if not used)	01	00000001
44	2C	Standard timing ID4 (01h if not used)	01	00000001
45	2D	Standard timing ID4 (01h if not used)	01	00000001
46	2E	Standard timing ID5 (01h if not used)	01	00000001
47	2F	Standard timing ID5 (01h if not used)	01	00000001
48	30	Standard timing ID6 (01h if not used)	01	00000001
49	31	Standard timing ID6 (01h if not used)	01	00000001
50	32	Standard timing ID7 (01h if not used)	01	00000001
51	33	Standard timing ID7 (01h if not used)	01	00000001
52	34	Standard timing ID8 (01h if not used)	01	00000001
53	35	Standard timing ID8 (01h if not used)	01	00000001
54	36	Pixel clock/10000 (LSB) (138.5MHz : 13850 = 361Ah)	1A	00011010
55	37	Pixel clock/10000 (MSB)	36	00110110
56	38	Horizontal active =xxxx pixels(lower 8bits) (1920 = 780h)	80	10000000
57	39	Horizontal blanking(Thbp)=xxxx pixels(lower 8bits)(Thbp=160=0A0h)	A0	10100000
58	3A	Horizontal active/Horizontal blanking (Thbp) (upper4:4 bits)	70	01110000
59	3B	Vertical active =xxxx lines(lower 8bits) (1080 = 438h)	38	00111000
60	3C	Vertical blanking (Tvbp) =xxxx lines(DE Blanking min for DE only panels)	1F	00011111
61	3D	Vertical active:Vertical blanking (Tvbp) (upper4:4bits)	40	01000000
62	3E	Horizontal sync , offset (Thfp) =xxxx pixels (48 = 30h)	30	00110000
63	3F	Horizontal sync , Pulse Width = xxxx pixels (32 = 20h)	20	00100000
64	40	Vertical Sync,Offset(Tvfp) = xx lines:Sync Width = xx lines	35	00110101
65	41	Horizontal Vertical Sync Offset/Width upper 2 bits	00	00000000
66	42	Horizontal image size = xxx mm (lower 8bits)(293.76mm = 126h)	26	00100110
67	43	Vertical image size = xxx mm (lower 8bits)(165.24mm = 0A5h)	A5	10100101
68	44	Horizontal image size / Vertical image size (upper 4bits)	10	00010000

69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
71	47	Non-interlaced,Normal,Digtal without Serrations,Negative	18	00011000
72	48	Pixel clock/10000 (LSB)	1A	00011010
73	49	Pixel clock/10000 (MSB)	36	00110110
74	4A	Horizontal active =xxxx pixels(lower 8bits)	80	10000000
75	4B	Horizontal blanking(Thbp)=xxxx pixels(lower 8bits)	A0	10100000
76	4C	Horizontal active/Horizontal blanking (Thbp) (upper4:4 bits)	70	01110000
77	4D	Vertical active =xxxx lines(lower 8bits)	38	00111000
78	4E	Vertical blanking (Tvbp) =xxxx lines(DE Blanking min for DE only panels)	33	00110011
79	4F	Vertical active:Vertical blanking (Tvbp) (upper4:4bits)	41	01000001
80	50	Horizontal sync , offset (Thfp)	30	00110000
81	51	Horizontal sync , Pulse Width = xxxx pixels	20	00100000
82	52	Vertical Sync,Offset(Tvfp) = xx lines:Sync Width = xx lines	75	01110101
83	53	Horizontal Vertical Sync Offset/Width upper 2 bits	0A	00001010
84	54	Horizontal image size = xxx mm (lower 8bits)	26	00100110
85	55	Vertical image size = xxx mm (lower 8bits)	A5	10100101
86	56	Horizontal image size / Vertical image size (upper 4bits)	10	00010000
87	57	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
88	58	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
89	59	Flags	18	00011000
90	5A	Flag	00	00000000
91	5B	Flag	00	00000000
92	5C	Flag	00	00000000
93	5D	Dummy Descriptor	FE	11111110
94	5E	Flag	00	00000000
95	5F	Dell P/N 1st Character (3 = 33h)	33	00110011
96	60	Dell P/N 2nd Character (0 = 30h)	30	00110000
97	61	Dell P/N 3rd Character (8 = 38h)	38	00111000
98	62	Dell P/N 4th Character (X = 58h)	58	01011000
99	63	Dell P/N 5th Character (0 = 30h)	30	00110000
100	64	LCD supplier EEDID revision #	80	10000000
101	65	Manufacture P/N (L = 4Ch)	4C	01001100
102	66	Manufacture P/N (Q = 51h)	51	01010001
103	67	Manufacture P/N (1 = 31h)	31	00110001
104	68	Manufacture P/N (3 = 33h)	33	00110011
105	69	Manufacture P/N (3 = 33h)	33	00110011
106	6A	Manufacture P/N (M = 4Dh)	4D	01001101
107	6B	Manufacture P/N (1 = 31h)	31	00110001

108	6C	Flag	00	00000000
109	6D	Flag	00	00000000
110	6E	Flag	00	00000000
111	6F	Data Type Tag: Manufacture Specified Data 00	00	00000000
112	70	Flag	00	00000000
113	71	Color Management	02	00000010
114	72	Panel Type and Revision	01	00000001
115	73	Frame Rate	03	00000011
116	74	Light Controller Interface and Maximum Luminance	28	00101000
117	75	Outdoor Features and Polarizer	00	00000000
118	76	Multi-Media Features	12	00010010
119	77	Multi-Media Features	00	00000000
120	78	Special Features	00	00000000
121	79	Special Features	0A	00001010
122	7A	Special Features	01	00000001
123	7B	(If<13 char,then terminate with ASCII code 0Ah,set remaining char 20h)	0A	00001010
124	7C	(If<13 char,then terminate with ASCII code 0Ah,set remaining char 20h)	20	00100000
125	7D	(If<13 char,then terminate with ASCII code 0Ah,set remaining char 20h)	20	00100000
126	7E	Extension flag (# of optional 128 EDID extension blocks to follow, Typ=0)	00	00000000
127	7F	Checksum(The 1-byte sum of all 128 bytes in this EDID block shall=0)	35	00110101

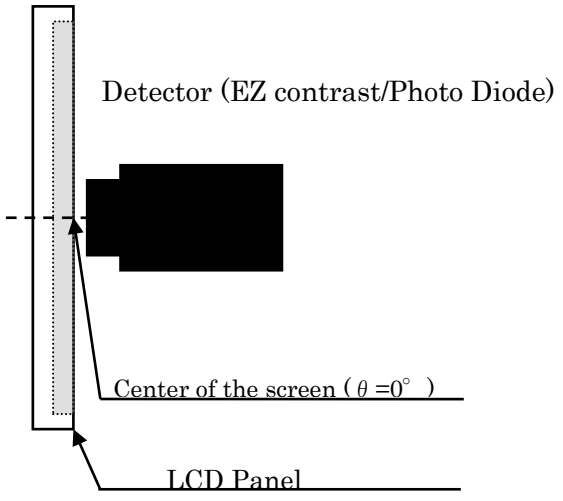
10. Optical Characteristics

Ta=+25℃, VDD=+3.3V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Horizontal	θ 21, θ 22	CR>10	80	88	—	deg.	[Note 10-1, 10-3, 10-4, 10-6]
	Vertical	θ 11		80	88	—	deg.	
		θ 12		80	88	—	deg	
Contrast ratio		CR	θ =0°	700	1000	—		[Note 10-2, 10-4, 10-6]
Response time		τ r+ τ d	θ =0°	—	25	—	ms	[Note 10-1, 10-5, 10-6]
Chromaticity of white		x		0.283	0.313	0.343		[Note 10-2, 10-6] Normal operation (PWM Duty=100%)
		y		0.299	0.329	0.359		
Chromaticity of red △3		x		—	0.640	—		
		y		—	0.330	—		
Chromaticity of green △3		x		—	0.300	—		
		y		—	0.600	—		
Chromaticity of blue △3		x		—	0.150	—		
		y		—	0.060	—		
NTSC ratio		%		68	72	—		
Luminance of white		Y_{LI}		340	400	—	cd/m^2	
White uniformity △3		δ w	θ =0°	—	—	1.25		[Note 10-2, 10-7]
		δ w13		—	—	1.5		[Note 10-2, 10-8]

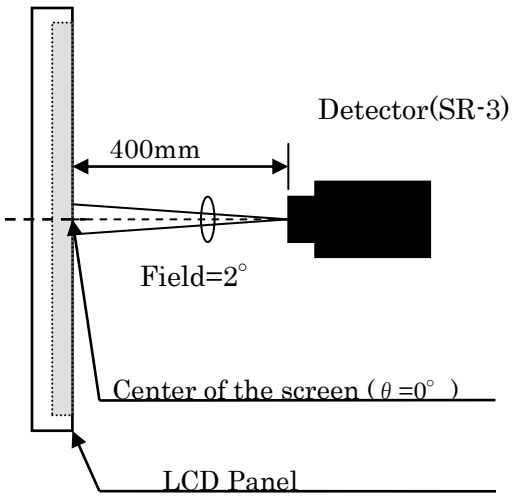
※ The measurement shall be taken 30 minutes after lighting the module at the following rating.
Condition: PWM Duty = 100%
The optical characteristics shall be measured in a dark room or equivalent.

[Note 10-1] Measurement of viewing angle range and Response time.

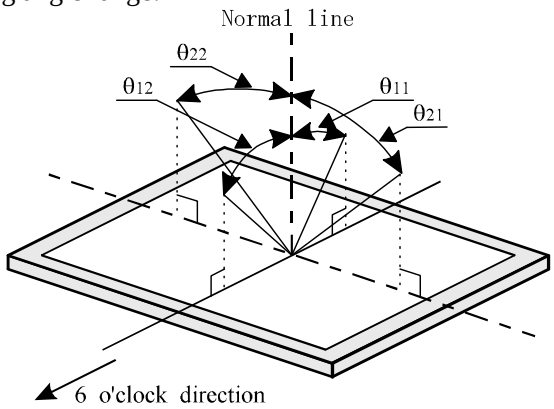


Viewing angle range:EZ-CONTRAST
/Response time: Photo diode)

[Note 10-2] Measurement of luminance and Chromaticity and Contrast.



[Note 10-3] Definitions of viewing angle range:



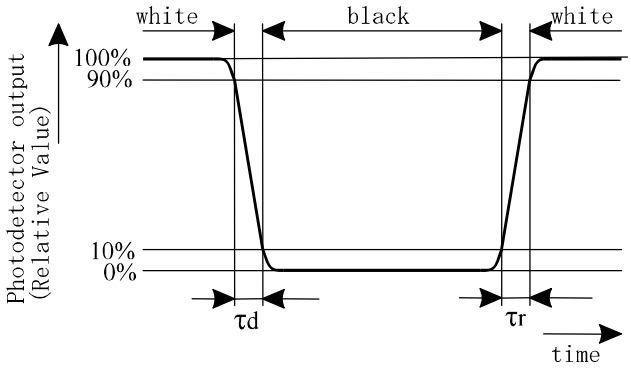
[Note 10-4] Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note 10-5] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white" .

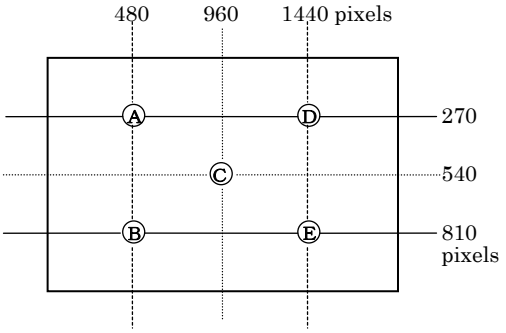


[Note 10-6] This shall be measured at center of the screen.

[Note 10-7] Definition of white uniformity:

White uniformity is defined as the following with five measurements (A～E).

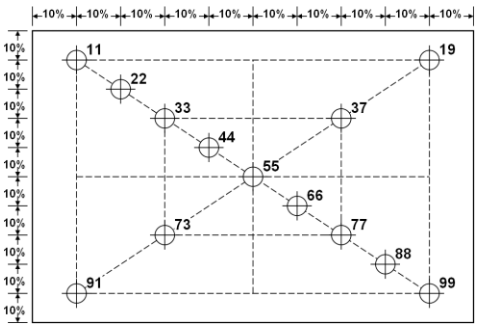
$$\delta w5 = \frac{\text{Maximum Luminance of five points (brightness)}}{\text{Minimum Luminance of five points (brightness)}}$$



[Note 10-7] Definition of white uniformity:

White uniformity is defined as the following with thirteen measurements (11,19,22,33,37,44,55,66,73,77,88,91,99).

$$\delta w13 = \frac{\text{Maximum Luminance of thirteen points (brightness)}}{\text{Minimum Luminance of thirteen points (brightness)}}$$



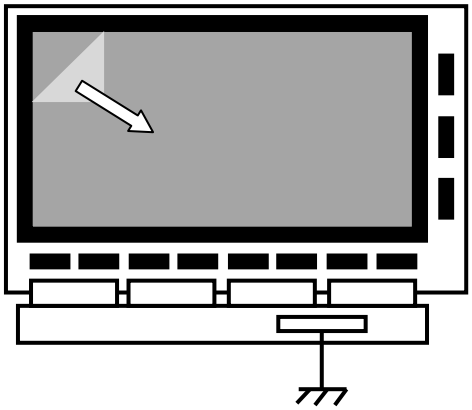
11. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

12. Handling Precautions △3

a) Be sure to turn off the power supply when inserting or disconnecting the cable.
Please insert for too much stress not to join a connector in the case of insertion of a connector.
b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
c) Since the front polarizer is easily damaged, pay attention not to scratch it.
d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling components.
h) This module has its circuitry PCBs on the side and should be handled carefully in order not to be stressed.
i) Laminate film is attached to the module surface to prevent it from being scratched. Peel the laminate film off slowly just before the use with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off the 'dust' on the polarizer by using an ionized nitrogen gun, etc. Working under the following environments is desirable.

- All workers wear conductive shoes, conductive clothes, conductive fingerstalls and grounding belts without fail.
- When you peel off the protection film,
  -Be sure to peel off slowly (recommended more than 5sec) and constant speed.
  -Peeling direction shows the following Figure.
  -Be sure to ground person with adequate methods such as the anti-static wrist band.
  -Be sure to ground all terminals of the PWB connector while peeling of the protection film.
  -Ionized air should be blown over during peeling action.
  -The protection film must not touch driver-ICs, PWB and all components on PWB.
  -If adhesive may remain on the polarizer after the protection film peeling off, please remove with isopropyl-alcohol.



j) The polarizer surface on the panel is treated with Anti Glare . In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.

k) Do not expose the LCD module to a direct sunlight, for a long period of time to protect the module from the ultra violet ray.

l) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.

m) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.

n) Disassembling the module can cause permanent damage and should be strictly avoided.
Please don't remove the fixed tape, insulating tape etc that was pasted on the original module.
(Except for protection film of the panel.)

o) Be careful when using it for long time with fixed pattern display as it may cause afterimage.
(Please use a screen saver etc., in order to avoid an afterimage.)

p) If a minute particle enters in the module and adheres to an optical material, it may cause display non-uniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.

q) Epoxy resin (amine series curing agent), silicone adhesive material (dealcoholization series and oxime series),
tray forming agent (azo compound) etc, in the cabinet or the packing materials may induce abnormal display with polarizer film deterioration regardless of contact or noncontact to polarizer film.
Be sure to confirm the component of them.

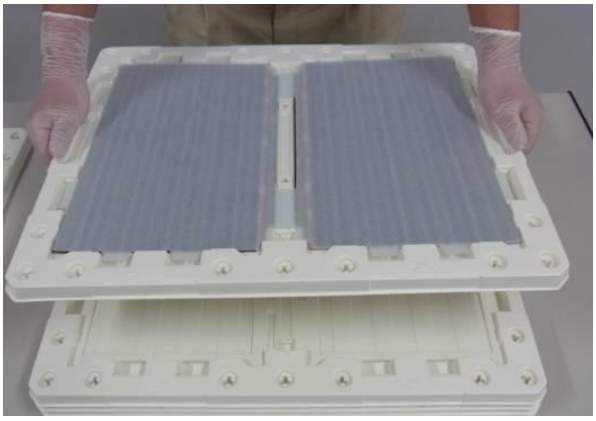
r) Do not use polychloroprene. If you use it, there is some possibility of generating Cl_2 gas that influences the reliability of the connection between LCD panel and driver IC.

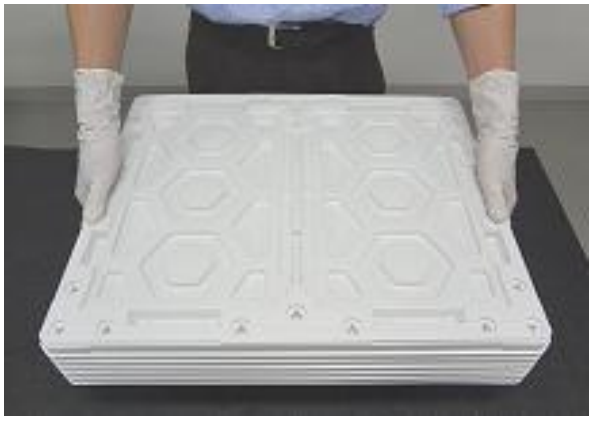
s) Do not put a laminate film on LCD module, after peeling of the original one. If you put on it, it may cause discoloration or spots because of the occurrence of air gaps between the polarizer and the film.

t) Ground module bezel to stabilize against EMI and external noise.

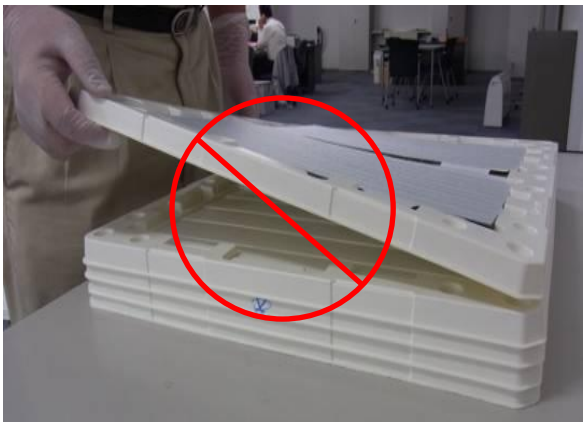
13. Precautions for Handling Tray △3

a.) Hold center of short side of tray with both hands when handling one or more trays.





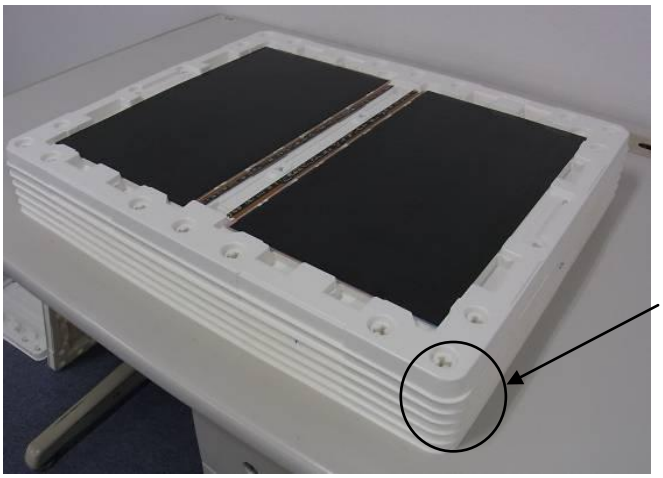
Caution：Do not handle with only one hand .



b.) Always place tray on flat surface.

c.) When stacking trays, please align same shape corner of each tray.
One corner is R corner. (Ref. Pic. 1)

d.) Maximum stacking quantity is the number of trays inside one box.
Ex.：In case of 30pcs LCD module per box (2pcs LCD module per tray), maximum stacking is 15 trays.



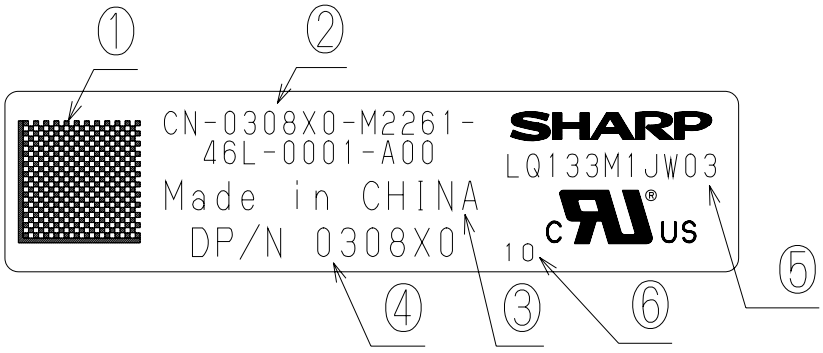
Pic.1 Stacking tray

14. Packaging Condition △3

Piling number of cartons	6
Package quantity in one carton	30 pcs
Carton size	554 x 444 x 240 mm
Total mass of one carton filled with full modules	13 kg
Packing form	Fig.1

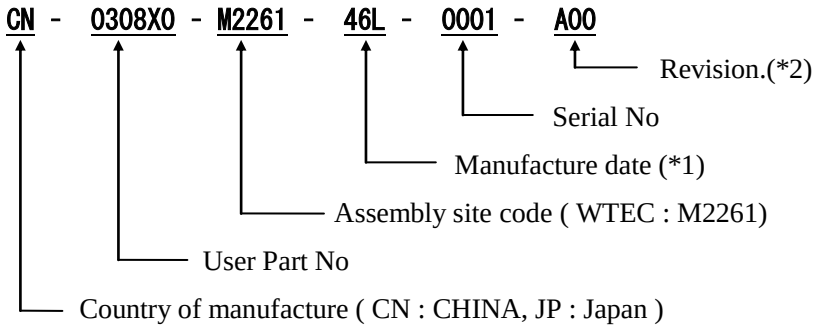
15. Label △3

1) Module Bar code label:



①Barcode(User code) ②User code ③Country of manufacture ④User Part No
⑤SHARP Model No ⑥Factory code

②User code contents



(*1) Manufacture date

1st digit : year (The last one digit of the year)

2nd digit : month (Jan:"1", Feb:"2", Oct:"A")

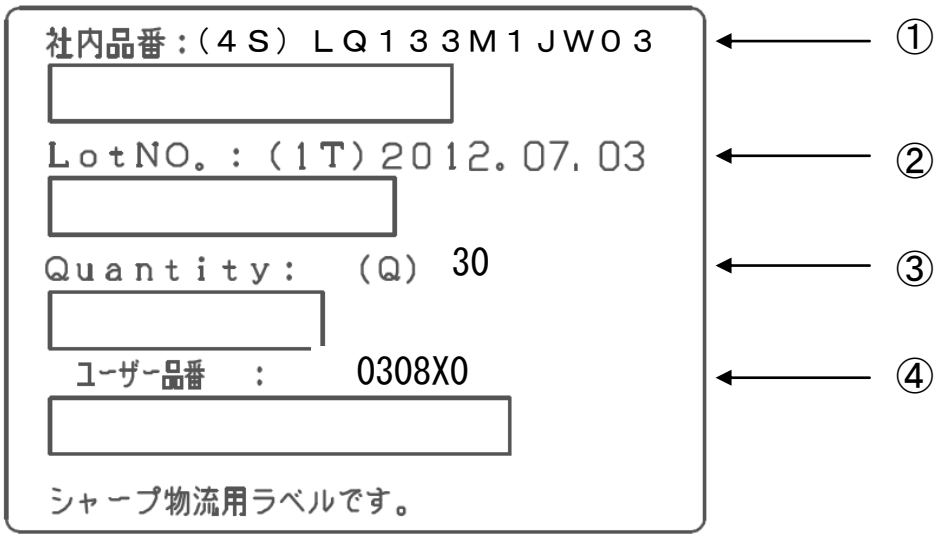
3rd digit : day (1st : "1", 2nd :"2", 10th : "A" 31st : "V")

(*2) Revision

Event production	Rev0	Rev1	・・・
SSI (TS)	X00	X01	・・・
PT (ES)	X10	X11	・・・
ST (CS)	X20	X21	・・・
XB (MP)	A00	A01	・・・

2) Packing bar code label

Notation/ Bar code: ①Model No. ②Date ③Quantity (30pcs / Carton) ④User No.



16. RoHS Directive

This LCD module is compliant with RoHS Directive.

17. Reliability Test Items △3

No.	Test item	Conditions
1	High temperature storage test	Ta = 60℃ 240h
2	Low temperature storage test	Ta = -25℃ 240h
3	High temperature & high humidity operation test	Ta = 40℃ ; 90%RH 240h (No condensation)
4	High temperature operation test	Ta = 50℃ 240h
5	Low temperature operation test	Ta = 0℃ 240h
6	Vibration test (non- operating)	Frequency:10~57Hz/Vibration width (one side):0.076mm :57~500Hz/Acceleration:9.8m/s^2 Sweep time: 11minutes Test period: 1 hour for each direction of X,Y,Z
7	Shock test (non- operating)	Max. gravity : 490 m/s^2 Pulse width : 11 ms, half sine wave Direction : ±X,±Y,±Z once for each direction.
8	ESD	Panel center, Around the module : One time for each position Contact discharging method:C=150pF,R=330Ω (non-operating) Pass +/- 10kV (operating) Pass +/- 8kV Air discharge method:C=150pF,R=330Ω (non-operating) Pass +/- 20kV (operating) Pass +/- 15kV

[Result Evaluation Criteria] Under the display quality test conditions with normal operation state. Do not change these conditions as such changes may affect practical display function.

[Normal operation state] Temperature : +15～+35℃, Humidity : 45～75%, Atmospheric pressure : 86～106kPa

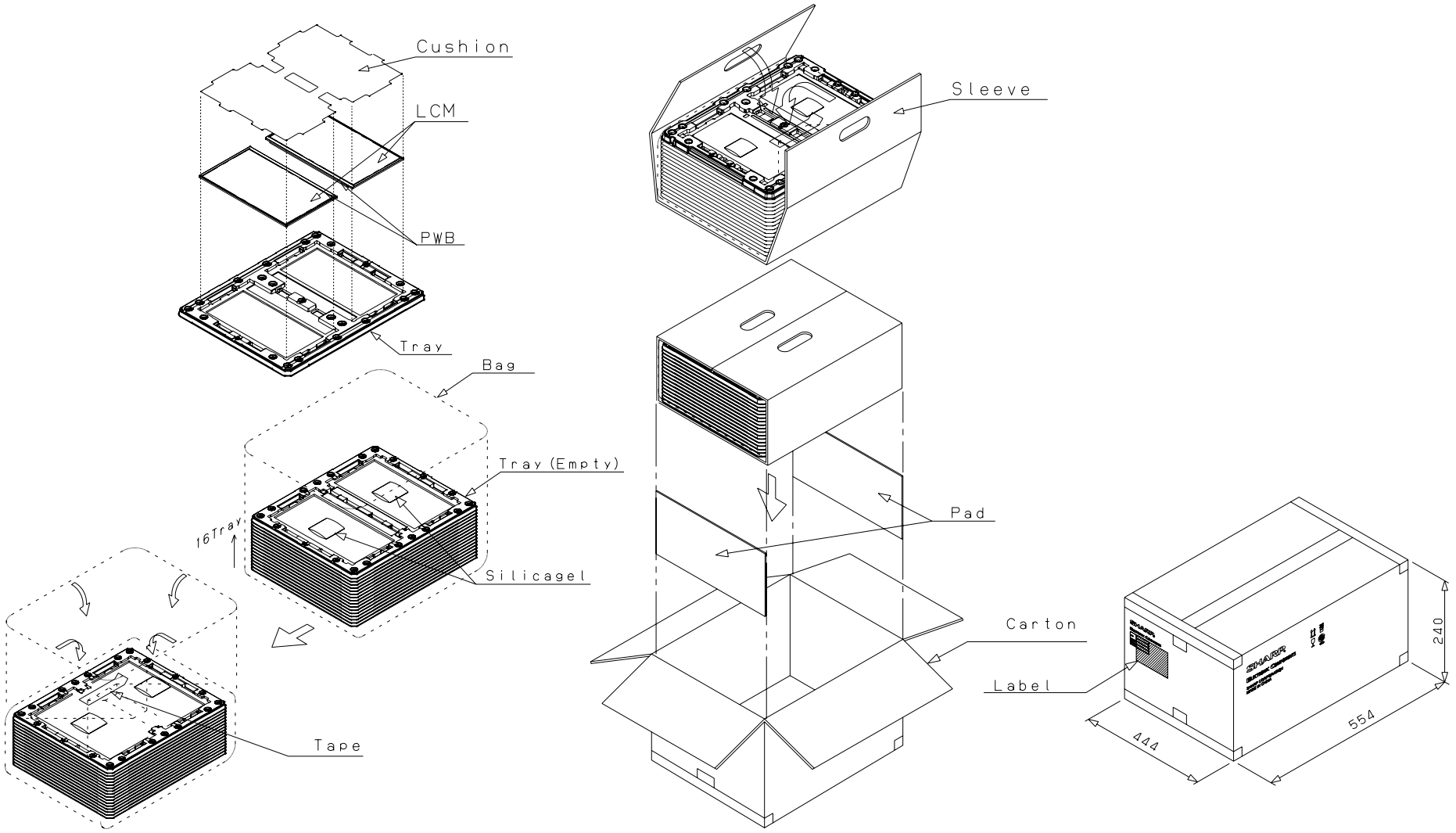
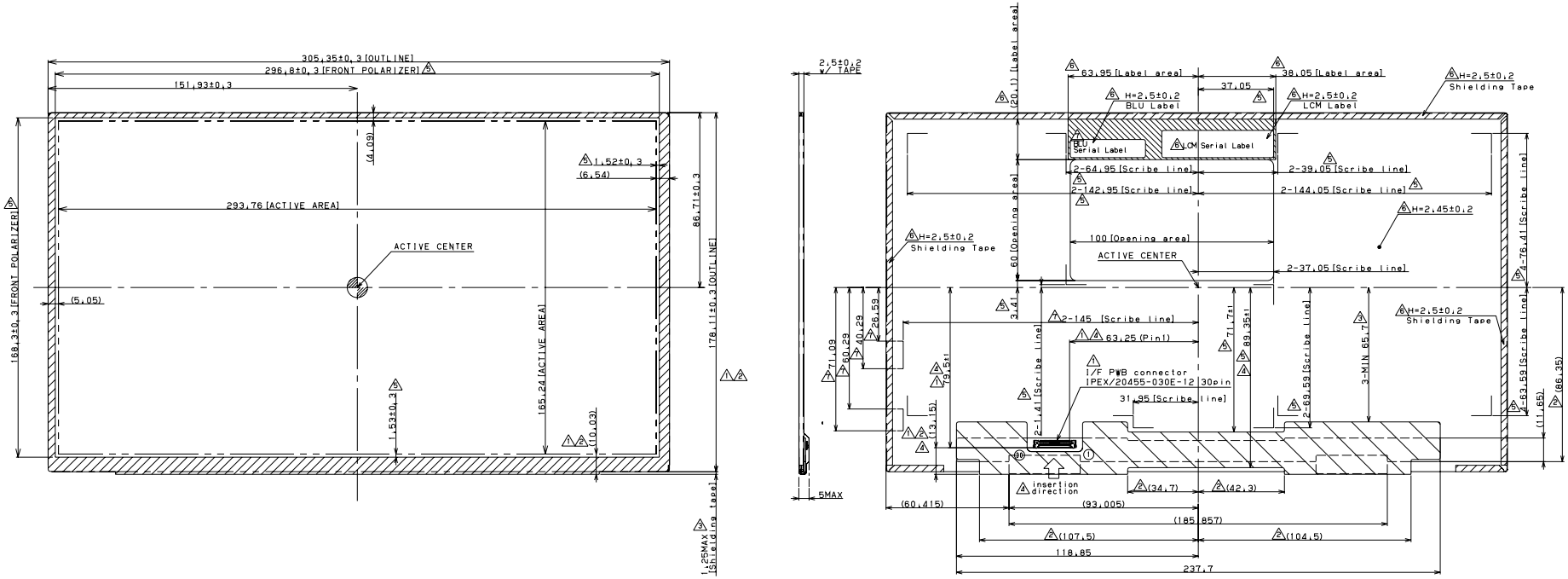


Fig. 1 Packing Condition △3

SHARP Confidential

TENTATIVE



△1	03, Jul, 2014	Add Scribe line Change the BL Label size
△6	09, Dec, 2013	Add Label area Add dimensions of the Shielding Tape
△5	28, Nov, 2013	Add dimensions of the polarizer Change the Shield Tape Change the Opening area Change the Scribe line
△4	22, Nov, 2013	Change the Shield Tape Change the Connector direction
△3	20, Nov, 2013	Add/Change dimension value Add Scribe line
△2	19, Nov, 2013	Change the Shield Tape
△1	15, Nov, 2013	Add to Connector, and Opening area, and Shield Tape
日付 / DATE		08, Oct, 2013
名称 NAME		13.3FHD OUTLINE
図番 DRAWING No.		2D-13X-068-07
SHARP CORPORATION		

Note
1. Unspecified tolerance to be 0.5.
2. Without warpage and deflection.

Fig. 2 Outline Dimensions △3