All information is subject to change without notice. Please read bottom notes.

FEATURES

TENTATIVE

(1) 14.1 SXGA+(1400x1050 pixels) display size for Notebook PC.
(2) SPWG-B Style dimension
(3) 2ch-LVDS interface system (H-Sync, V-Sync)

MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline (typ.)	299.0 (*W*) x 228.6max (*H*) x 5.5max (*D*) mm
Number of Pixels	1400 (*W*) x 1050 (*H*) pixels
Active Area	285.6 (*W*) x 214.2 (*H*) mm
Viewing Area	287.6 (*W*) x 216.2 (*H*) mm
Pixel Pitch	0.204 (*W*) x 0.204 (*H*)mm
Weight (approximately)	(390) g
Backlight	Single CCFL, Sidelight type

ABSOLUTE MAXIMUM RATINGS

Item		Min.	Max.	Unit
Supply Voltage	(V_{DD})	-0.3	4.0	V
	(V_{FL})	0	2.0	kV(rms)
FL Driving Frequency (f_{FL})		(0)	(100)	KHz
Input Signal Voltage (V_{IN})		-0.3	V_{DD}+0.3	V
Operating Ambient Temperature *1		0	50	°C
Operating Temperature for Panel *2		0	60	°C
Storage Temperature		-20	60	°C
Storage Humidity (Max. wet bulb temperature = 39°C)		10	90	%(RH)

*1: Wet bulb temperature should be 39°C Max., and no condensation of water.
*2: The surface temperature caused by self heat radiation of cell itself is specified on this item.

ELECTRICAL SPECIFICATION (*T*a=25°C)

Item		Min.	Typ.	Max.	Unit	Remarks
Supply Voltage	(V_{DD})	3.0	3.3	3.6	V	
	(V_{FL})	---	625	---	V(rms)	I_{FL} =6.0mA(rms)
FL Start Voltage		1550	---	---	V(rms)	*T*a=0°C
Differential Input Voltage	(V_{ID})	100	---	600	mV	
Common Mode Input Voltage	(V_{CM})	0.5	1.2	1.5	V	
Current Consumption	(I_{DD}) *3	---	(450)	---	mA	
	(I_{FL}) *4	3.0	---	6.0	mA(rms)	
Power Consumption *1 *2		---	(5.2)	---	W	I_{FL} =6.0mA(rms)

*3 : Refer to TH63LVDF84A-85 Specification by Thine Electronics, Inc.
*4: 8 color bars pattern
*5: Except the efficiency of FL inverter

OPTICAL SPECIFICATION (*T*a=25°C)

Item		Min.	Typ.	Max.	Unit	Remarks
Contrast Ratio (*CR*)		100	250	---	---	
Response Time	(t_{ON})	---	---	50	ms	
	(t_{OFF})	---	---	50	ms	
Luminance (*L*)		160	200	---	cd/m^2	I_{FL}=6.0mA(rms)

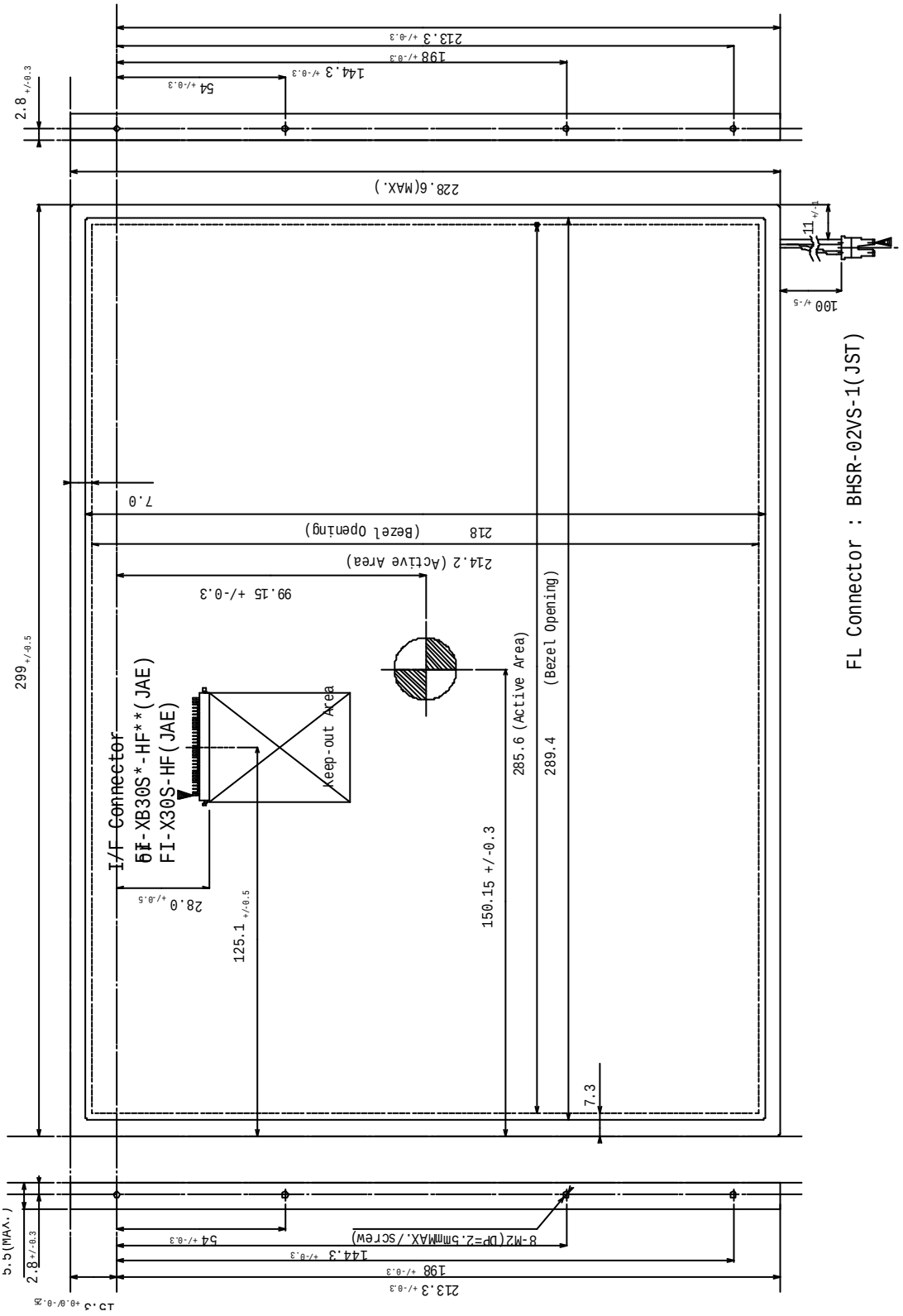
DIMENSIONAL OUTLINE

(Front)

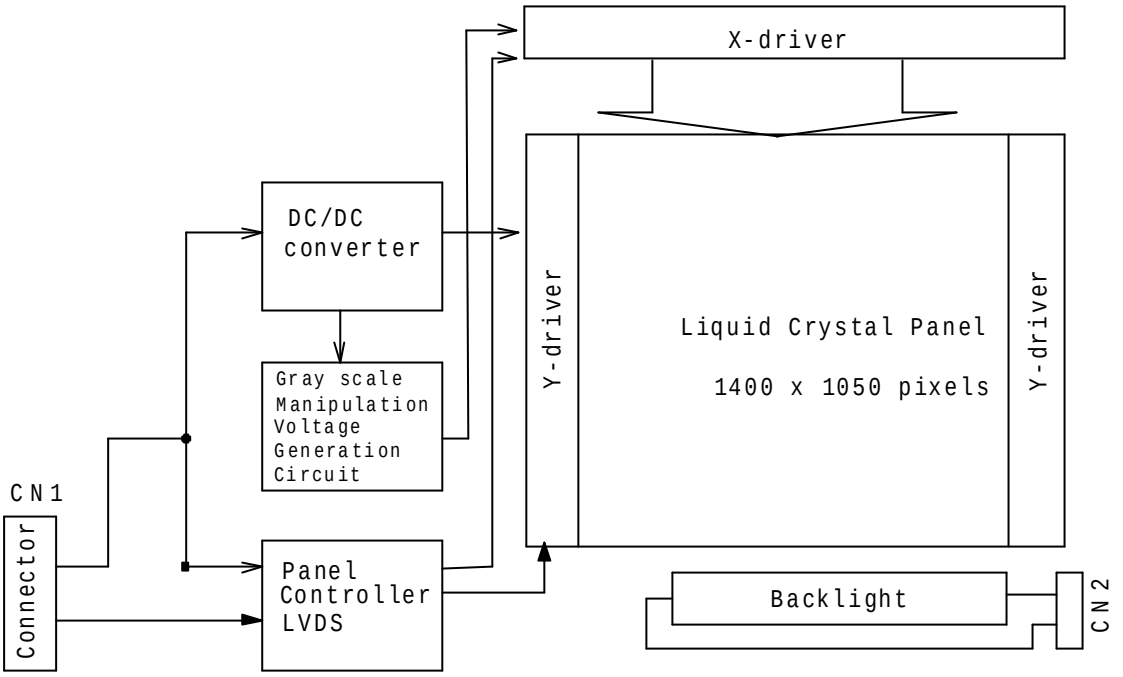
TENTATIVE

Unit : mm

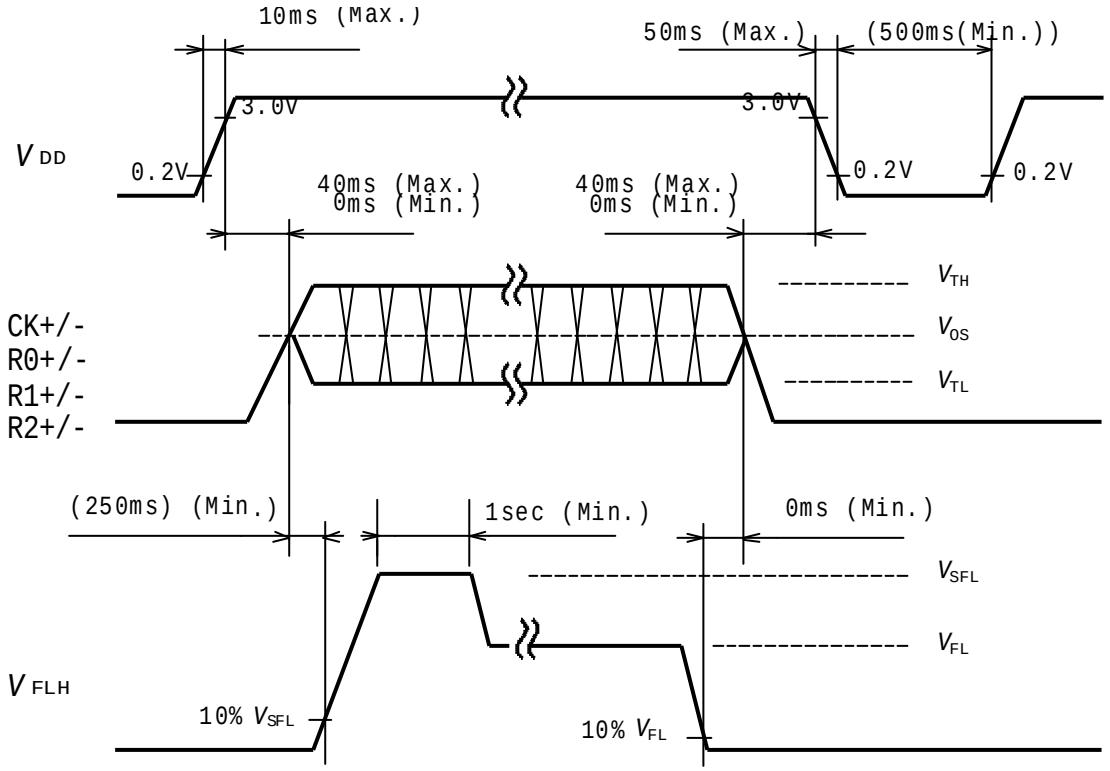
Standard tolerance : +/-0.5



BLOCK DIAGRAM

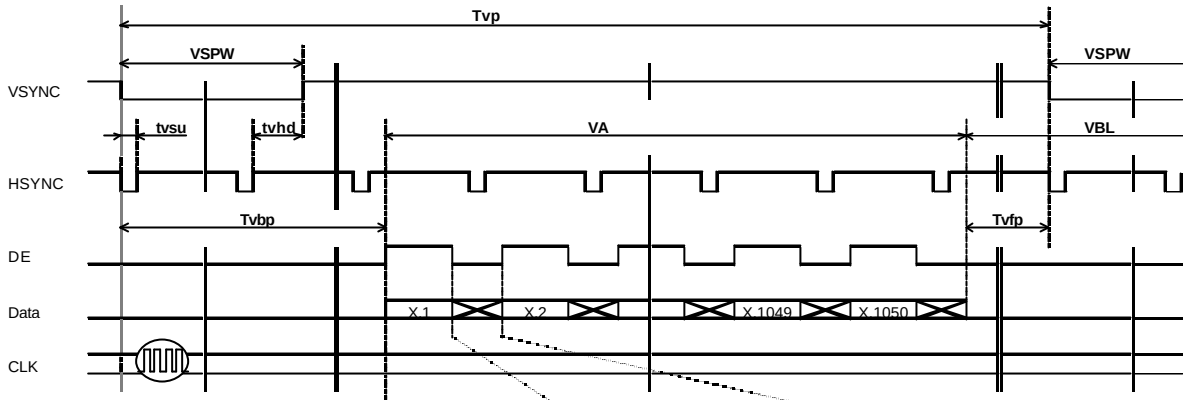


SEQUENCE OF POWER SUPPLIES AND SIGNALS

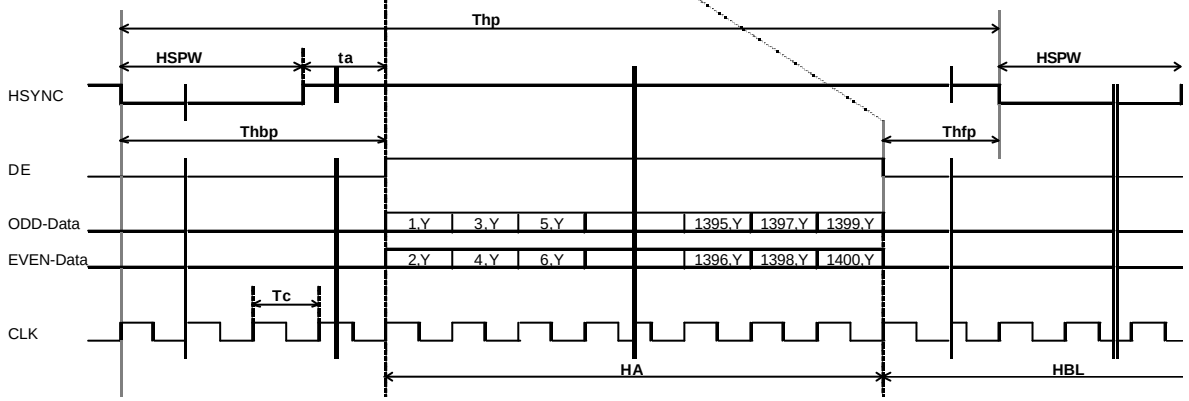


TIMING CHART

(1) Vertical Timing



(2) Horizontal Timing



TIMING SPECIFICATION 1) 2) 3) 4) 5) 6) 7)

Item	Symbol	min.	typ.	max.	unit
Horizontal Scanning Term	*T*hp	828	844	1024	*T*c
H-sync Pulse Width	*HSPW*	8	-	-	*T*c
Horizontal Front Porch	*t*hfp	8	-	-	*T*c
Horizontal Back Porch	*t*a	8	-	-	*T*c
Horizontal Blanking Term	*HBL*	128	144	324	*T*c
Horizontal Display Term	*HA*	700	700	700	*T*c
Frame Period	*T*vp	1059	1066	1066	*T*hp
V-sync Pulse Width	*VSPW*	2	-	-	*T*hp
V-sync Set Up Time (to H-sync)	*tv*su	8	-	-	*T*c
V-sync Hold Time	*tv*hd	8	-	-	-
Vertical Front Porch	*tv*fp	2	-	8	-
Vertical Back Porch	*T*vbp	6	-	14	-
Vertical Blanking Term	*VBL*	10	16	92	*T*hp
Vertical Display Term	*VA*	1050	1050	1050	*T*hp
DE Pulse Width	*HA*	700	700	700	*T*c
Clock Period	*T*c	17.96	18.519	19.597	ns

Note 1) Refer to “Timing Chart” and LVDS (THC63LVDF84A-85) specifications by THine Electronics, Inc.

Note 2) If CLK is fixed to "H" or "L" level for certain period while DE is supplied, the panel may be damaged.

Note 3) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.
There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.

Note 4) Do not make *tv*, *t*h, *t*hbp and tvds fluctuate.
If *tv*, *t*h, *t*hbp and *t*vds are fluctuate, the panel displays black.

Note 5) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note 6) CLK count of each Horizontal Scanning Time should be always the same.
V-Blanking period should be “*n*” X “Horizontal Scanning Time”. (*n*: integer)
Frame period should be always the same.

Note 7) Please keep below equations.
VBL = *T*vfp + *T*vbp
HSPW = *HBL* – *T*hfp – ta
*T*hbp = *HSPW* + ta

CONNECTOR PIN ASSIGNMENT FOR INTERFACE

CN1 INPUT SIGNAL (FI-XB30S*-HF** or FI-X30S-HF** / JAE)

[Mating Connector :Wire Type FI-X30H(Housing), FI-XC3-1-15000(Contact)
FPC Type FI-X30M or FI-X30MR
Coax Type FI-X30C or FI-X30C2(Housing), FI-X30CH-7000(Shell)]

Terminal No.	Symbol	Function
1	V_{SS}	GND
2	V_{DD}	POWER SUPPLY : +3.3V
3	V_{DD}	POWER SUPPLY : +3.3V
4	V_{EDID}	DDC 3.3V POWER SUPPLY : +3.3V
5	NC	Non-Connection
6	CLK_{EDID}	DDC Clock
7	$DATA_{EDID}$	DDC Data
8	RxOIN0-	Negative LVDS differential data input (Odd), [R0-R5, G0]
9	RxOIN0+	Positive LVDS differential data input (Odd), [R0-R5, G0]
10	V_{SS}	GND
11	RxOIN1-	Negative LVDS differential data input (Odd), [G1-G5, B0-B1]
12	RxOIN1+	Positive LVDS differential data input (Odd), [G1-G5, B0-B1]
13	V_{SS}	GND
14	RxOIN2-	Negative LVDS differential data input (Odd), [B2-B5, HS, VS, DE]
15	RxOIN2+	Positive LVDS differential data input (Odd), [B2-B5, HS, VS, DE]
16	V_{SS}	GND
17	RxOCLKIN-	Negative LVDS differential clock input (Odd)
18	RxOCLKIN+	Positive LVDS differential clock input (Odd)
19	V_{SS}	GND
20	RxEIN0-	Negative LVDS differential data input (Even), [R0-R5, G0]
21	RxEIN0+	Positive LVDS differential data input (Even), [R0-R5, G0]
22	V_{SS}	GND
23	RxEIN1-	Negative LVDS differential data input (Even), [G1-G5, B0-B1]
24	RxEIN1+	Positive LVDS differential data input (Even), [G1-G5, B0-B1]
25	V_{SS}	GND
26	RxEIN2-	Negative LVDS differential data input (Even), [B2-B5, HS, VS, DE]
27	RxEIN2+	Positive LVDS differential data input (Even), [B2-B5, HS, VS, DE]
28	V_{SS}	GND
29	RxECLKIN-	Negative LVDS differential clock input (Even)
30	RxECLKIN+	Positive LVDS differential clock input (Even)

CN2 CCFL POWER SOURCE

Connector : BHSR-02VS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

Mating Connector : SM02B-BHS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

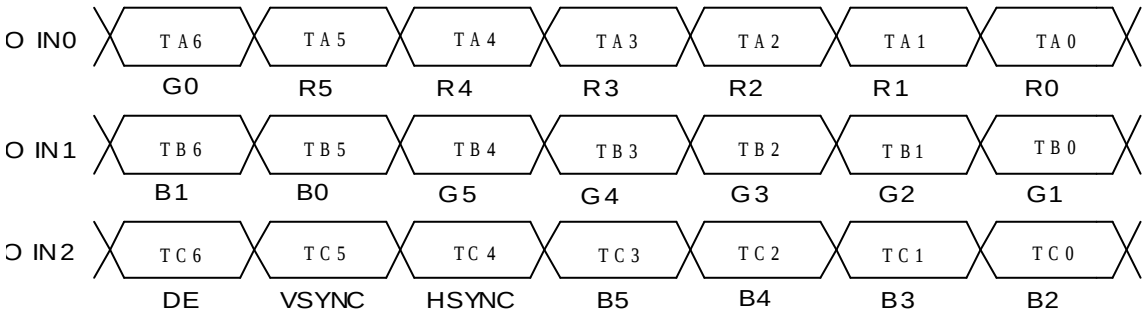
Terminal No.	Symbol	Function
1	V_{FLH}	CCFL Power Supply (high voltage)
2	V_{FLL}	CCFL Power Supply (low voltage)

RECOMMENDED TRANSMITTER(THC63LVDM63A,THC63LVDM63A-85) TO LTD141EM4S INTERFACE ASSIGNMENT

Case1: 6bit Transmitter

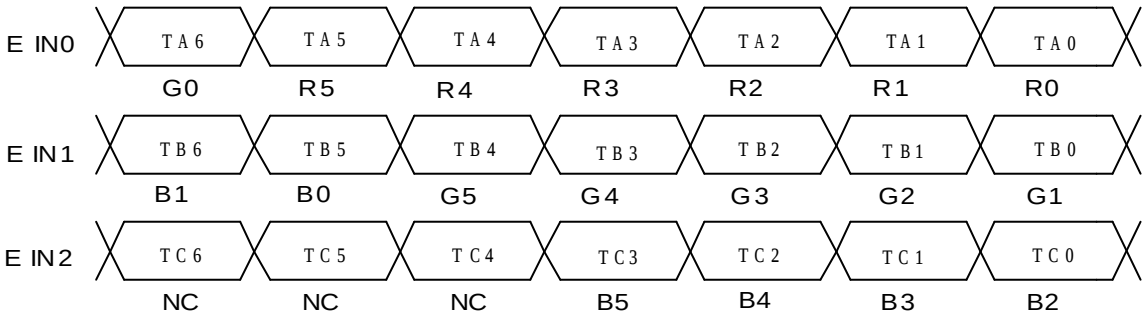
ODD DATA

Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	To LTD141EM4S Interface(CN1)	
Symbol		Symbol	Function		Terminal	Symbol
TA0	44	R0	Red Pixels Display Data (LSB)	TA- TA+	No.8 No.9	OIN0- OIN0+
TA1	45	R1	Red Pixels Display Data			
TA2	47	R2	Red Pixels Display Data			
TA3	48	R3	Red Pixels Display Data			
TA4	1	R4	Red Pixels Display Data			
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data (LSB)			
TB0	6	G1	Green Pixels Display Data	TB- TB+	No.11 No.12	OIN1- OIN1+
TB1	7	G2	Green Pixels Display Data			
TB2	9	G3	Green Pixels Display Data			
TB3	10	G4	Green Pixels Display Data			
TB4	12	G5	Green Pixels Display Data (MSB)			
TB5	13	B0	Blue Pixels Display Data (LSB)			
TB6	15	B1	Blue Pixels Display Data			
TC0	16	B2	Blue Pixels Display Data	TC- TC+	No.14 No.15	OIN2- OIN2+
TC1	18	B3	Blue Pixels Display Data			
TC2	19	B4	Blue Pixels Display Data			
TC3	20	B5	Blue Pixels Display Data (MSB)			
TC4	22	HSYNC	H-SYNC			
TC5	23	VSYNC	V-SYNC			
TC6	25	DE	Compound Synchronization Signal			
CLK IN	26	NCLK	Data Sampling Clock	TCLK- TCLK+	No.17 No.18	OCLK- OCLK+



EVEN DATA

Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	To LTD141EM4S Interface(CN1)	
Symbol		Symbol	Function		Terminal	Symbol
TA0	44	R0	Red Pixels Display Data (LSB)	TA- TA+	No.20 No.21	EIN0- EIN0+
TA1	45	R1	Red Pixels Display Data			
TA2	47	R2	Red Pixels Display Data			
TA3	48	R3	Red Pixels Display Data			
TA4	1	R4	Red Pixels Display Data			
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data (LSB)			
TB0	6	G1	Green Pixels Display Data	TB- TB+	No.23 No.24	EIN1- EIN1+
TB1	7	G2	Green Pixels Display Data			
TB2	9	G3	Green Pixels Display Data			
TB3	10	G4	Green Pixels Display Data			
TB4	12	G5	Green Pixels Display Data (MSB)			
TB5	13	B0	Blue Pixels Display Data (LSB)			
TB6	15	B1	Blue Pixels Display Data			
TC0	16	B2	Blue Pixels Display Data	TC- TC+	No.26 No.27	EIN2- EIN2+
TC1	18	B3	Blue Pixels Display Data			
TC2	19	B4	Blue Pixels Display Data			
TC3	20	B5	Blue Pixels Display Data (MSB)			
TC4	22	NC	Non Connection (open)			
TC5	23	NC	Non Connection (open)			
TC6	25	NC	Non Connection (open)			
CLK IN	26	NCLK	Data Sampling Clock	TCLK- TCLK+	No.29 No.30	ECLK- ECLK+

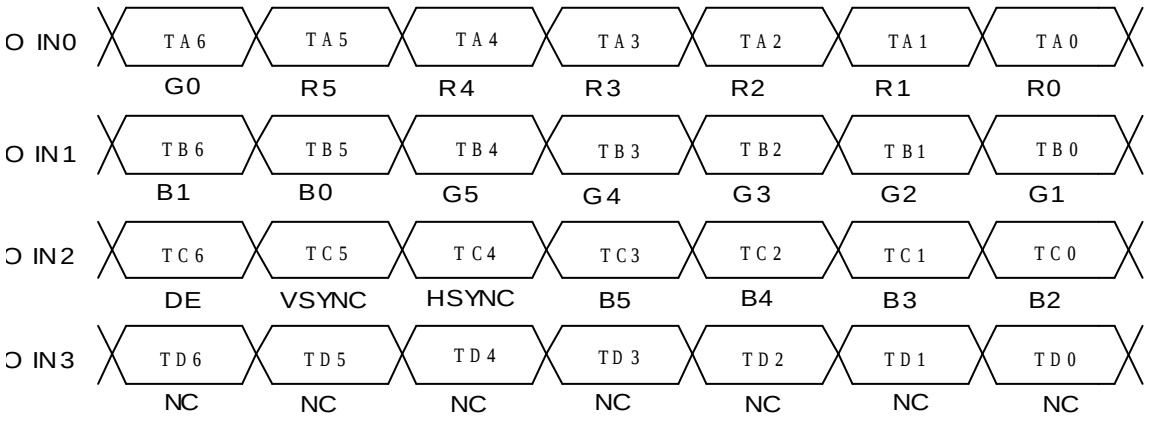


RECOMMENDED TRANSMITTER(THC63LVDM83A,THC63LVDM83A-85) TO LTD141EM4S INTERFACE ASSIGNMENT

Case2: 8bit Transmitter

ODD DATA

Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	To LTD141EM4S Interface(CN1)	
Symbol		Symbol	Function		Terminal	Symbol
TA0	51	R0	Red Pixels Display Data (LSB)	TA- TA+	No.8 No.9	OIN0- OIN0+
TA1	52	R1	Red Pixels Display Data			
TA2	54	R2	Red Pixels Display Data			
TA3	55	R3	Red Pixels Display Data			
TA4	56	R4	Red Pixels Display Data			
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data(LSB)			
TB0	6	G1	Green Pixels Display Data	TB- TB+	No.11 No.12	OIN1- OIN1+
TB1	7	G2	Green Pixels Display Data			
TB2	11	G3	Green Pixels Display Data			
TB3	12	G4	Green Pixels Display Data			
TB4	14	G5	Green Pixels Display Data(MSB)			
TB5	15	B0	Blue Pixels Display Data (LSB)			
TB6	19	B1	Blue Pixels Display Data			
TC0	20	B2	Blue Pixels Display Data	TC- TC+	No.14 No.15	OIN2- OIN2+
TC1	22	B3	Blue Pixels Display Data			
TC2	23	B4	Blue Pixels Display Data			
TC3	24	B5	Blue Pixels Display Data (MSB)			
TC4	27	HSYNC	H-SYNC			
TC5	28	VSYNC	V-SYNC			
TC6	30	DE	Compound Synchronization Signal			
TD0	50	NC	Non Connection (open)	TD- TD+	-	-
TD1	2	NC	Non Connection (open)			
TD2	8	NC	Non Connection (open)			
TD3	10	NC	Non Connection (open)			
TD4	16	NC	Non Connection (open)			
TD5	18	NC	Non Connection (open)			
TD6	25	NC	Non Connection (open)			
CLK IN	31	NCLK	Data Sampling Clock	TCLK- TCLK+	No.17 No.18	OCLK- OCLK+



EVEN DATA

Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	To LTD141EM4S Interface(CN1)	
Symbol		Symbol	Function		Terminal	Symbol
TA0	51	R0	Red Pixels Display Data (LSB)	TA- TA+	No.20 No.21	EIN0- EIN0+
TA1	52	R1	Red Pixels Display Data			
TA2	54	R2	Red Pixels Display Data			
TA3	55	R3	Red Pixels Display Data			
TA4	56	R4	Red Pixels Display Data			
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data(LSB)			
TB0	6	G1	Green Pixels Display Data	TB- TB+	No.23 No.24	EIN1- EIN1+
TB1	7	G2	Green Pixels Display Data			
TB2	11	G3	Green Pixels Display Data			
TB3	12	G4	Green Pixels Display Data			
TB4	14	G5	Green Pixels Display Data(MSB)			
TB5	15	B0	Blue Pixels Display Data (LSB)			
TB6	19	B1	Blue Pixels Display Data			
TC0	20	B2	Blue Pixels Display Data	TC- TC+	No.26 No.27	EIN2- EIN2+
TC1	22	B3	Blue Pixels Display Data			
TC2	23	B4	Blue Pixels Display Data			
TC3	24	B5	Blue Pixels Display Data (MSB)			
TC4	27	NC	Non Connection (open)			
TC5	28	NC	Non Connection (open)			
TC6	30	NC	Non Connection (open)			
TD0	50	NC	Non Connection (open)	TD- TD+	-	-
TD1	2	NC	Non Connection (open)			
TD2	8	NC	Non Connection (open)			
TD3	10	NC	Non Connection (open)			
TD4	16	NC	Non Connection (open)			
TD5	18	NC	Non Connection (open)			
TD6	25	NC	Non Connection (open)			
CLK IN	31	NCLK	Data Sampling Clock	TCLK- TCLK+	No.29 No.30	ECLK- ECLK+

256k (k=1024) COLORS COMBINATION TABLE

	Display	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0	Gray Scale Level
Basic Color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	-
	Green	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	-
	Light Blue	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	-
	Red	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	H	H	H	H	H	H	L	L	L	L	L	L	H	H	H	H	H	H	-
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	-
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
Gray Scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L 1
		L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L 2
					:						:						:			L3…
					:						:						:			L60
		H	H	H	H	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L61
		H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L62
	Red	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	Red L63
Gray Scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L 1
		L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L 2
					:						:						:			L3…
					:						:						:			L60
		L	L	L	L	L	L	H	H	H	H	L	H	L	L	L	L	L	L	L61
		L	L	L	L	L	L	H	H	H	H	H	L	L	L	L	L	L	L	L62
	Green	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	Green L63
Gray Scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L 1
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L 2
					:						:						:			L3…
					:						:						:			L60
		L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	L	H	L61
		L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	L	L62
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	Blue L63
Gray Scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	H	L	L	L	L	L	H	L	L	L	L	L	H	L 1
		L	L	L	L	H	L	L	L	L	L	H	L	L	L	L	L	H	L	L 2
					:						:						:			L3…
					:						:						:			L60
		H	H	H	H	L	H	H	H	H	H	L	H	H	H	H	H	L	H	L61
		H	H	H	H	H	L	H	H	H	H	H	L	H	H	H	H	H	L	L62
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	White L63

FOR SAFETY

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.
In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA LCD MODULES".
Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

1) SPECIAL PURPOSES

A) Toshiba Matsushita Display Technology's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.

B) Since Toshiba Matsushita Display Technology's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display Technology's published specification limits.

C) In addition, since Toshiba Matsushita Display Technology Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.
Toshiba Matsushita Display Technology doses not warrant the module, if customer disassembled or modified it.

3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.
If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.
If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.
DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.
Employ protection circuit for power supply, whenever the specification or TD specifies it.
Suitable protection circuit should be applied for each system design.

7) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.