Product Specification

M215HAN01.3

AU OPTRONICS CORPORATION

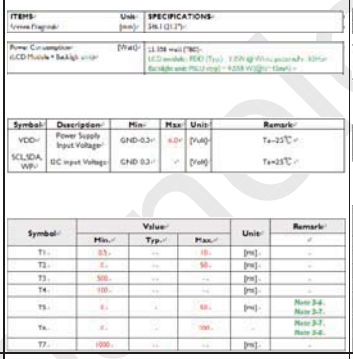
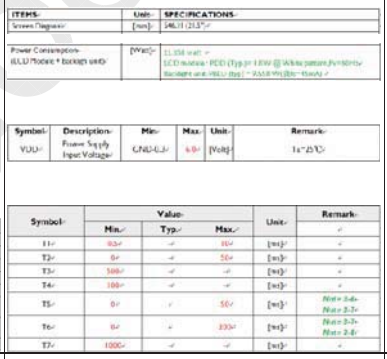
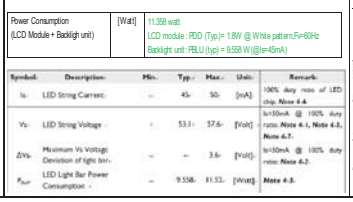
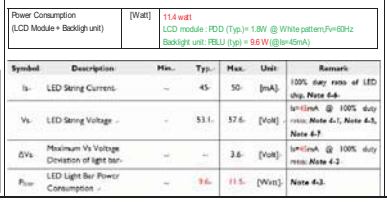
() Preliminary Specification
(V) Final Specification

Module	21.5" Color TFT-LCD
Model Name	M215HAN01.3

Customer	Date	Approved by	Date
		Howard Lee	April 19 2018
Approved by		Prepared by	Date
		Chiyinwu	April 19 2018
Note: This Specification is subject to change without notice.		Qisda corporation	

Contents

Record of Revision

Version	Date	Page	Old description (文字敘述 or 貼圖)	New Description (文字敘述 or 貼圖)	Remark
0.0	2017/11/08	-		Frist Draft (Preliminary)	
0.1	2017/12/19	P5	Power Consumption 13.1 watt	Power Consumption 11.858 watt	
0.1	2017/12/19	P26	LED String Current 50(typ)/55(max)	LED String Current 45(typ)/50(max)	
0.1	2017/12/19	P31		Packing Specification	
0.2	2017/12/29	P33		Modify 2D Drawing	
0.3	2018/01/17	P14		Modify Recommended Operating Condition	
0.3	2018/01/17	P32		Modify 2D Drawing	
04	2018/02/01	P10		ADD Mechanical Characteristics	
04	2018/02/01	P33		Modify 2D Drawing	
05	2018/02/08	P5 P14 P21			
06	2018/04/02	P28 P31	ESD (Electro Static Discharge): Contact Discharge: ± 15KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point. Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point. TY.4B230.02S BAG ANTI-STATIC M215HTN01 HDPE 1 4K.39E07.001 MYLAR FILM M270DTN01.5QB 2 TY.4D230.01R CARTON T215HVN05.1 0.08333 TY.4G230.0NQ CUSHION PACKING T215HVN05.1 0.08333	ESD (Electro Static Discharge): Contact Discharge: ± 8KV, 150pF(330Ω) 1sec 8 points, 25 times/ point. Air Discharge: ± 8KV, 150pF(330Ω) 1sec 8 points, 25 times/ point. TY.4B230.02S BAG ANTI-STATIC M215HTN01 HDPE 1 4K.39E07.001 MYLAR FILM M270DTN01.5QB 2 4D.42701.001 CARTON PACKING Q215HAE00-A000 0.08333 4G.42704.001 CUSHION PACKING Q215HAE00-A000 0.08333	
07	2018/04/11	P21 P32		add note 3-9 2D Drawing Add Note.	
08	2018/04/19	P5 P26			

1 Handling Precautions

1) Since polarizer is easily damaged, do not touch or press the surface of polorizer with hand.
2) Be sure to turn off power supply when inserting or disconnecting from input connector.
3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
7) Do not open or modify the Module Assembly.
8) Do not press the reflector sheet at the back of the module to any directions.
9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.
10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.
11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.
12) Please avoid touching COF position while you are doing mechanical design.
13) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5□ and 35□ at normal humidity.
14) Do not apply the same pattern for a long time, it will enhance relevant defect.

2 General Description

This specification applies to the 21.5 inch wide Color a-Si TFT-LCD Module M215HAN01.3. The display supports the Full HD - 1920(H) x 1080(V) screen format and 16.7M colors (RGB 6-bits+Hi-FRC). The input interface is Dual channel LVDS and this module doesn't contain an driver board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25□ condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	546.21 (21.5")
Active Area	[mm]	476.064 (H) x 267.786 (V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	247.95 (per one triad) ×247.95
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	AHVA Mode, (Advanced Hyper-Viewing Angle) Normally Black
White Luminance (Center)	[cd/m^2]	250 (Typ.)
Contrast Ratio	-	1000(Typ.)
Response Time	[msec]	14 (Typ., GTG)
Power Consumption (LCD Module + Backligh unit)	[Watt]	11.4 watt LCD module : PDD (Typ.)= 1.8W @ White pattern,Fv=60Hz Backlight unit: PBLU (typ) = 9.6 W(@Is=45mA)
Weight	[Grams]	1800(Typ)
Outline Dimension	[mm]	484.46(H) × 284.49(V) × 11.1(D) Typ.
Electrical Interface	-	Dual channel LVDS , 8 bits RGB data input
Support Color	-	16.7M colors (RGB 6-bits+Hi-FRC)
Surface Treatment	-	Anti-Glare , 3H
Temperature Range Operating Storage (Shipping)	 [°C] [°C]	 0 to +50 -20 to +60
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 7.0 Compliance

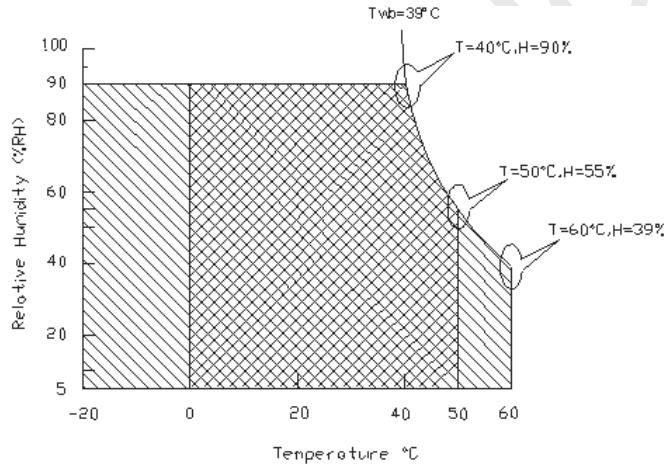
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	***Note 2-1***
TGS	Glass surface temperature (operation)	0	+65	[°C]	***Note 2-1*** *Function judged only*
HOP	Operation Humidity	5	90	[%RH]	***Note 2-1***
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

1. 90% RH Max (Ta ≦39℃)
2. Max wet-bulb temperature at 39℃ or less. (Ta ≦39℃)
3. No condensation



Operating Range Storage Range +

2.3 Optical Characteristics

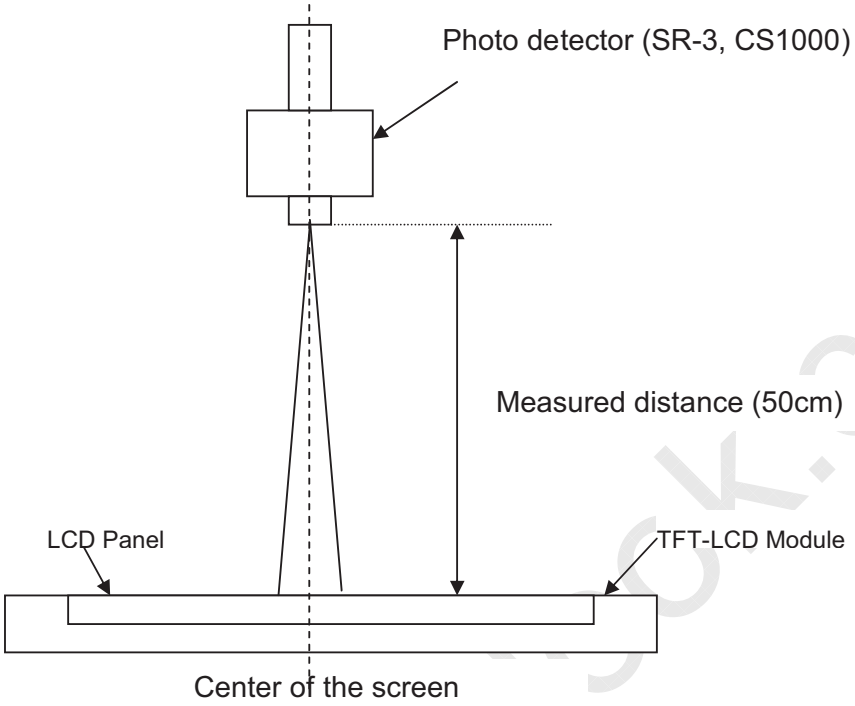
The optical characteristics are measured on the following test condition.

Test Condition:

1. Equipment setup: Please refer to ***Note 2-2***.
2. Panel Lighting time: 30 minutes
3. VDD=5.0V, Fv=60Hz,Is=45mA,Ta=25□
4. L/B must be in the bottom side at normal and MNT aging state

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
L_W	White Luminance (Center of screen)		200	250	-	[cd/m2]	***Note 2-2***
L_{Uni}	Luminance Uniformity (9 points)		75	80	-	[%]	***Note 2-3***
CR	Contrast Ratio (Center of screen)		600	1000	-	-	***Note 2-4***
θ_R	Horizontal Viewing Angle (CR=10)	Right	80	89	-	[degree]	***Note 2-5***
θ_L		Left	80	89	-		
Φ_H	Vertical Viewing Angle (CR=10)	Up	80	89	-		
Φ_L		Down	80	89	-		
θ_R	Horizontal Viewing Angle (CR=5	Right	80	89			
θ_L		Left	80	89			
Φ_H	Vertical Viewing Angle (CR=5)	Up	80	89			
Φ_L		Down	80	89			
T_{GTG}	Response Time	Gray To Gray		14		[msec]	***Note 2-6***
R_x	Color Coordinates (CIE 1931)	Red x	0.617	0.647	0.677	-	
R_y		Red y	0.304	0.334	0.364		
G_x		Green x	0.290	0.320	0.350		
G_y		Green y	0.595	0.625	0.655		
B_x		Blue x	0.125	0.155	0.185		
B_y		Blue y	0.02	0.050	0.080		
W_x		White x	0.283	0.313	0.343		
W_y		White y	0.299	0.329	0.359		
NTSC ratio				72		[%]	

Note 2-2: Equipment setup :

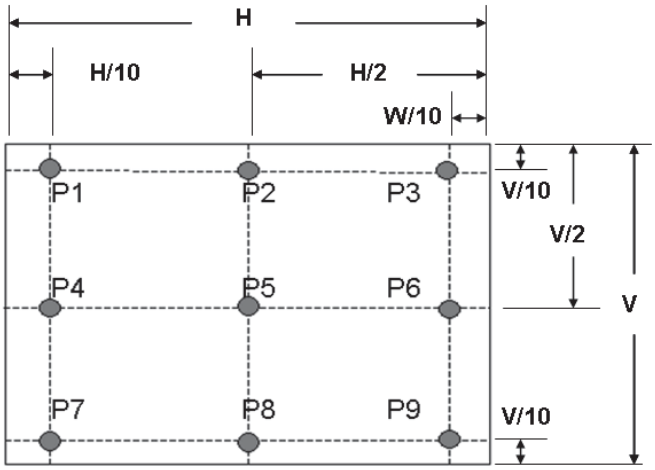


Note 2-3: Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 Points (P1 ~ P9)}}{\text{Maximum Luminance of 9 Points (P1 ~ P9)}}$$

a.Test pattern: White Pattern



Note 2-4: Contrast Ratio Measurement

Definition:

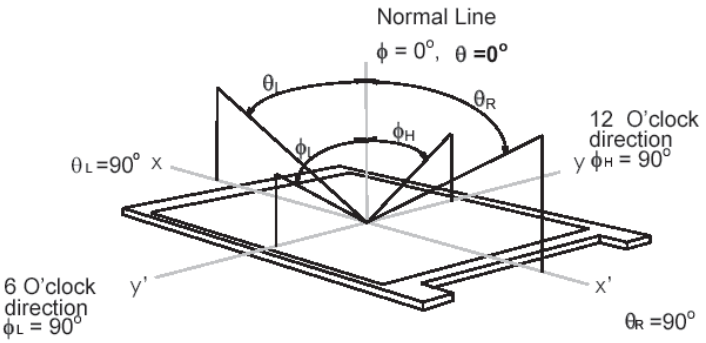
$$\text{Contrast Ratio} = \frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$$

a. Measured position: Center of screen (P5) & perpendicular to the screen (θ=Φ=0°)

Note 2-5: Viewing angle measurement

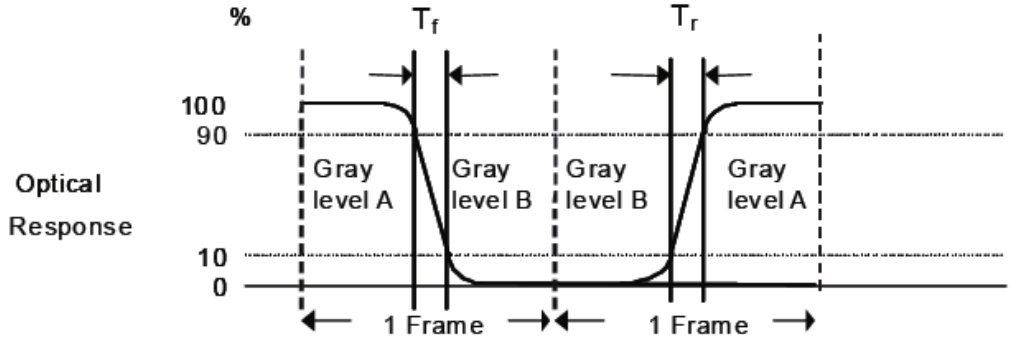
Definition: The angle at which the contrast ratio is greater than 10 & 5 .

a. Horizontal view angle: Divide to left & right (θ_L & θ_R)
Vertical view angle: Divide to up & down (Φ_H &Φ_L)



Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Gray level A" to "Gray level B" (falling time, TF), and from "Gray level B" to "Gray level A" (rising time, TR), respectively. The response time is interval between the 10% and 90% of optical response.



The gray to gray response time is defined as the following table.

Gray Level to Gray Level	Target gray level

		L0	L63	L127	L191	L255
Start gray level	L0					
	L63					
	L127					
	L191					
	L255					

T_{GTG_typ} is the total average time at rising time and falling time of gray to gray

2.3 Mechanical Characteristics

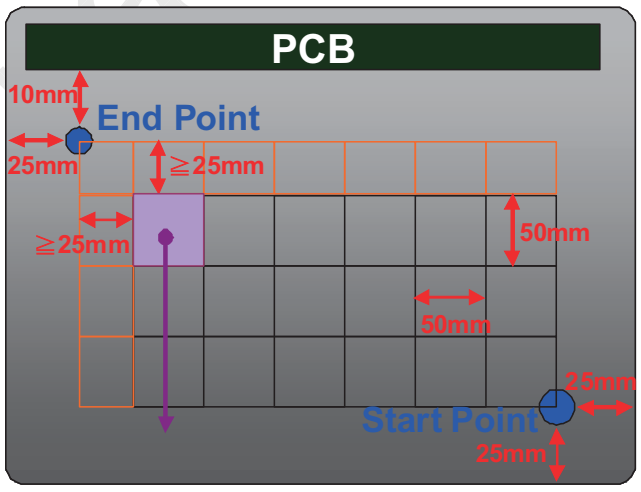
Symbol	Description	Min.	Max.	Unit	Remark
P_{bc}	Backside Compression	2.5	-	[Kgf]	***Note 2-7***

Note 2-7: Test Method:

The point is at a distance from right-downside 25mm x 25mm defined as the Start Point of Measure Points, and the point is at a distance 25mm from left-side & around 10mm from PCB defined as the End Point.

Align 50mm x 50mm block from Start Point on the Bezel Back, and the corners of each block are Measure Points.

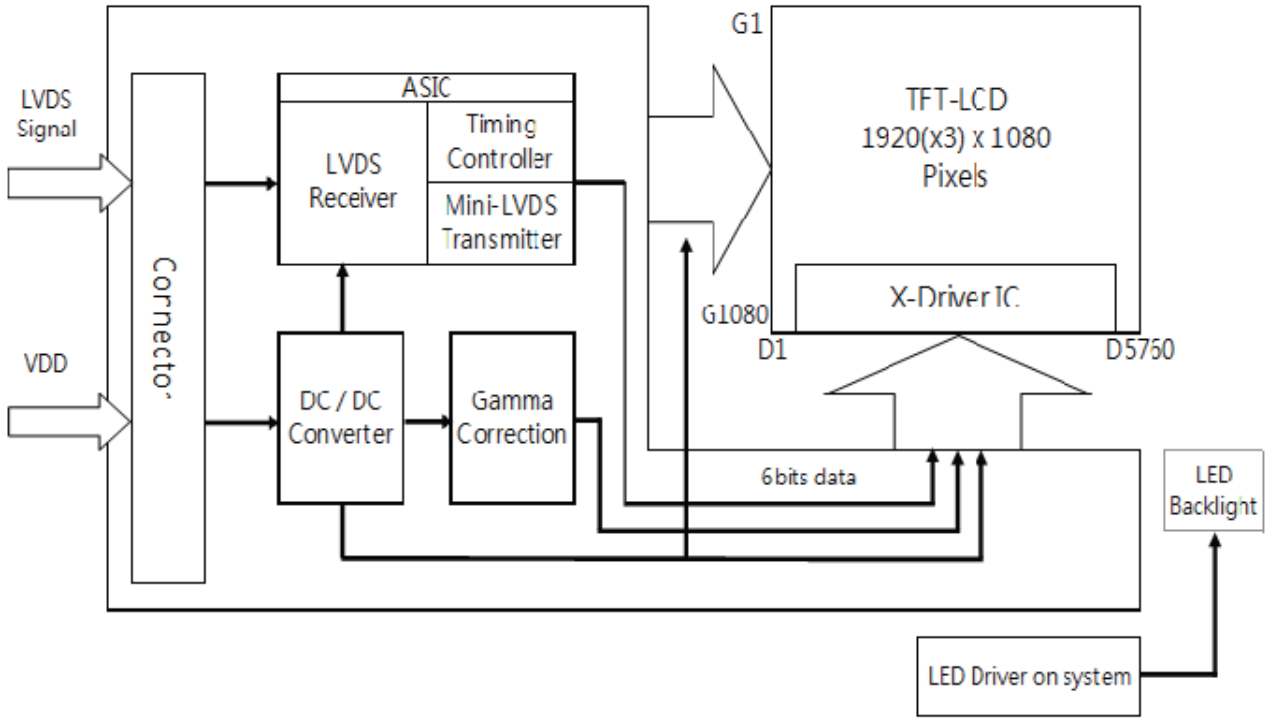
If the distance from the last block to each side of the End Point ≧ 25mm, add other blocks to make sure that most area of Bezel Back can be measured.



3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 21.5 inch Color TFT-LCD Module.



3.2 Interface Connection

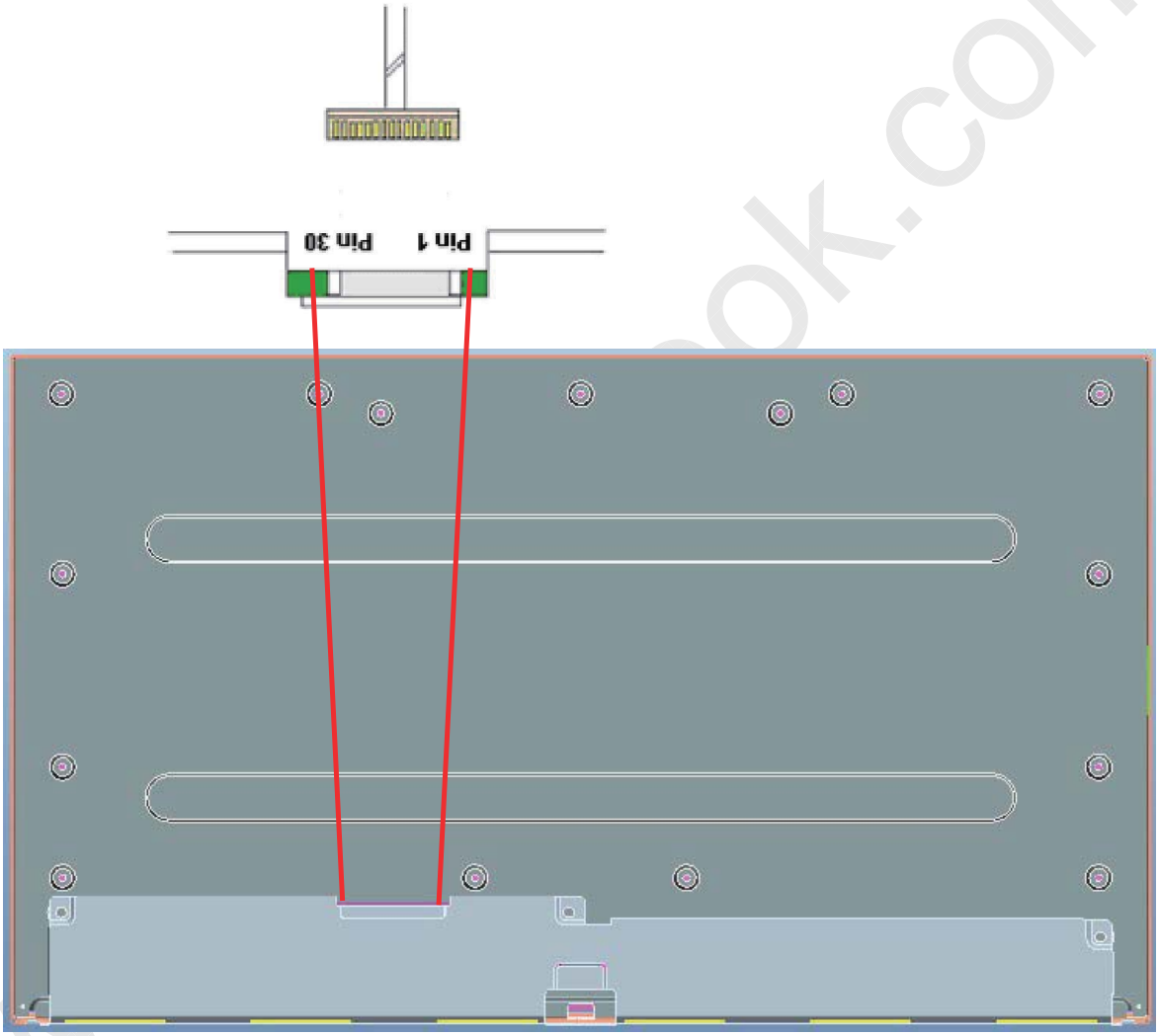
3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM
	Part Number	187034-3009	MSBKT2407P30HB
Mating Connector	Manufacturer	JAE or Compatible	
	Part Number	FI-X30HL (Locked Type)	

3.2.2 Connector Pin Assignment

PIN #	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	NC	No connection (for internal use only. Do not connect)	
25	NC	No connection (for internal use only. Do not connect)	
26	NC	No connection (for internal use only. Do not connect)	

27	NC	No connection (for internal use only. Do not connect)	
28	VDD	Power Supply Input +5.0 Voltage	
29	VDD	Power Supply Input +5.0 Voltage	
30	VDD	Power Supply Input +5.0 Voltage	



3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

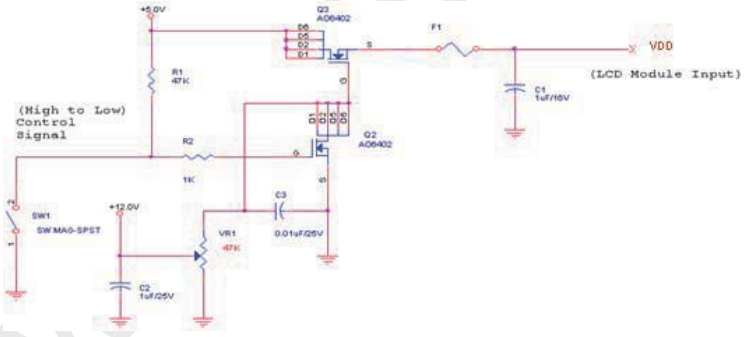
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25□

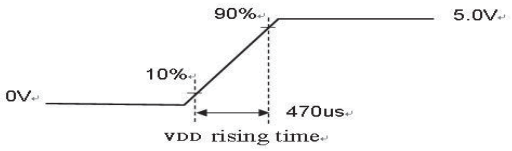
3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.36	0.95	[A]	VDD= 5.0V, White Pattern, Fv=60Hz
			0.4	1.13	[A]	VDD= 5.0V, White Pattern, Fv=75Hz
PDD	VDD Power Consumption	-	1.80	4.75	[Watt]	VDD= 5.0V, White Pattern, Fv=60Hz
			2.00	5.65	[Watt]	VDD= 5.0V, White Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	***Note 3-1***
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, White Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:

Test circuit:

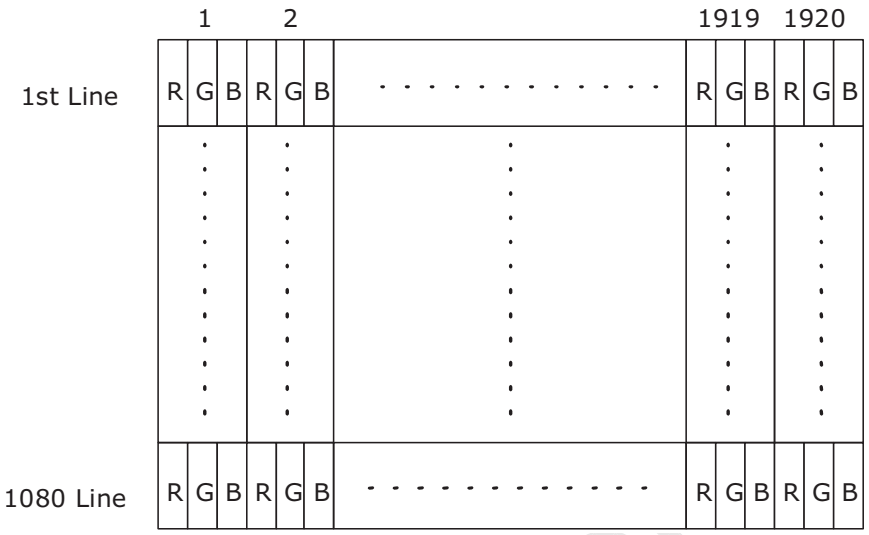




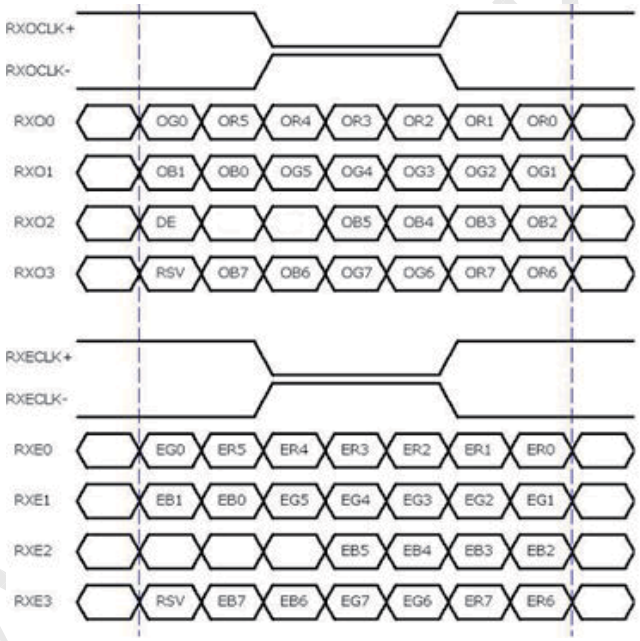
The duration of VDD rising time: 470us.

3.4 Signal Characteristics

3.4.1 LCD Pixel Format



3.4.2 LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 3-2:

a. O = "Odd Pixel Data" E = "Even Pixel Data"

b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB:R7, LSB:R0)								GREEN data (MSB:G7, LSB:G0)								BLUE data (MSB:B7, LSB:B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

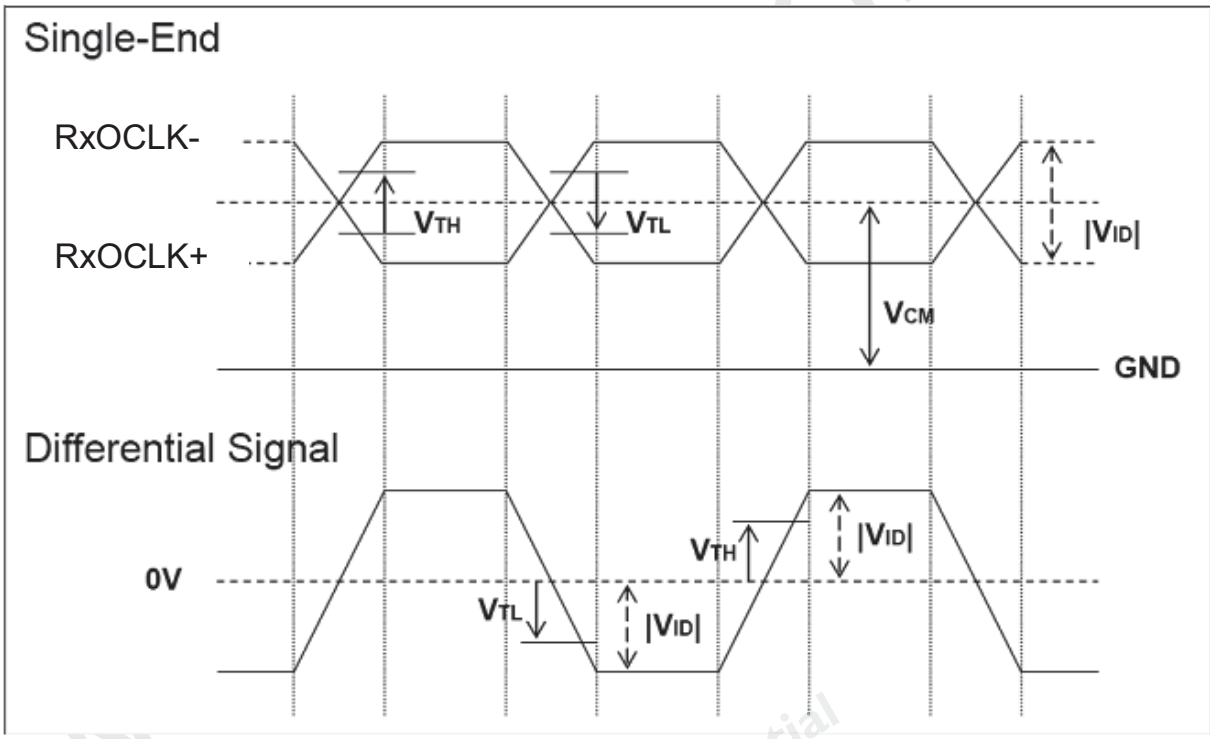
3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	-	-	+100	[mV]	V_{CM} = 1.2V
V_{TL}	LVDS Differential Input Low Threshold	-100	-	-	[mV]	V_{CM} = 1.2V
$\|V_{ID}\|$	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V_{TH}-V_{TL} = 200mV

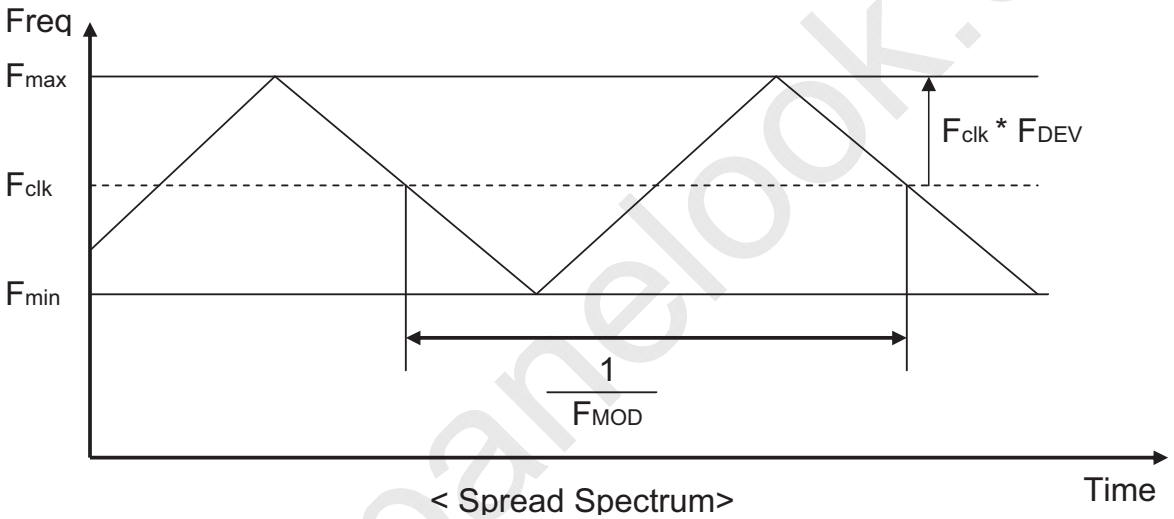
LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



< Spread Spectrum>

Fclk: LVDS Clock Frequency

3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1094	1130	1836	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	756	Th	
Fv		Frequency	47	60	77	Hz	***Note 3-3***
Th	Horizontal Section	Period	1000	1050	1678	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	40	90	718	Tclk	
Fh		Frequency	51.5	67.8	90.0	KHz	***Note 3-4***
Tclk	LVDS Clock	Period	11.2	14.0	19.4	ns	1/Fclk
Fclk		Frequency	51.5	71.2	90.0	MHz	***Note 3-5***

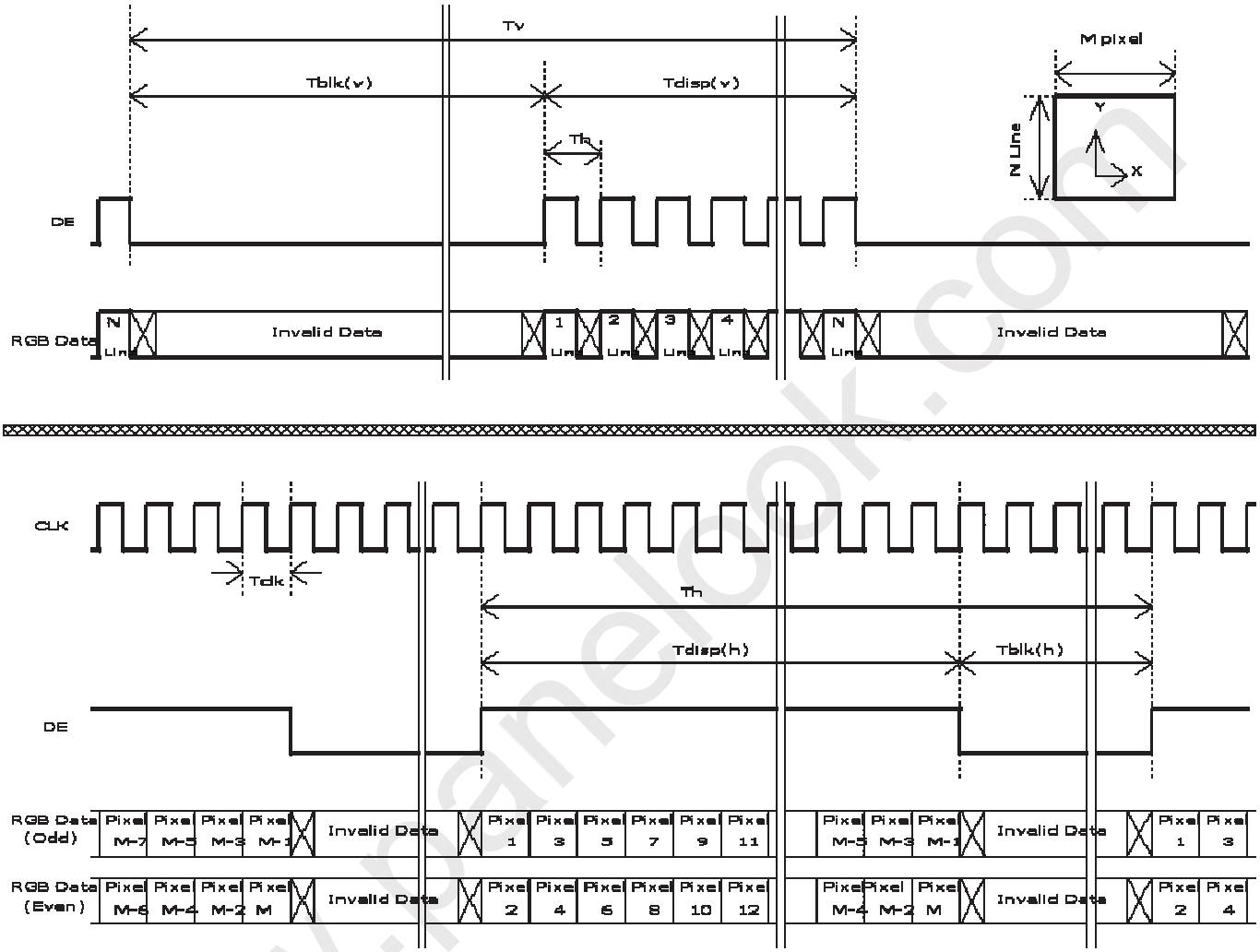
Note 3-3: The optimal Vertical Frequency is 50~76 Hz for best picture quality.
Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

Fh (Min.) = Fclk (Min.) / Th (Min.);
Fh (Typ.) = Fclk (Typ.) / Th (Typ.);
Fh (Max.)= Fclk (Max.) / Th (Min.);

Note 3-5: The equation is listed as following. Please don't exceed the above recommended value.

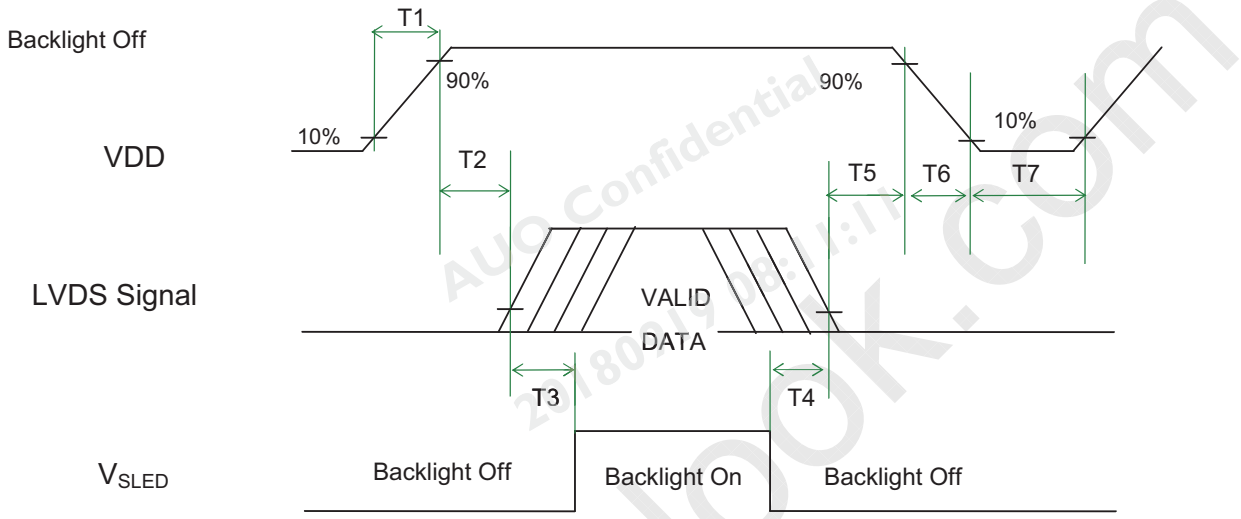
Fclk (Min.) = Fv (Min.) x Th (Min.) x Tv (Min.);
Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.);

3.4.6 Input Timing Diagram



3.5 Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	***Note 3-6*** ***Note 3-7***
T6	0		200	[ms]	***Note 3-7*** ***Note 3-8***
T7	1000	-	-	[ms]	

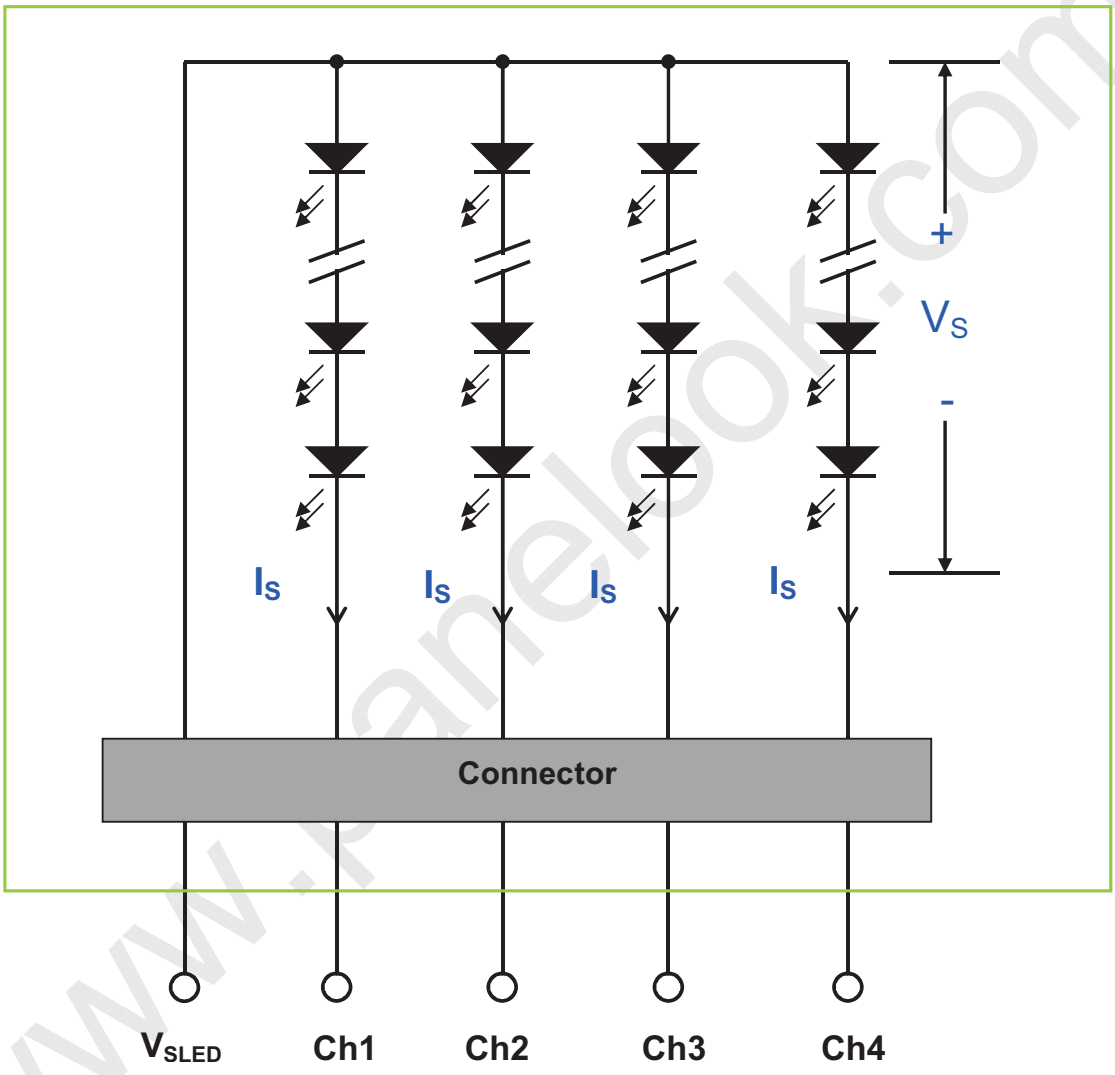
Note 3-6 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.
Note 3-7 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.
Note 3-8 : Voltage of VDD must decay smoothly after power-off.(customer system decide this value)

Note 3-9 : If T4 < 100ms, there will be no reliability concern, but the display may momentarily show abnormal screen

4 Backlight Unit

4.1 Block Diagram

The following shows the block diagram of the 21.5 inch Backlight Unit. And it includes 72 pcs LED in the LED light bar. (4 strings and 18 pcs LED of one string).



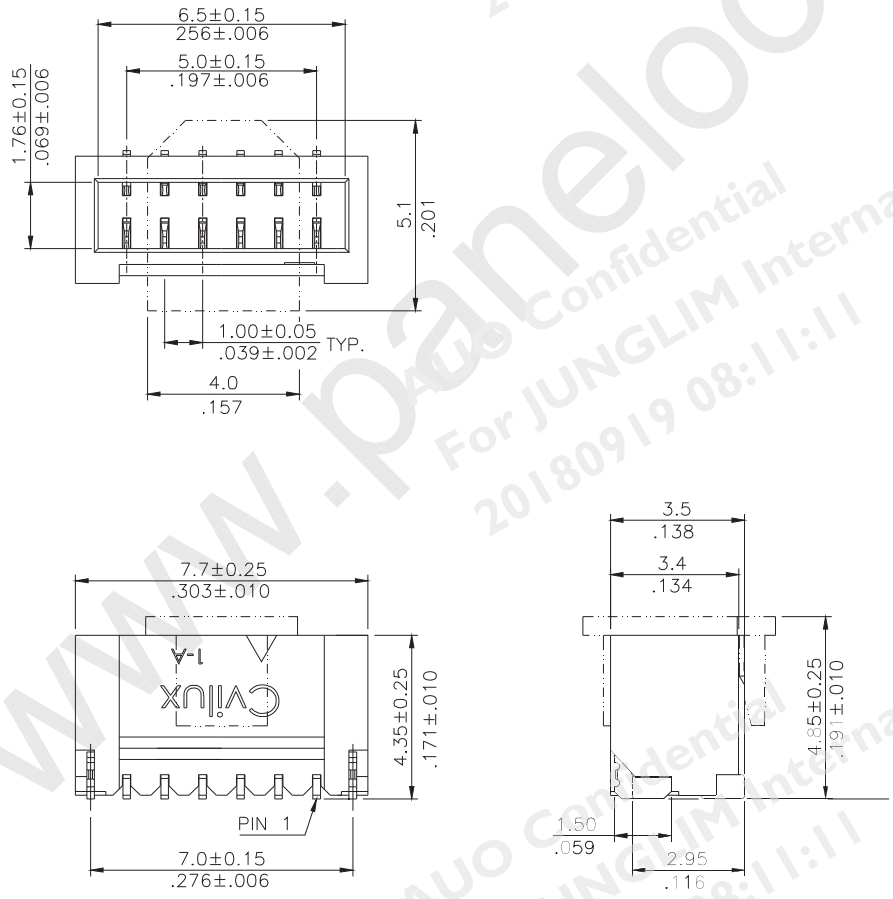
4.2 Interface Connection

4.2.1 Connector Type

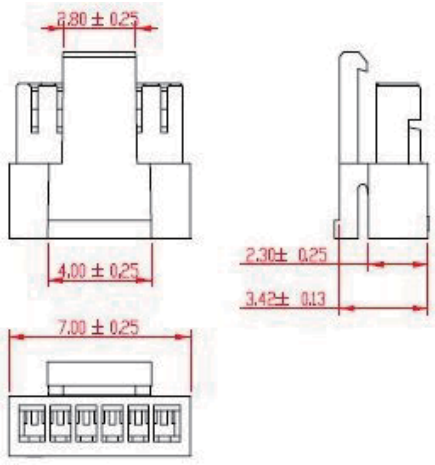
Backlight Connector	Manufacturer	CVILUX
	Part Number	CI1406M1VL0-NH
Mating Connector	Manufacturer	ENTERY
	Part Number	H112K-P06N-00B (Non-Locking type) H112K-P06N-11B(White) (Locking type) H112K-P06N-13B(Black) (Locking type)

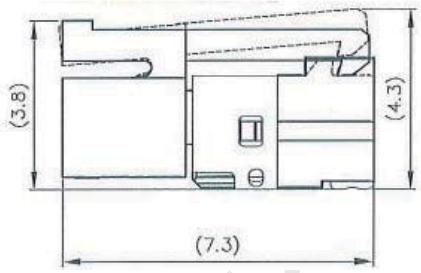
Backlight Connector dimension:

$H \times V \times D = 7.7 \times 3.5 \times 4.85, Pitch = 1.0(unit = mm)$



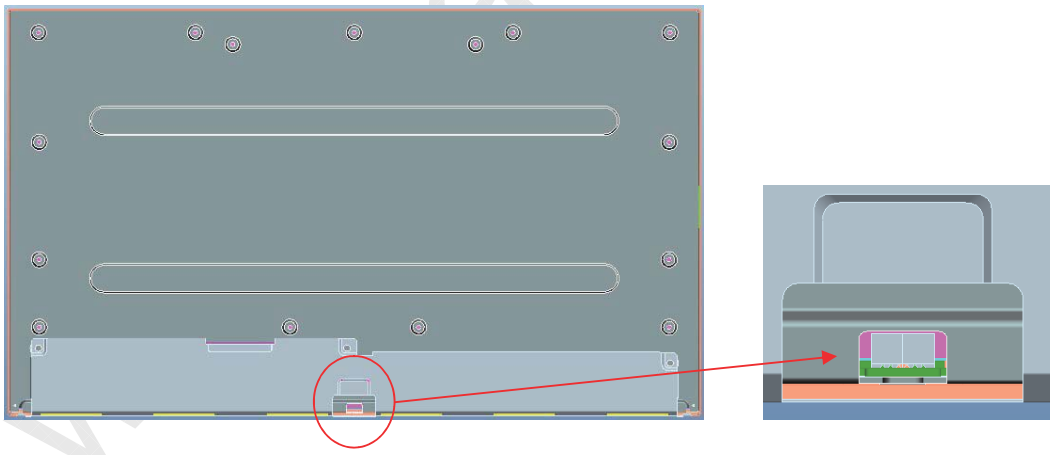
Mating Connector dimension:





4.2.2 Connector Pin Assignment

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	V_{SLED}	LED Power Supply Voltage Input Terminal	
4	V_{SLED}	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	



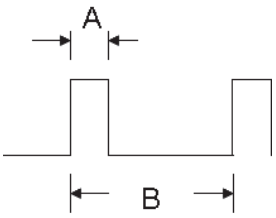
4.3 Electrical Characteristics

4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25□)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	120	[mA]	100% duty ratio
			140	[mA]	*Duty ratio□ 10%* *Pulse time=10 ms*



Duty ratio= (A / B) X 100% ; (A: Pulse time, B: Period)

4.3.2 Recommended Operating Condition

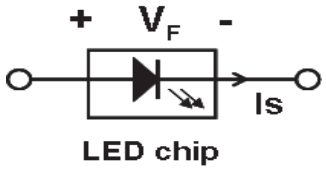
(Ta=25℃)

Symbol	Description	Min.	Typ.	Max.	Unit	Remark
Is	LED String Current	-	45	50	[mA]	100% duty ratio of LED chip, ***Note 4-6***
Vs	LED String Voltage		53.1	57.6	[Volt]	Is=45mA @ 100% duty ratio; ***Note 4-1, Note 4-5, Note 4-7***
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	3.6	[Volt]	Is=45mA @ 100% duty ratio; ***Note 4-2***
P_{BLU}	LED Light Bar Power Consumption	-	9.6	11.5	[Watt]	***Note 4-3***
LT_{LED}	LED Life Time	30,000	-	-	[Hour]	***Note 4-4***
OVP	Over Voltage Protection in system board	110% Vsmax	-	-	[Volt]	***Note 4-5***

Note 4-1: Vs (Typ.) = V_F (Typ.) X LED No. (one string);

a. V_F: LED chip forward voltage, V_F (Min.)=2.7V, V_F(Typ.)=2.95V, V_F(Max.)=3.2V

b. The same euqation to calculate Vs(Min.) & Vs (Max.) for respective V_F (Min.) & V_F(Max.);



Note 4-2: ΔVs (Max.) = ΔV_F X LED No. (one string);

a. ΔV_F: LED chip forward voltage deviation; (0.2 V , each Bin of LED V_F)

Note 4-3: P_{BLU} (Typ.) = Vs (Typ.) X Is (Typ.) X 4 ; (4 is total String No. of LED Light bar)

P_{BLU} (Max.) = Vs (Max.) X Is (Typ.) X 4 ;

Note 4-4: Definition of life time:

a. Brightness of LED becomes to 50% of its original value

b. Test condition: Is =45mA and 25□ (Room Temperature)

Note 4-5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. It's strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

Note 4-6: Recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

Note 4-7: Ensure that the LED light bar is not subjected either forward or reverse voltage while monitor set is on standby mode or not in use.

5 Reliability Test

Reliability test items are listed as following table. (*Bare Panel only*)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50□, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50□, 50%RH, 300hours	
Low Temperature Operation	Ta= 0□, 300hours	
High Temperature Storage (HTS)	Ta= 60□, 300hours	
Low Temperature Storage (LTS)	Ta= -20□, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Thermal Shock Test (TST)	-20□/30min, 60□/30min, 100 cycles	***Note 5-1***
ESD (Electro Static Discharge)	Contact Discharge: ± 8 KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	***Note 5-2***
	Air Discharge: ± 8 KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	

Note 5-1: a. A cycle of rapid temperature change consists of varying the temperature from -20□ to 60□, and back again. Power is not applied during the test.
b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed
No data lost
Self-recoverable
No hardware failures.

ESD discharged points should avoid display area and periphery front bezel of display area. Suggest points were 4 side parallel edge of display area surface.

Metal front bezel must cover half area of BM (black matrix), and metal front bezel must connect with metal back bezel to protect source IC of panel by ESD damaged.

Note 5-3 : Result Evaluation Criteria:

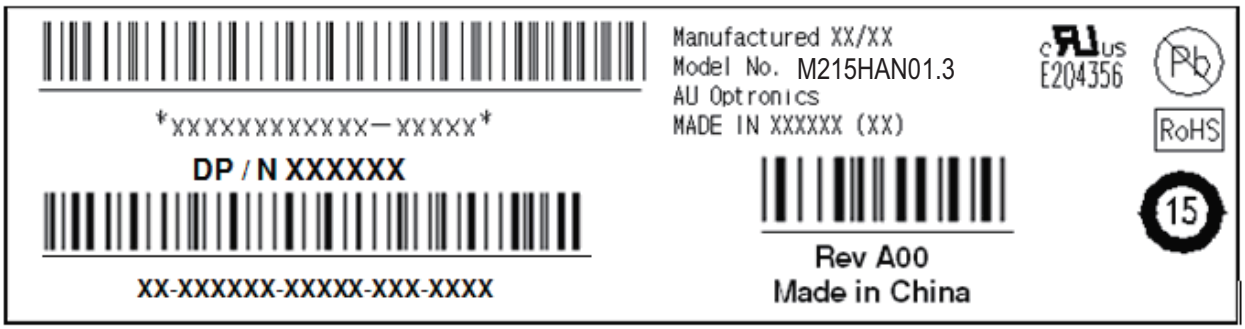
TFT-LCD panels test should take place after gradually cooling enough at room temperature

In the normal application, there should be no particular problems that may affect the

display function.

6 Shipping Label

The label is on the panel as shown below: (Example)



Note 6-1: For Pb Free products, we will add for identification.

Note 6-2: For RoHS compatible products, we will add for identification.

Note 6-3: For China RoHS compatible products, AUO will add for identification.

Note 6-4: The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

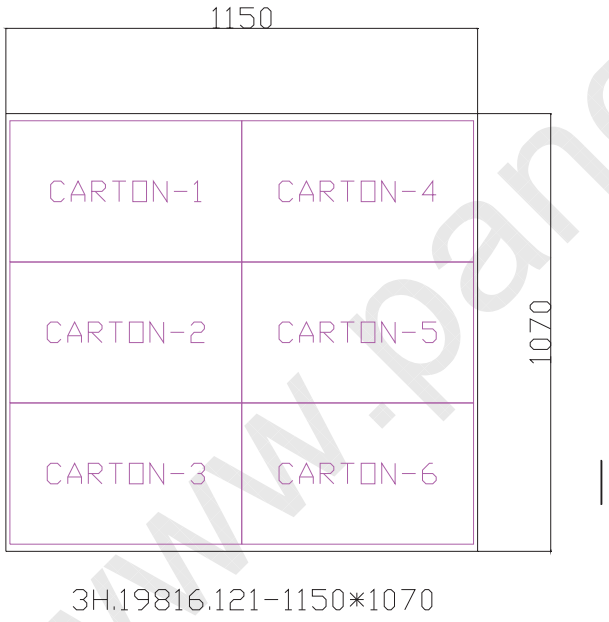
7 Packing Specification

7.1 Packing

Child Item	Description	Q'ty
TY.4B230.02S	BAG ANTI-STATIC M215HTN01 HDPE	1
4K.39E07.001	MYLAR FILM M270DTN01.5QB	2
4D.42701.001	CARTON PACKING Q215HAE00-A000	0.08333
4G.42704.001	CUSHION PACKING Q215HAE00-A000	0.08333

Model	Package Stack	Layer / Pallet
Q215HAE00_A000	6 cartons/layer，12 pcs/carton， 3 layers / pallet Total 216pcs	6 cartons/layer ; 3 layers/pallet ; Total 18 cartons

Layout :



8 Mechanical Characteristics