PRIME VIEW

PD104VT3

Version : *10.0*

TECHNICAL SPECIFICATION

MODEL NO. : PD104VT3

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Please contact PVI or its agent for further information.

☐Customer's Confirmation

Customer

Date

By

☐PVI's Confirmation

Confirmed By Tommy Chang

Prepared By 李穎爵

Revision History

Rev.	Issued Date	Revised Contents
1.0	Dec 11,2002	New
1.1	Mar. 18,2003	Modify Page 5 : Mechanical Drawing(change PCBA outline dimension) Page 6 : TFT-LCD Panel Driving (pin 30 must connect to ground) Page 7-3 : Input / Output signal timing chart (Back Porch form 49 to 48) Page 17 : Reliability test (High Temperature & High Humidity Operation Test from 60℃,95%RH to 60℃,90%RH)
1.2	Mar. 31,2003	Add Page 17 : 10.Handling Cautions Page 18 : 12. Indication of Lot Number Label
1.3	May. 28,2003	Add Page 7 : 7-1 Recommended Operating Conditions (Digital input voltage) Page 14 : 9. Optical Characteristics (Luminance uniformity from 50%(min.) to 70%(min.) Page 17 : 11. Reliability Test(Low Temperature Operation test from 0℃ to -10℃)
1.4	Aug. 26 ,2003	Modify Page 4 : 4.Mechanical Drawing of TFT-LCD Module (point the warranty label position) Page 9 : 7-4 Display Time Range(remove positive input signal) Page 18 : 11. Reliability Test(remove low temperature storage test) Page 18 12. Indication of Lot Number Label
1.5	Nov. 25 ,2003	Modify Page 4,5 : Mechanical Drawing Page 16 : Contrast Ratio (CR fromTyp.180,Min.100 to Typ.400,Min.200) Page 20 : Indication of Lot Number Label Page 8 : Electrical Characteristics a.Input/Output signal timing chart b.Display Time Range Add Page 6 : Support the DENB mode & Image Reversion : Up/Down and Left/Right
1.6	May. 18,2004	Modify Page 7 : Note 5-2 : DENB input signal
1.7	Sep. 14,2004	Modify Page 5 : Mechanical Drawing
1.8	Sep. 30,2004	Modify Page 3: 3.Mechanical Specifications Surface treatment Page 21 : Packing Diagram Page 9 : Note 7-3 Add Lamp kick –off time

Revision History

1.9	Jan. 17,2005	Del Page 19 : 12. Indication of Lot Number Label
2.0	Jan.13,2006	Add Page 6: Note5-5 Page 19: Low Temperature Storage Test -20℃ High Temperature Operation Test +60℃ Modify Page8: 7.Electrical Characteristics 7-1) Recommended Operating Conditions Starting Voltage (25℃) (Reference Value) from 1060 change to 1080 Vrms Starting Voltage (0℃) (Reference Value) from 1300 change to 1410 Vrms Page 19: Criteria
10.0	Aug 13, 2008	Add Page 20 11.Handling Cautions 11-1 item e)

TECHNICAL SPECIFICATION

CONTENTS

1. Application

This data sheet applies to a color TFT LCD module, PD104VT3.
PD104VT3 module applies to OA product, car TV(must use Analog to Digital drive board), which require high quality flat panel display. If you must use in high reliability environment can't over reliability test condition.
Prime View assume no responsibility for any damage resulting from the use of the device which dose not comply with the instructions and the precautions in these specification sheet.

2. Features

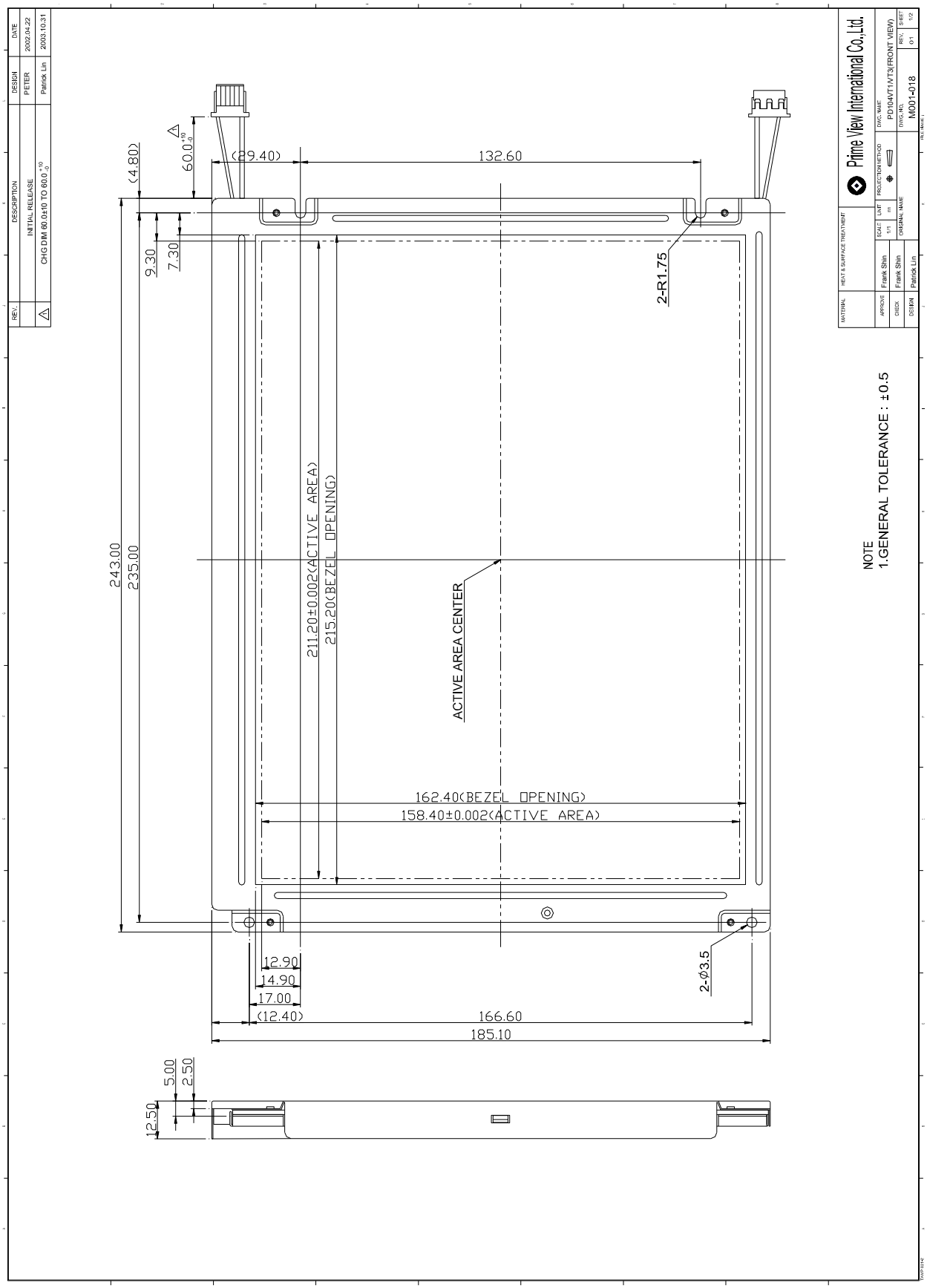
. Support the DENB mode
. Image Reversal : Up/Down and Left/Right
. Amorphous silicon TFT LCD panel with back-light unit
. Pixel in stripe configuration
. Slim and compact, designed for O/A application
. Display Colors：262,144 colors
. Optimum Viewing Direction：6 o'clock
. +3.3V DC supply voltage for TFT LCD panel driving
. Backlight driving DC/AC inverter not included in this module
. TTL transmission interface
. Wide viewing angle

3.Mechanical Specifications

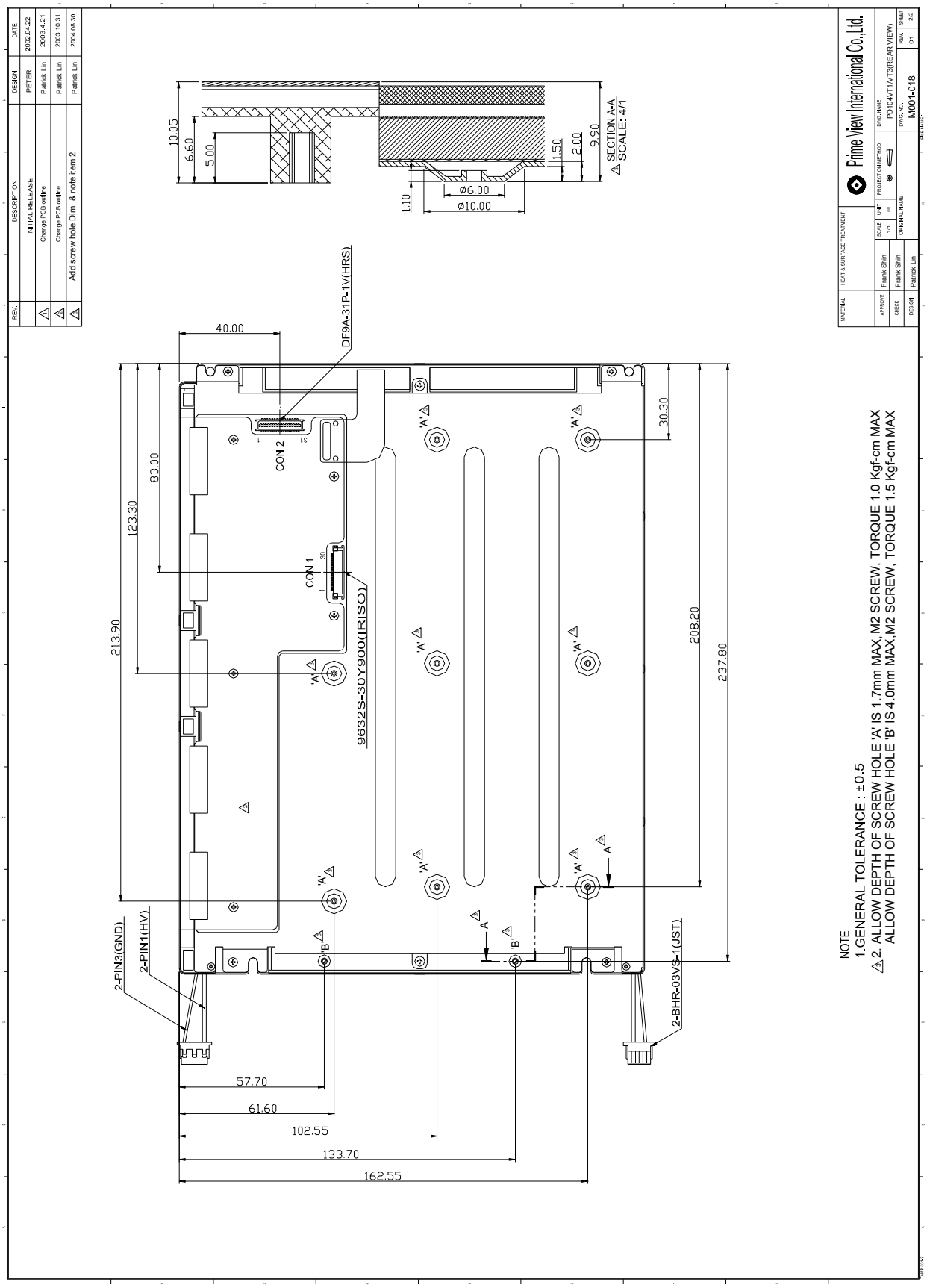
Parameter	Specifications	Unit
Screen Size	26.4(diagonal)	cm
	10.4 (diagonal)	inch
Display Format	640×(R, G, B)×480	dot
Display Colors	262,144	
Active Area	211.2(H)×158.4(V)	mm
Pixel Pitch	0.330(H)×0.330(V)	mm
Pixel Configuration	Stripe	
Outline Dimension	243.0(w)×185.1 (H)×12.5 (D)	mm
Weight	516±20	g
Back-light	CCFL, 2 tubes	
Surface treatment	Anti-glare and Wide View film	
Display mode	Normally white	
Gray scale inversion direction	6 o'clock [ref to Note 10-1]	-

4.Mechanical Drawing of TFT-LCD Module

Outline Drawing : Front View (unit mm)



Outline Drawing : Rear View (unit mm)



5.Input / Output Terminals

5-1) TFT-LCD Panel Driving

Connector type : IRISO , 9632S-30Y900 , PIN No.30 pin,pitch=0.5mm

Pin No.	Symbol	Function	Remark
1	CLK	Clock Signal for Sampling Image Digital Data	
2	Hsync	Horizontal Synchronous Signal	
3	Vsync	Vertical Synchronous Signal	
4	GND	Ground (0V)	
5	R0	Red Image Data Signal (LSB)	
6	R1	Red Image Data Signal	
7	R2	Red Image Data Signal	
8	R3	Red Image Data Signal	
9	R4	Red Image Data Signal	
10	R5	Red Image Data Signal (MSB)	
11	GND	Ground (0V)	
12	G0	Green Image Data Signal (LSB)	
13	G1	Green Image Data Signal	
14	G2	Green Image Data Signal	
15	G3	Green Image Data Signal	
16	G4	Green Image Data Signal	
17	G5	Green Image Data Signal (MSB)	
18	GND	Ground (0V)	
19	B0	Blue Image Data Signal (LSB)	
20	B1	Blue Image Data Signal	
21	B2	Blue Image Data Signal	
22	B3	Blue Image Data Signal	
23	B4	Blue Image Data Signal	
24	B5	Blue Image Data Signal (MSB)	
25	GND	Ground (0V)	
26	DENB	Data Enable Signal	Note 5-1 Note 5-2
27	VCC	DC +3.3V Power Supply	
28	VCC	DC +3.3V Power Supply	
29	R/L U/D	Horizontal Image Shift-direction Select Signal Vertical Image Shift-direction Select Signal	Note 5-3
30	GND	Ground (0V)	Note 5-4

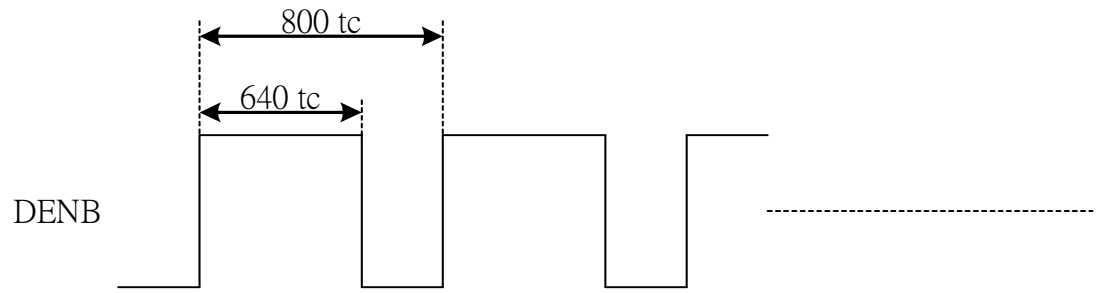
Note 5-1 : The relationship between DENB & SYNC. mode

1. DENB mode with the top priority.
2. When working with the SYNC. mode , The Hsync and Vsync determine the timings.
3. This pin must connect to ground, if without DENB.

Mode / SYNC.	DENB	VGA
Hsync Polarization	**Don't care**	**Negative**
Vsync Polarization	**Don't care**	**Negative**

Note 5-2 : DENB input signal.

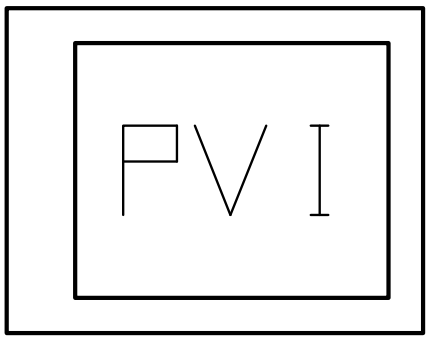
If customer wanted to off the DENB mode , you must keep the DENB (pin 26) always High or Low.



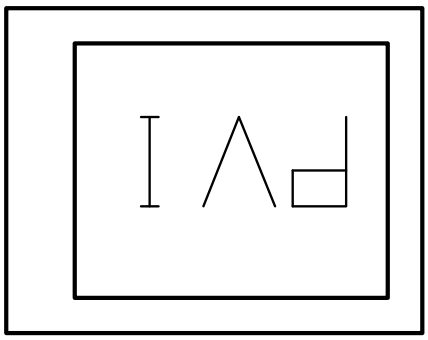
tc: the period of sample clock.

Note 5-3 : The definitions of U/D & R/L

U/D & R/L (PIN 29)= High



U/D & R/L (PIN 29)= Low



Note 5-4:This pin must connect to ground, if without grounding the panel can't turn on

5-2) Backlight driving

Connector type:BHR-03VS-1 (JST) , PIN No .3pin, pitch=4mm

Pin No	Symbol	Description	Remark
1	VL1	Input terminal (Hi voltage side)	Wire color : Pink
2	NC	No Connection	
3	VL2	Input terminal (Low voltage side)	Wire Color : White Note 5-5

Note 5-5 : Low voltage side of backlight inverter connects with ground of inverter circuits.

6. Absolute Maximum Ratings :

The followings are maximum values , which if exceeded, may cause faulty operation or damage to the unit.

GND=0V, Ta=25℃

Parameters	Symbol	MIN.	MAX.	Unit	Remark
Supply Voltage	Vcc	-0.3	+4.0	V	
Input Signal Voltage	V_{IN}	-0.3	Vcc+0.3	V	Note 6-1
Backlight Driving Voltage	V_L	-	2000	V	
Backlight Driving Frequency	F_L	0	100	KHz	

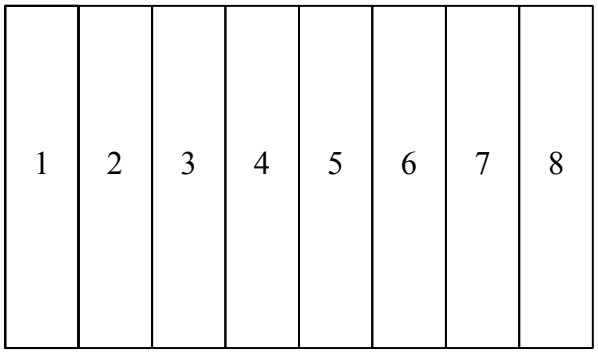
Note 6-1 : Input signals include CLK , Hsync , Vsync , DENB , R[0:5] , G[0:5] and B[0:5].

7.Electrical Characteristics

7-1) Recommended Operating Conditions: GND = 0V，Ta = 25℃

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Supply Voltage		Vcc	3.0	3.3	3.6	V	
Current Dissipation		I_{cc}	-	300	390	mA	Note 7-1
Digital input voltage	H level	V_{IN}	0.7 V_{CC}	-	V_{CC}	V	
	L level	V_{IL}	-0.1	-	0.1 V_{CC}	V	
Lamp Current		I_{FL}	3.0	6.0	8.0	mA	Per CCFL Note 7-2 Note 7-4
Lamp Voltage		V_L	486	540	594	Vrms	Note 7-2
Starting Voltage (25℃) (Reference Value)		Vs	-	-	1080	Vrms	Note 7-3
Starting Voltage (0℃) (Reference Value)		Vs	-	-	1410		
Lamp Driving Frequency		F_L	50	60	70	KHz	Note 7-5

Note 7-1 : To test the current dissipation of Vcc, using the “color bars” testing pattern shown as below



Idd current dissipation testing pattern

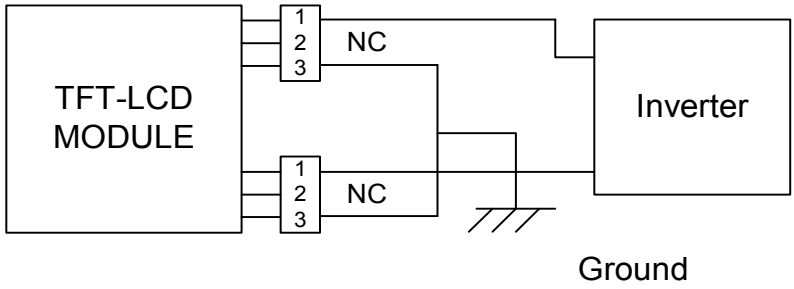
1. White
2. Yellow
3. Cyan
4. Green
5. Magenta
6. Red
7. Blue
8. Black

Note 7-2 : In order to satisfy the quality of B/L, no matter use what kind of inverter, the output lamp current must between Min. and Max. to avoid the abnormal display image caused by B/L.

Note 7-3 : The" Max of starting voltage " means the minimum voltage of inverter to turn on the CCFL. and it should be applied to the lamp for more than 1 second to start up. Otherwise the lamp may not be turned on.
PVI strongly recommend that the minimum voltage of inverter could be designed for 0°C condition.

Note 7-4 : Lamp current is measured with current meter for high frequency as shown below

Lamp current dissipation testing configuration



Note1:Pin 1 is high voltage,Pin 2 NC, Pin 3 ground.
Note2:One Lamp Current is 6mA.Two Lamp 12mA.

Note 7-5: The waveform of lamp driving voltage should be as closed to a perfect sine wave as possible.

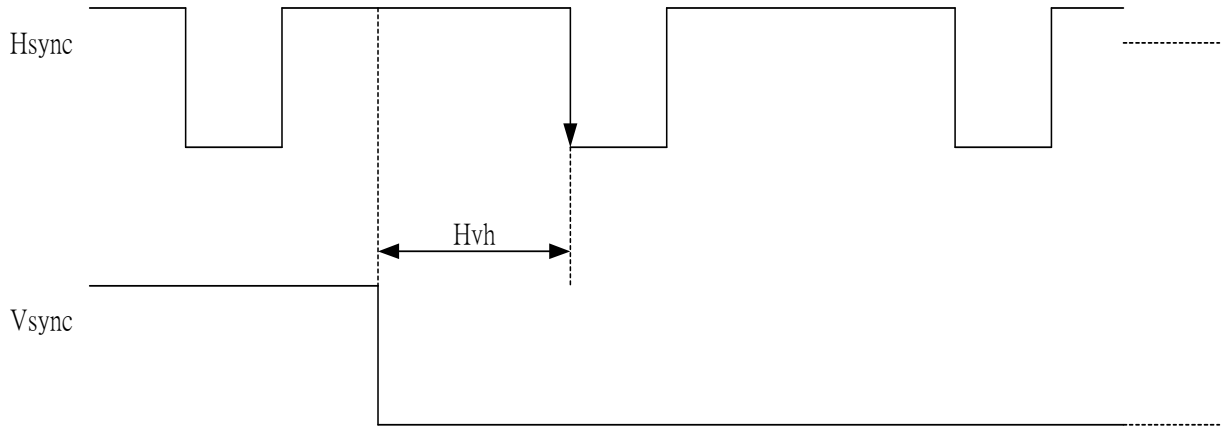
7-2) Input / Output signal timing chart

Parameters		Symbol	Min.	Typ.	Max.	Unit	Note
CLK	Frequency	Fc=1/tc		25.175		MHz	Note 7-5
		tc		40		ns	
Hsync	Period	Hp		31.778		us	
				800		tc	
	Display period	Hdp		640		tc	
	Pulse width	Hpw	12	96	139	tc	
	Back-porch	Hbp	12	48	139	tc	
	Front-porch	Hfp		16		tc	
	Hpw+Hbp		136	144	151	tc	
	Hsync-CLK	Hhc	10		Tc-10	ns	
	Vsync-Hsync	Hvh	0	0	200	tc	
Vsync	Period	Vp		16.8		ms	
			515	525	800	Hp	
	Display period	Vdp		480		Hp	
	Pulse width	Vpw	2	2	35	Hp	
	Back-porch	Vbp	2	33	35	Hp	
	Front-porch	Vfp	1	10		Hp	
	Vpw+Vbp		31	35	38	Hp	
Data	CLK-DATA	Dcd	10			ns	
	DATA-CLK	Ddc	10			ns	
DENB	Horizontal scanning period	T1	780	800	900	tc	
	Horizontal display period	T2		640		tc	
	Vertical display period	T3		480		T1	
	Frame cycling period	T4	515	525	800	T1	

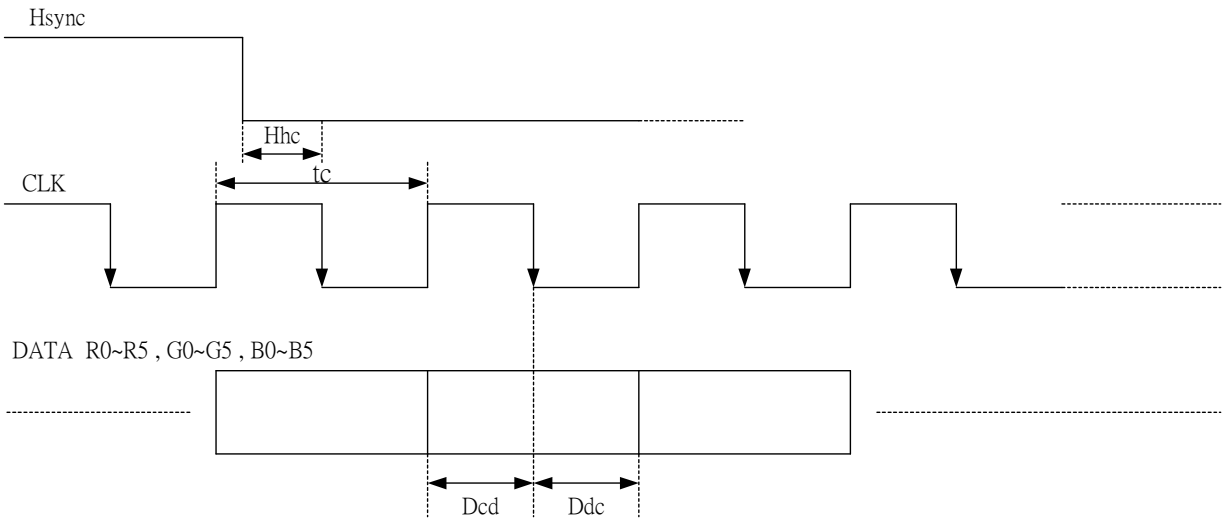
Note 7-5 : Tc is the period of sampling clock. In case of low-frequency , the image-flicker may occur.

7-3) Display Time Range

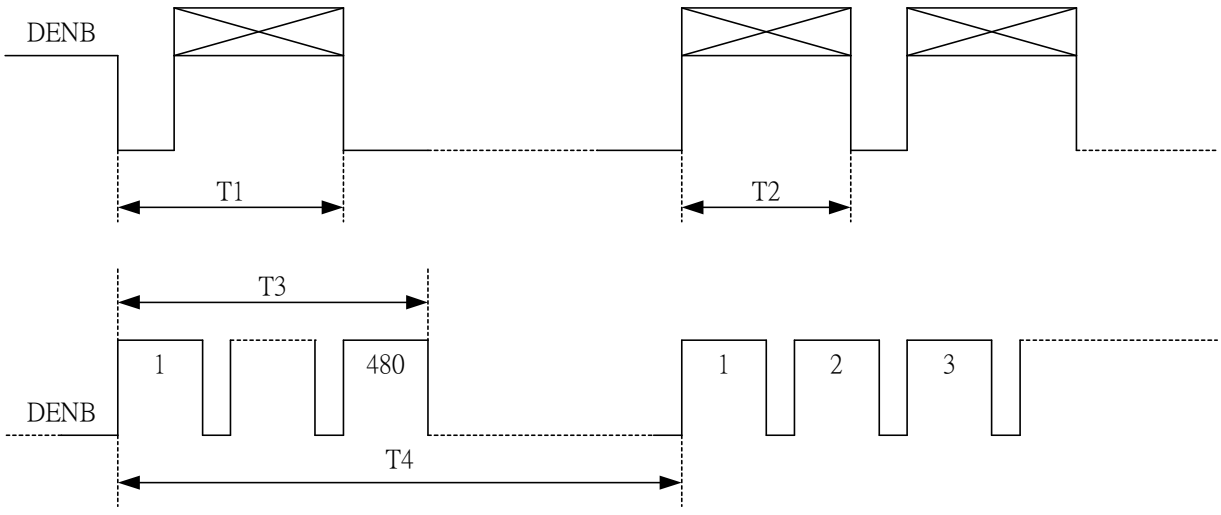
(1) Vertical Timing :



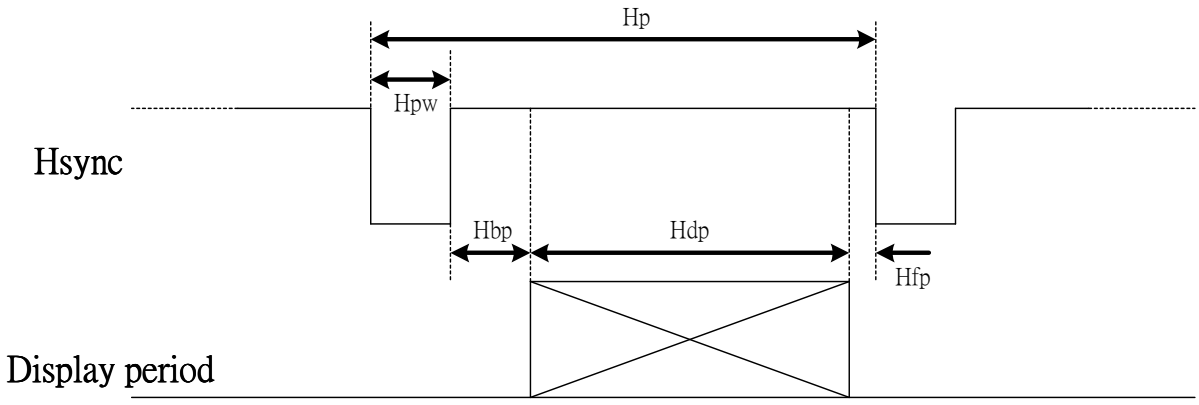
(2) Horizontal Timing :



(3) DENB Timing :

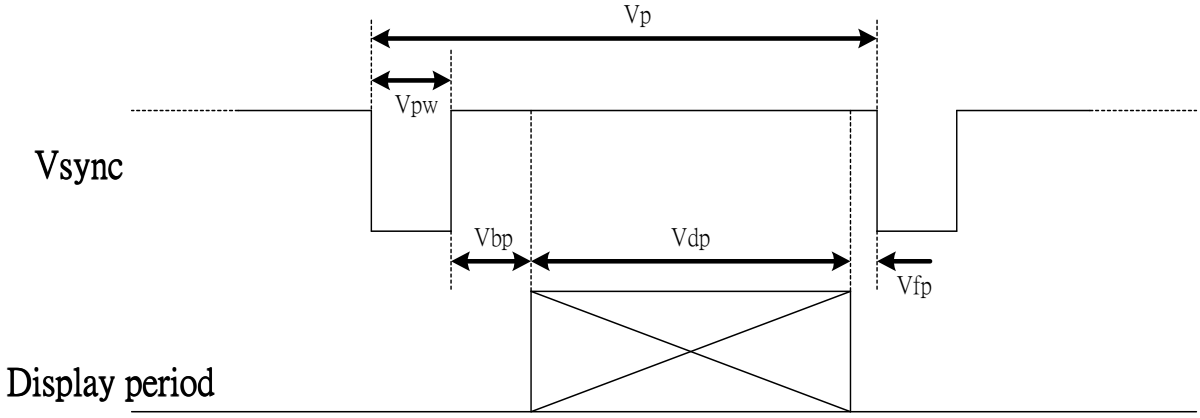


(4) Detail of Horizontal Timing :



Item	Description	Clock Cycles	Time
Hpw	Horizontal Width	96	3.813 μs
Hbp	Horizontal B-Porch	48	1.907 μs
Hdp	Horizontal Display	640	25.422 μs
Hfp	Horizontal F-Porch	16	0.636 μs
Hp	Horizontal Total	800	31.778 μs

(5) Detail of Vertical Timing :

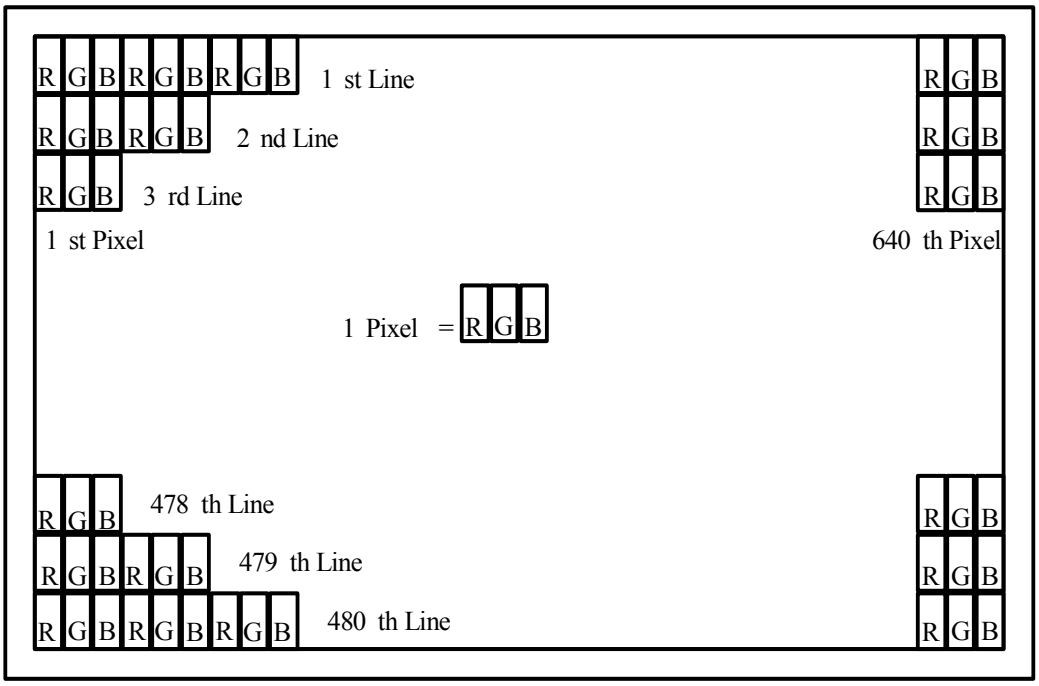


Item	Description	Horizontal Lines	Time
Vpw	Vertical Width	2	63.5 μs
Vbp	Vertical B-Porch	33	1.049 ms
Vdp	Vertical Display	480	15.253 ms
Vfp	Vertical F-Porch	10	317.8 μs
Vp	Vertical Total	525	16.683 ms

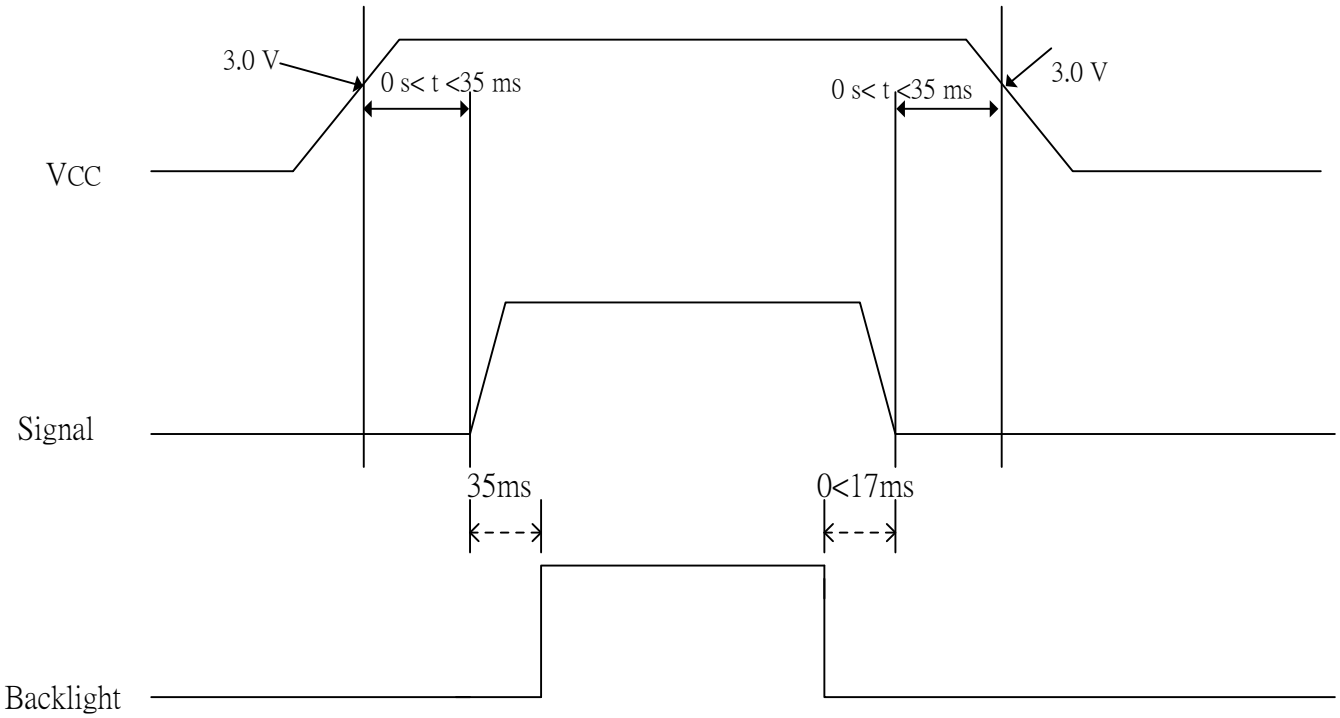
7-5) Display Color and Gray Scale Reference

Color		Input Color Data																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker																		
	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓
	Brighter																		
	Red (61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker																		
	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓
	Brighter																		
	Green (61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Darker																		
	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓
	Brighter																		
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

8. Pixel Arrangement



9. Power On Sequence



1. The supply voltage for input signals should be same as $V_{CC.}$
2. When the power is off , please keep whole signals (Hsync, Vsync, CLK, Data) low level or high impedance

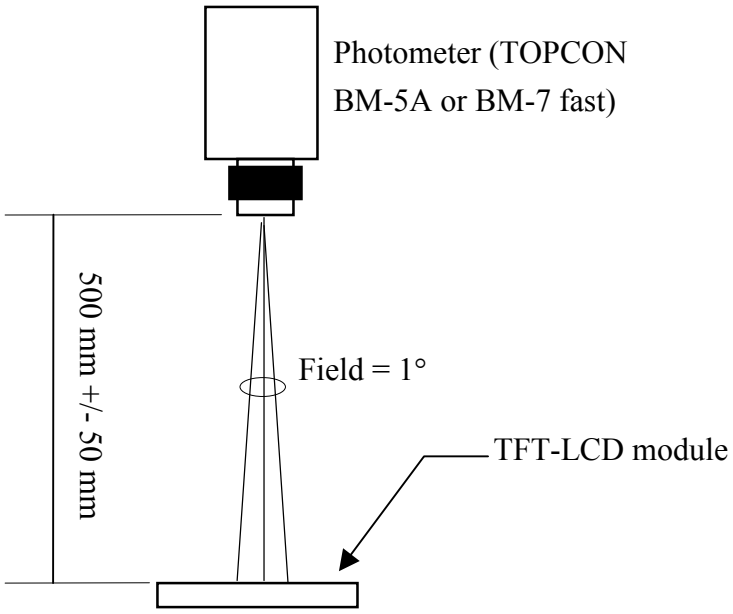
10. Optical Characteristics

10-1) Specification:

Ta=25℃

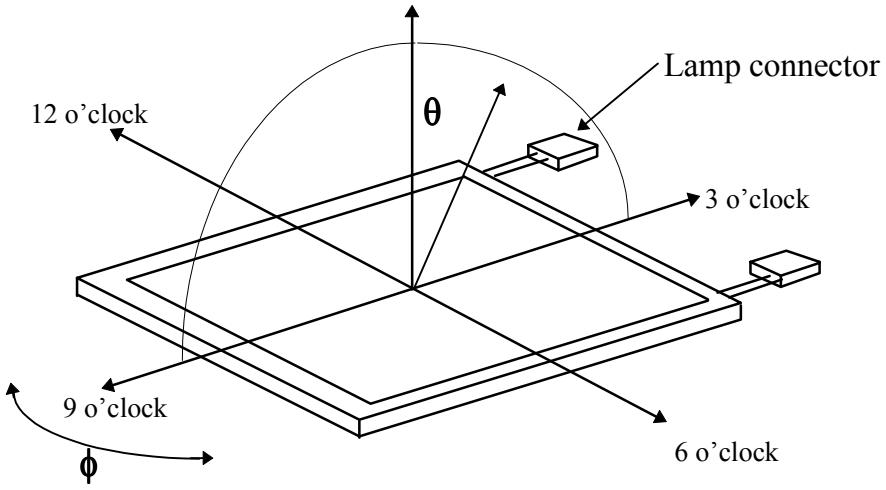
Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks
Viewing Angle	Horizontal	θ	CR＞10	±55	±60		deg	Note 10-1
	Vertical	θ (to 6 o'clock)		50	55	-	deg	
		θ (to 12 o'clock)		35	40	-	deg	
Contrast Ratio		CR		200	400	-	-	Note 10-2
Response time	Rise	Tr	θ =0°	-	15		ms	Note 10-4
	Fall	Tf		-	25		ms	
Brightness			θ =0°/ φ =0	290	330		cd/㎡	Note 10-3
Luminance Uniformity		U		70	80	-	%	Note 10-5
Lamp Life Time				30000	-	-	hrs	
White Chromaticity		x		0.279	0.309	0.339	-	
		y		0.307	0.337	0.367	-	
Cross Talk			θ =0°	-	-	3.5	%	Note10-6

All the optical measurement shall be executed 30 minutes after backlight being turn-on. The optical characteristics shall be measured in dark room (ambient illumination on panel surface less than 1 Lux). The measuring configuration shows as following figure.



Optical characteristics measuring configuration

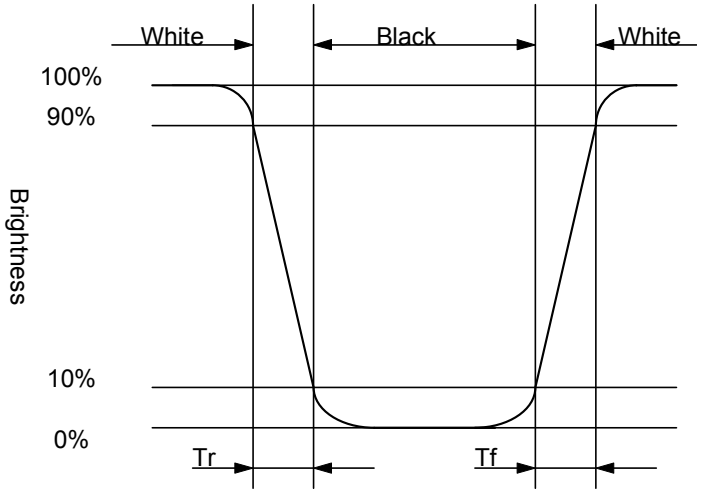
Note 10-1：The definitions of viewing angles are as follow



Note 10-2 : The definition of contrast ratio $CR = \frac{\text{Luminance when Testing point is White}}{\text{Luminance when Testing point is Black}}$

Note 10-3：Topcon BM-7 fast luminance meter 1°field of view is used in the testing (after 30 minutes' operation). The typical luminance value is measured at lamp current 12.0 mA.

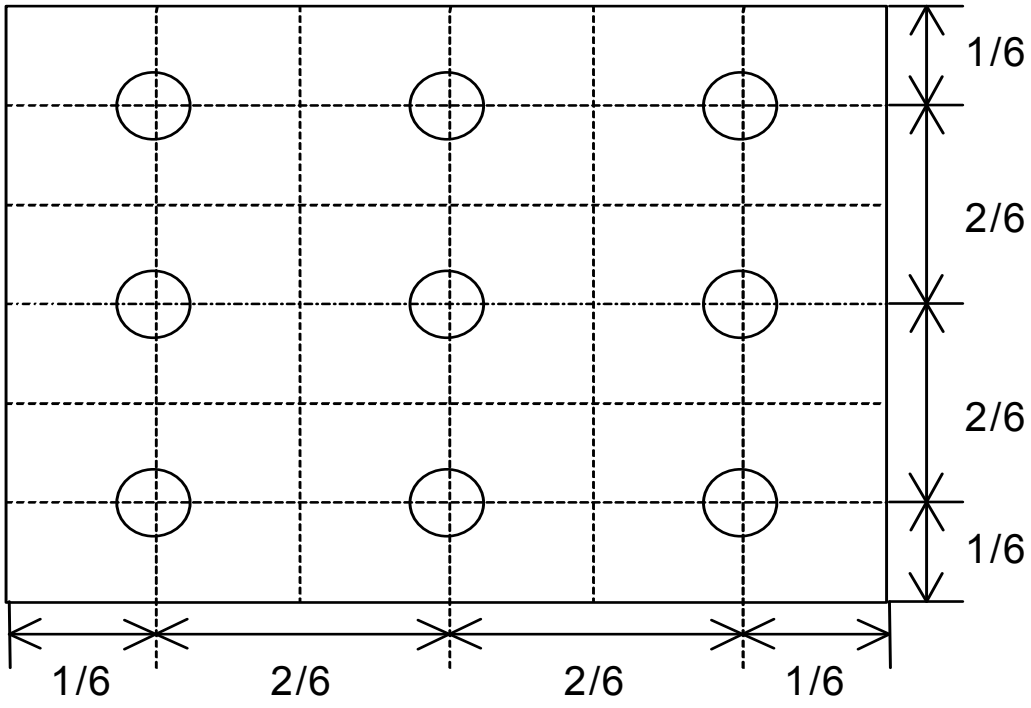
Note 10-4: Definition of Response Time T_r and T_f:



Note 10-5 : The uniformity of LCD is defined as

$$U = \frac{\text{The Minimum Brightness of the 9 testing Points}}{\text{The Maximum Brightness of the 9 testing Points}}$$

Luminance meter : BM-5A or BM-7 fast (TOPCON)
Measurement distance : 500 mm +/- 50 mm
Ambient illumination : < 1 Lux
Measuring direction : Perpendicular to the surface of module
The test pattern is white.

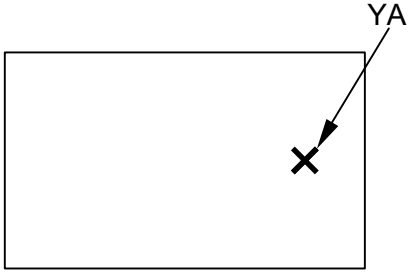


Note 10-6 : Cross Talk (CTK) = $\frac{|YA-YB|}{YA} \times 100\%$

YA : Brightness of Pattern A
YB : Brightness of Pattern B

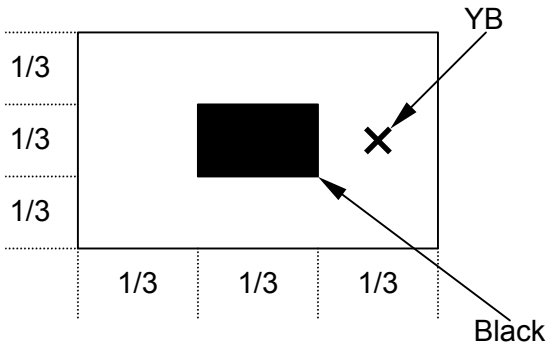
Pattern A

(Gray Level 31)



Pattern B

(Gray Level 31, central black box exclusive)



✕: Testing Point (A and B are at the same point.)

(Gray Level 0)

11. Handling Cautions

11-1) Mounting of module

a) Please power off the module when you connect the input/output connector.

b) Please connect the ground pattern of the inverter circuit surely. If the connection is not perfect, some following problems may happen possibly.

1.The noise from the backlight unit will increase.

2.The output from inverter circuit will be unstable.

3. In some cases a part of module will heat.

c) Polarizer which is made of soft material and susceptible to flaw must be handled carefully.

d) Protective film (Laminator) is applied on surface to protect it against scratches and dirts.

e) Please following the tear off direction as figure11-1 to remove the protective film as slowly as possible, so that electrostatic charge can be minimized.

11-2) Precautions in mounting

a) When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth.

b) Wipe off water drops or finger grease immediately. Long contact with water may cause discoloration or spots.

c) TFT-LCD module uses glass which breaks or cracks easily if dropped or bumped on hard surface. Please handle with care.

d) Since CMOS LSI is used in the module. So take care of static electricity and earth yourself when handling.

11-3) Adjusting module

a) Adjusting volumes on the rear face of the module have been set optimally before shipment.

b) Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described may not be satisfied.

11-4) Others

a) Do not expose the module to direct sunlight or intensive ultraviolet rays for many hours.

b) Store the module at a room temperature place.

c) The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump read lead line around.

d) If LCD panel breaks, it is possibly that the liquid crystal escapes from the panel. Avoid putting it into eyes or mouth. When liquid crystal sticks on hands, clothes or feet. Wash it out immediately with soap.

e) Observe all other precautionary requirements in handling general electronic components.

f) Please adjust the voltage of common electrode as material of attachment by 1 module.

11-5) Polarizer mark

The polarizer mark is to describe the direction of wide view angle film how to mach up with the rubbing direction.

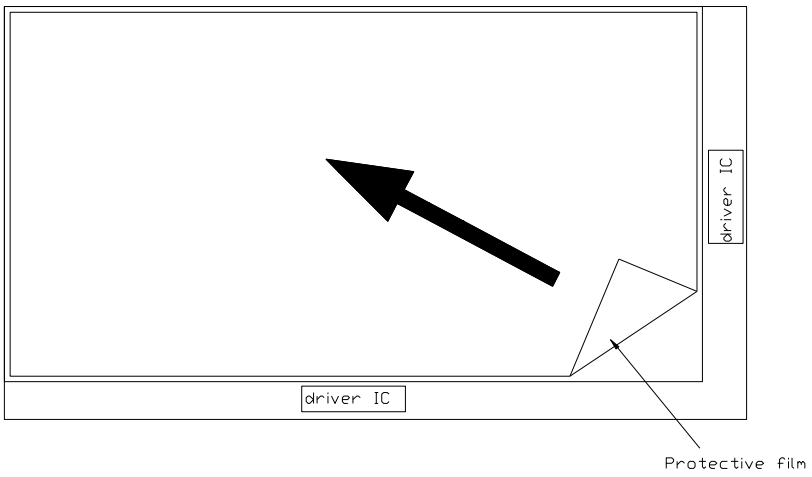


Figure 11-1 the way to peel off protective film

12. Reliability Test

No	Test Item	Test Condition	Remark
1	High Temperature Storage Test	Ta = +70℃, 240 hrs	
2	Low Temperature Storage Test	Ta = -20℃, 240 hrs	
3	High Temperature Operation Test	Ta = +60℃, 240 hrs	
4	Low Temperature Operation Test	Ta = -10℃, 240 hrs	
5	High Temperature & High Humidity Operation Test	Ta = +60℃, 90%RH, 240 hrs (No Condensation)	
6	Thermal Cycling Test (non-operating)	0℃ → +60℃, 50 Cycles 1Hr 1Hr	
7	Vibration Test (non-operating)	Frequency：10 ~ 57 H_Z, Amplitude：0.5 mm 58~500Hz, 1G Sweep time: 11 min Test Period: 3 hrs (1 hr for each direction of X, Y, Z)	
8	Shock Test (non-operating)	80G, 6ms, X,Y, Z 1 times for each direction	
9	Electrostatic Discharge Test (non-operating)	150pF, 330Ω Air: ±15KV; Contact: ±8KV 10 times/point, 9 points/panel face	

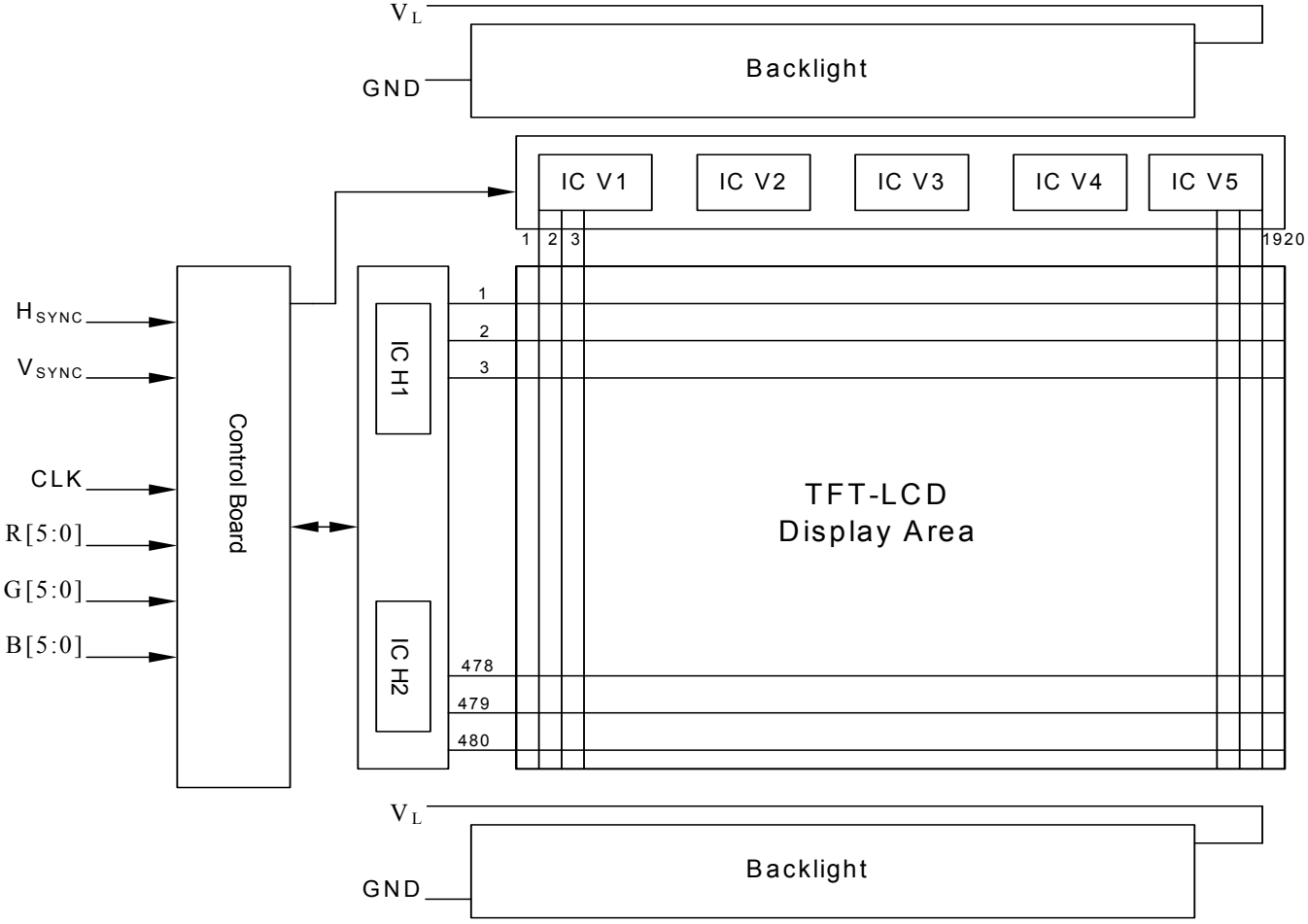
Ta: ambient temperature

Note 12-1 : The protective film must be removed before temperature test.

[Criteria]

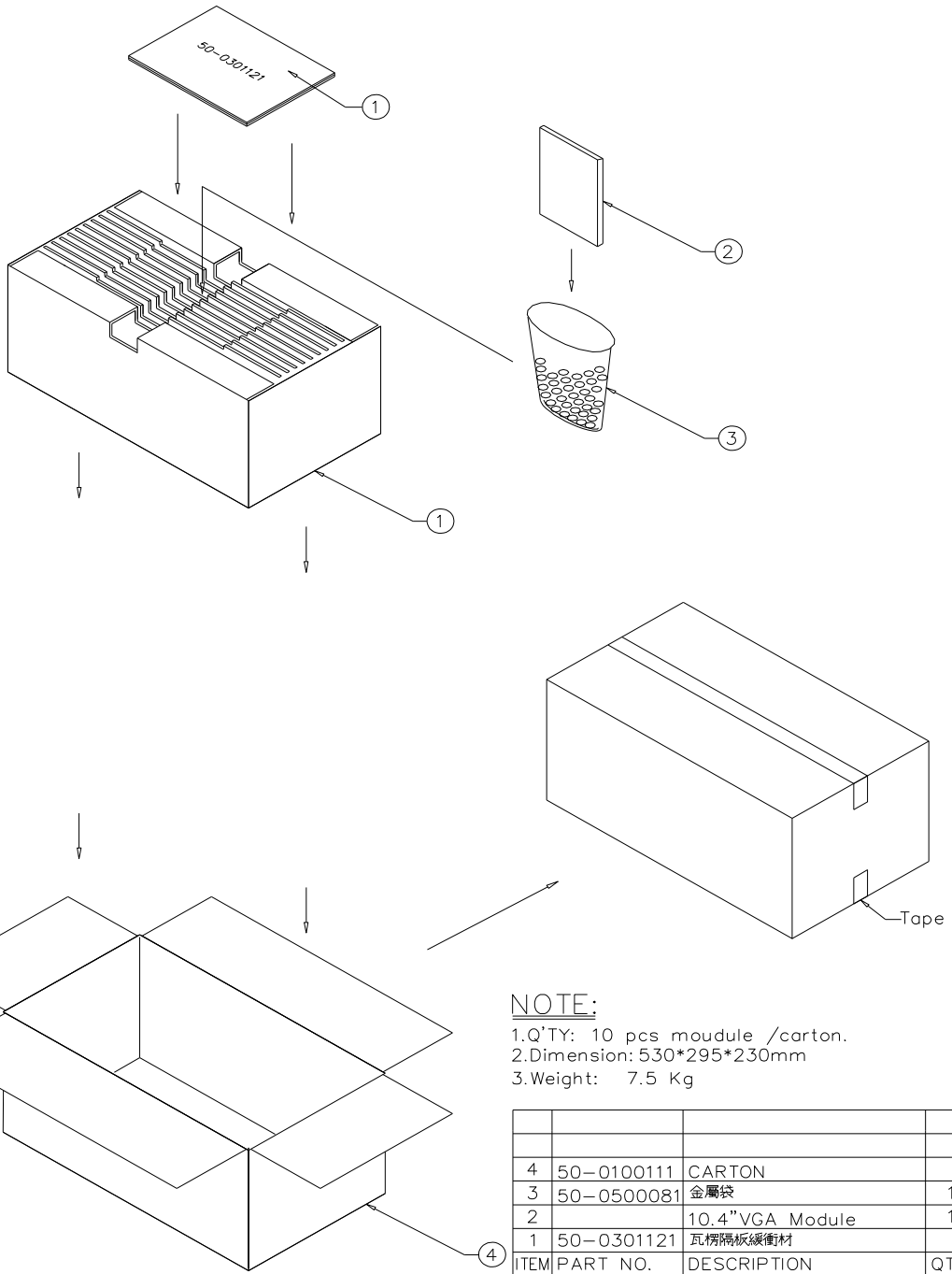
1. Main LCD should normally work under the normally condition no defect of function, screen quality and appearance (including : line defect ,no image)

13. Block Diagram



14. Packing Diagram

ZONE	REV.	DOCUUMENT NO.	DESCRIPTION	DATE	REV.BY



NOTE:

1.Q'TY: 10 pcs moudule /carton.
2.Dimension: 530*295*230mm
3.Weight: 7.5 Kg

ITEM	PART NO.	DESCRIPTION	QTY	REMARK
4	50-0100111	CARTON	1	
3	50-0500081	金屬袋	10	抗靜電
2		10.4"VGA Module	10	
1	50-0301121	瓦楞隔板緩衝材	1	上蓋+ 底座

MTL.SPEC.	UNSPECIFIED TOL'S ANGLE ROUGHNESS	REMARK	元太科技工業股份有限公司 Prime View International Co., Ltd.

			SCALE	UNIT	SHEET	DWG.TITLE
APPROVE	Frank Shin	'04.09.22			1 OF 1	PD104VT1/VT3 Packing Drawing
CHECK	Frank Shin	'04.09.22				
DRAWN	Patrick Lin	'04.09.22	MTL.NO.		DWG FILE:	REV. 02 / A4 SIZE